



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = Not Run

ProLiant DL380 G5
(3.16 GHz, Intel Xeon processor X5460)

SPECint_rate_base2006 = 113

CPU2006 license: 3

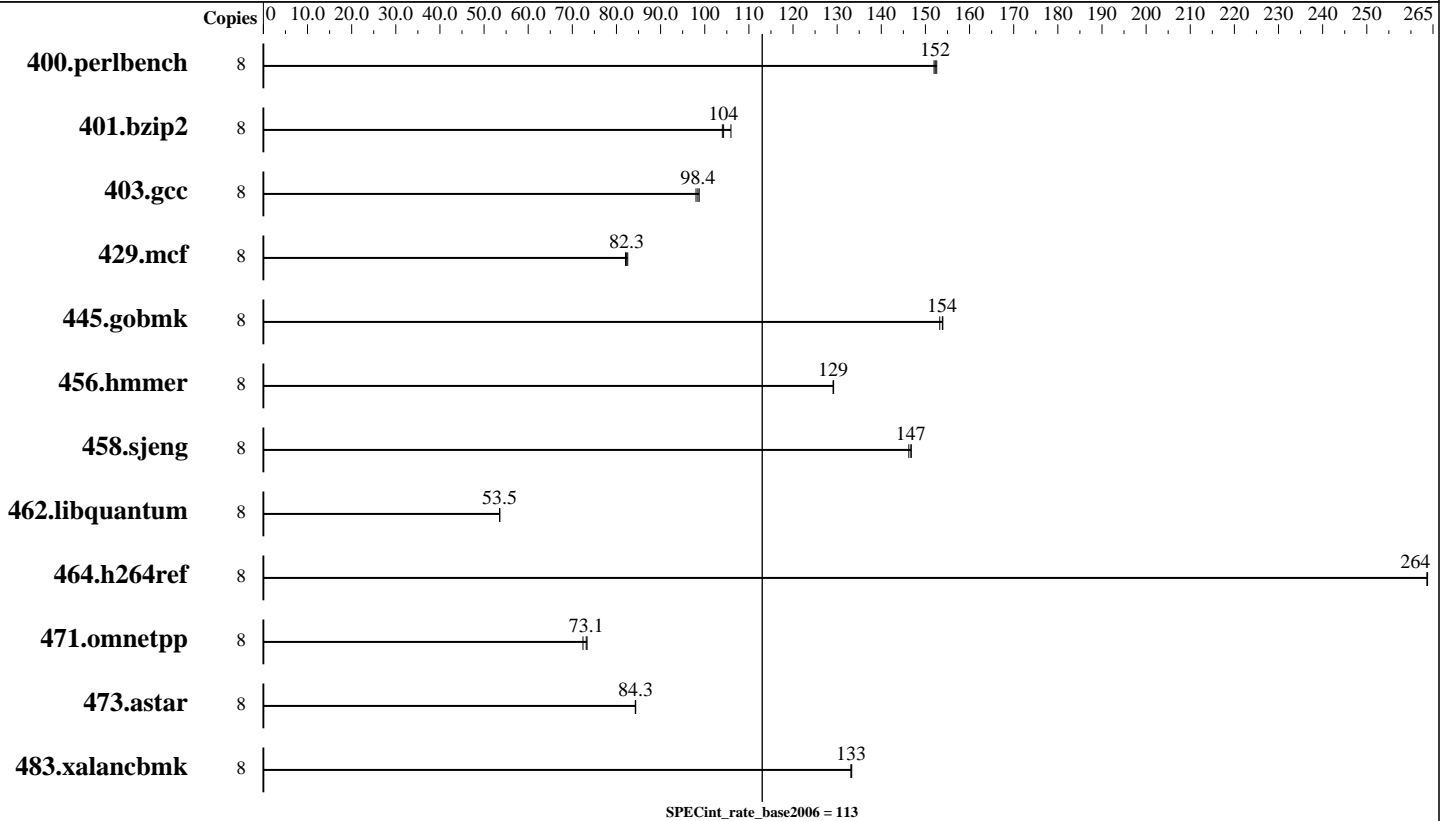
Test date: Jan-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5460
 CPU Characteristics: 3.16 GHz, 2x6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3166
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300F CL5)
 Disk Subsystem: 1x72 GB 15 K SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1
 Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for applications running on IA-32 and Intel 64, Version 10.1
 Build 20070913 Package ID: l_cc_p_10.1.008
 Auto Parallel: No
 File System: ext2
 System State: Multi-user run level 3
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap Library 8.1
 binutils-2.17.50



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<u>513</u>	<u>152</u>	514	152	512	153							
401.bzip2	8	<u>740</u>	<u>104</u>	742	104	729	106							
403.gcc	8	652	98.7	<u>654</u>	<u>98.4</u>	657	98.0							
429.mcf	8	884	82.6	<u>886</u>	<u>82.3</u>	889	82.0							
445.gobmk	8	545	154	<u>546</u>	<u>154</u>	548	153							
456.hammer	8	578	129	578	129	<u>578</u>	<u>129</u>							
458.sjeng	8	662	146	659	147	<u>660</u>	<u>147</u>							
462.libquantum	8	3097	53.5	<u>3096</u>	<u>53.5</u>	3096	53.5							
464.h264ref	8	<u>671</u>	<u>264</u>	671	264	671	264							
471.omnetpp	8	691	72.4	682	73.3	<u>684</u>	<u>73.1</u>							
473.astar	8	<u>666</u>	<u>84.3</u>	666	84.3	666	84.3							
483.xalanbmk	8	414	133	<u>415</u>	<u>133</u>	415	133							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:

Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled
Hardware Prefetch Disabled

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/compiler/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-int-flags.20090714.01.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:03:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 January 2008.