



# SPEC® CINT2006 Result

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## Acer Incorporated

### SPECint®\_rate2006 = 52.3

## Acer Altos R520 (Intel Xeon 5130)

### SPECint\_rate\_base2006 = 45.7

CPU2006 license: 97

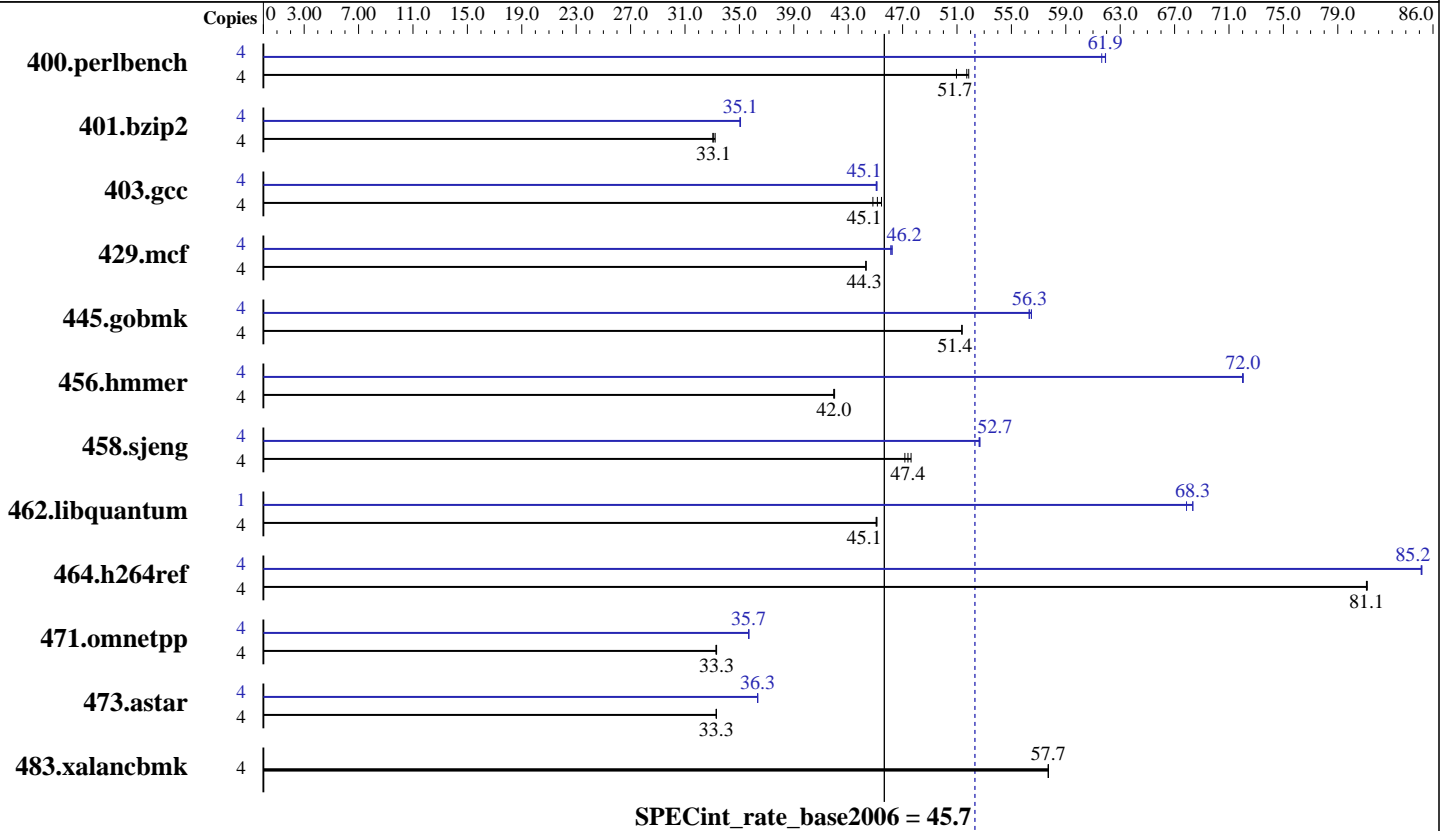
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2007

Hardware Availability: Nov-2006

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon 5130  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)  
 Disk Subsystem: 1 x 73GB 10000PRM SAS HDD  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 SP1 (x86\_64)  
 Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux  
 Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	767	51.0	<b>756</b>	<b>51.7</b>	754	51.9	4	634	61.6	<b>631</b>	<b>61.9</b>	631	61.9
401.bzip2	4	<b>1166</b>	<b>33.1</b>	1162	33.2	1168	33.0	4	1100	35.1	1102	35.0	<b>1101</b>	<b>35.1</b>
403.gcc	4	<b>713</b>	<b>45.1</b>	718	44.8	709	45.4	4	715	45.1	714	45.1	<b>714</b>	<b>45.1</b>
429.mcf	4	823	44.3	<b>823</b>	<b>44.3</b>	823	44.3	4	<b>790</b>	<b>46.2</b>	791	46.1	789	46.2
445.gobmk	4	<b>817</b>	<b>51.4</b>	817	51.4	817	51.4	4	745	56.3	<b>745</b>	<b>56.3</b>	743	56.5
456.hmmer	4	<b>890</b>	<b>42.0</b>	890	41.9	889	42.0	4	<b>518</b>	<b>72.0</b>	518	72.0	518	72.0
458.sjeng	4	1016	47.6	<b>1021</b>	<b>47.4</b>	1026	47.2	4	<b>919</b>	<b>52.7</b>	919	52.7	920	52.6
462.libquantum	4	<b>1838</b>	<b>45.1</b>	1840	45.1	1838	45.1	1	<b>303</b>	<b>68.3</b>	305	67.9	303	68.3
464.h264ref	4	<b>1091</b>	<b>81.1</b>	1091	81.1	1091	81.2	4	<b>1039</b>	<b>85.2</b>	1039	85.2	1040	85.1
471.omnetpp	4	<b>750</b>	<b>33.3</b>	751	33.3	750	33.3	4	700	35.7	701	35.7	<b>701</b>	<b>35.7</b>
473.astar	4	843	33.3	<b>843</b>	<b>33.3</b>	844	33.3	4	<b>773</b>	<b>36.3</b>	773	36.3	773	36.3
483.xalancbmk	4	<b>478</b>	<b>57.7</b>	478	57.7	478	57.7	4	<b>478</b>	<b>57.7</b>	478	57.7	478	57.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result was measured on the Acer Altos R520.  
The Altos R720 and Altos R520 are electronically equivalent.

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

BIOS Settings

Hardware Prefetcher set to ON

Adjacent Cache Line Prefetch set to OFF

All results used taskset to bind processes to cores.  
"ulimit -s unlimited" was set for stacksize unlimited  
OMP\_NUM\_THREADS set to number of cores

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc  
-L/opt/intel/cce/10.1.008/lib  
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.08.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.08.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 16:10:22 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 23 January 2008.