SPEC® CINT2006 Result
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Acer Incorporated

Acer Altos R520 (Intel Xeon 5120)

SPECint®_rate2006 = 44.1

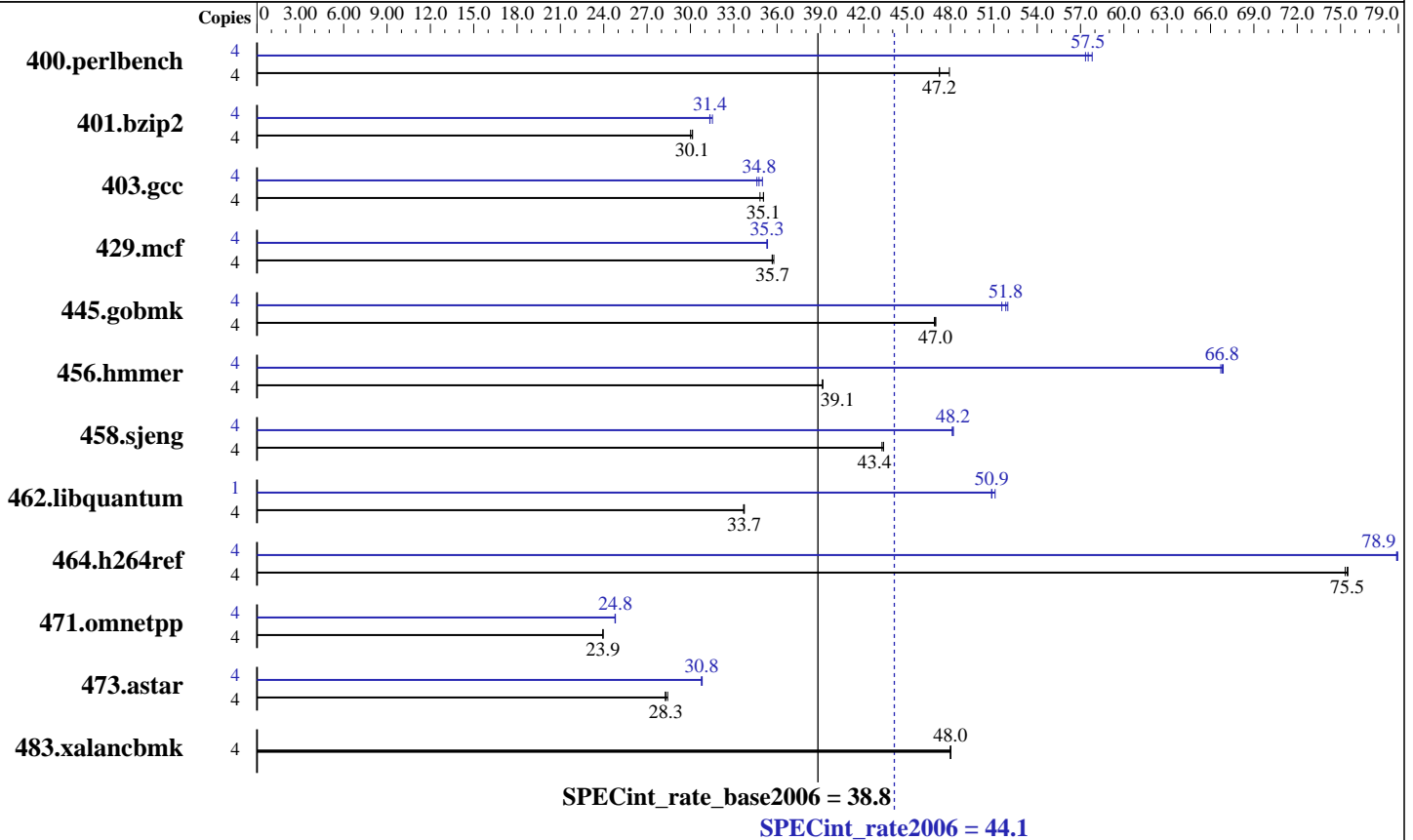SPECint_rate_base2006 = 38.8

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Dec-2007
Hardware Availability: Nov-2006
Software Availability: Nov-2007

Copies 0 3.00 6.00 9.00 12.0 15.0 18.0 21.0 24.0 27.0 30.0 33.0 36.0 39.0 42.0 45.0 48.0 51.0 54.0 57.0 60.0 63.0 66.0 69.0 72.0 75.0 79.0

Benchmark	Copies	Value
400.perlbench	4	57.5
	4	47.2
401.bzip2	4	31.4
	4	30.1
403.gcc	4	34.8
	4	35.1
429.mcf	4	35.3
	4	35.7
445.gobmk	4	51.8
	4	47.0
456.hmmer	4	66.8
	4	39.1
458.sjeng	4	48.2
	4	43.4
462.libquantum	1	50.9
	4	33.7
464.h264ref	4	78.9
	4	75.5
471.omnetpp	4	24.8
	4	23.9
473.astar	4	30.8
	4	28.3
483.xalancbmk	4	48.0

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Hardware

CPU Name: Intel Xeon 5120
CPU Characteristics: 1066MHz system bus
CPU MHz: 1860
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
Disk Subsystem: 1 x 73GB 10000PRM SAS HDD
Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 SP1 (x86_64) Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**827**	**47.2**	815	47.9	827	47.2	4	676	57.8	681	57.3	**679**	**57.5**
401.bzip2	4	1280	30.2	1286	30.0	**1282**	**30.1**	4	1232	31.3	**1231**	**31.4**	1225	31.5
403.gcc	4	918	35.1	**919**	**35.1**	925	34.8	4	**927**	**34.8**	931	34.6	920	35.0
429.mcf	4	1020	35.8	**1023**	**35.7**	1023	35.7	4	**1033**	**35.3**	1032	35.3	1033	35.3
445.gobmk	4	893	47.0	895	46.9	**893**	**47.0**	4	**810**	**51.8**	807	52.0	814	51.6
456.hmmer	4	**953**	**39.1**	954	39.1	953	39.2	4	559	66.7	558	66.9	**559**	**66.8**
458.sjeng	4	1116	43.4	**1116**	**43.4**	1119	43.3	4	**1004**	**48.2**	1004	48.2	1006	48.1
462.libquantum	4	2461	33.7	**2458**	**33.7**	2458	33.7	1	406	51.1	**407**	**50.9**	408	50.8
464.h264ref	4	1172	75.5	**1173**	**75.5**	1175	75.3	4	1121	79.0	1122	78.9	**1122**	**78.9**
471.omnetpp	4	1044	23.9	**1044**	**23.9**	1044	23.9	4	1009	24.8	1008	24.8	**1008**	**24.8**
473.astar	4	994	28.3	988	28.4	**993**	**28.3**	4	911	30.8	**912**	**30.8**	913	30.8
483.xalancbmk	4	575	48.0	575	48.0	**575**	**48.0**	4	575	48.0	575	48.0	**575**	**48.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
    This result was measured on the Acer Altos R520.
    The Altos R720 and Altos R520 are electronically equivalent.

    All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
    for peak, are compiled in 64-bit mode
    BIOS Settings
      Hardware Prefetcher set to ON

      Adjacent Cache Line Prefetch set to OFF

    All results used taskset to bind processes to cores.
    "ulimit -s unlimited" was set for stacksize unlimited
    OMP_NUM_THREADS set to number of cores
```

Base Compiler Invocation

C benchmarks:
```
    icc
```

C++ benchmarks:
```
    icpc
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/cce/10.1.008/bin/icc
           -L/opt/intel/cce/10.1.008/lib
           -I/opt/intel/cce/10.1.008/include
```

```
456.hmmer: /opt/intel/cce/10.1.008/bin/icc
           -L/opt/intel/cce/10.1.008/lib
           -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias`
`-prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo`
`-no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch`
`-opt-streaming-stores always -vec-guard-write`
`-opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2`
`-ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=block`
`-Wl,-z,muldefs`
`-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo`
`-no-prec-div -ansi-alias -opt-ra-region-strategy=routine`
`-Wl,-z,muldefs`
`-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.08.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.08.xml`

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