



SPEC[®] CINT2006 Result

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NEC Corporation

Express5800/140Rf-4
(Intel Xeon processor X7350)

SPECint[®]2006 = 24.5

SPECint_base2006 = 21.2

CPU2006 license: 9006

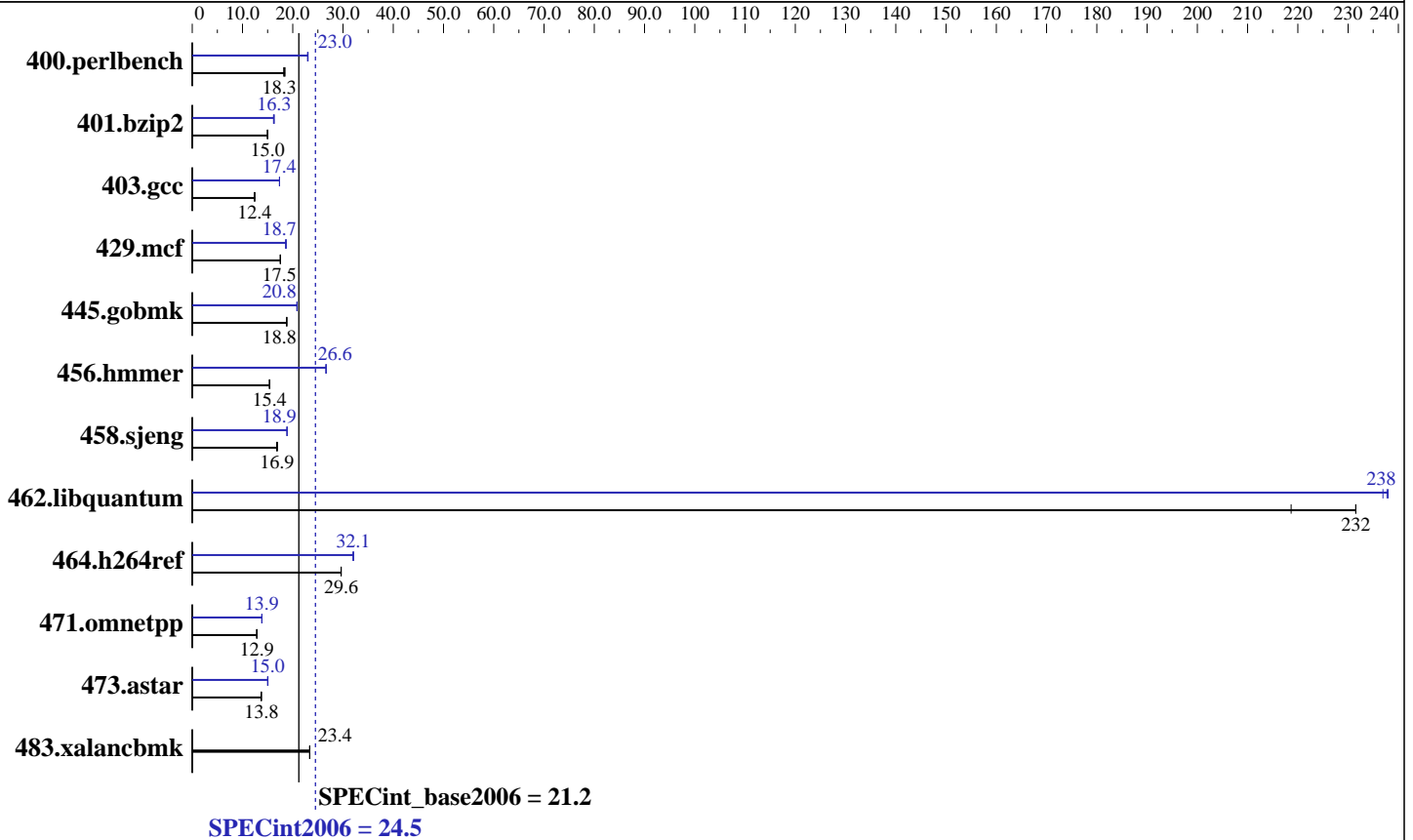
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Dec-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X7350
 CPU Characteristics: 2.93 GHz, 2x4 MB L2 shared, 1066 MHz bus
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 16 cores, 4 chips, 4 cores/chip
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (16x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 10000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 SP1 (x86_64), Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: L_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>533</u>	<u>18.3</u>	537	18.2	529	18.5	426	23.0	424	23.1	<u>425</u>	<u>23.0</u>
401.bzip2	<u>643</u>	<u>15.0</u>	646	14.9	643	15.0	597	16.2	<u>593</u>	<u>16.3</u>	592	16.3
403.gcc	645	12.5	<u>648</u>	<u>12.4</u>	652	12.3	463	17.4	<u>464</u>	<u>17.4</u>	464	17.4
429.mcf	521	17.5	<u>520</u>	<u>17.5</u>	519	17.6	491	18.6	<u>489</u>	<u>18.7</u>	488	18.7
445.gobmk	<u>558</u>	<u>18.8</u>	557	18.8	558	18.8	<u>503</u>	<u>20.8</u>	503	20.9	503	20.8
456.hmmer	607	15.4	607	15.4	<u>607</u>	<u>15.4</u>	<u>350</u>	<u>26.6</u>	351	26.6	350	26.7
458.sjeng	<u>717</u>	<u>16.9</u>	720	16.8	714	17.0	<u>641</u>	<u>18.9</u>	642	18.9	641	18.9
462.libquantum	94.8	219	<u>89.5</u>	<u>232</u>	89.5	232	<u>87.2</u>	<u>238</u>	87.1	238	87.5	237
464.h264ref	746	29.7	<u>747</u>	<u>29.6</u>	747	29.6	689	32.1	<u>690</u>	<u>32.1</u>	692	32.0
471.omnetpp	485	12.9	488	12.8	<u>486</u>	<u>12.9</u>	<u>451</u>	<u>13.9</u>	450	13.9	452	13.8
473.astar	513	13.7	<u>510</u>	<u>13.8</u>	510	13.8	468	15.0	<u>467</u>	<u>15.0</u>	466	15.1
483.xalancbmk	295	23.4	<u>295</u>	<u>23.4</u>	296	23.3	295	23.4	<u>295</u>	<u>23.4</u>	296	23.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Bios settings:
Adjacent Sector Prefetch: Enabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-ia32-intel64-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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