



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 33.7

PowerEdge R200 (Intel Xeon 3065, 2.33 GHz)

SPECint\_rate\_base2006 = 29.2

CPU2006 license: 55

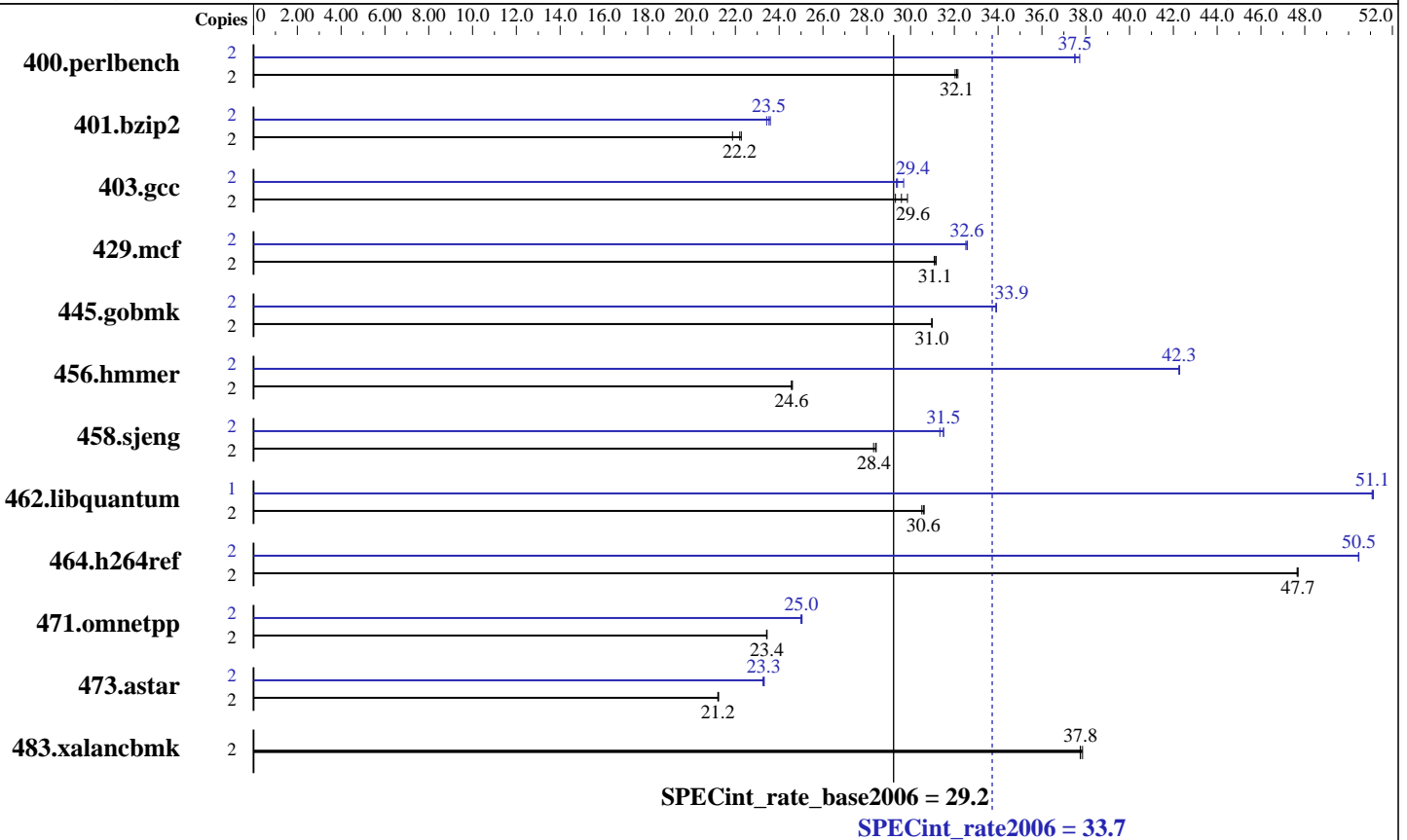
Test date: Nov-2007

Test sponsor: Dell Inc.

Hardware Availability: Nov-2007

Tested by: Dell Inc.

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon 3065  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2333  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (4 x 2GB 800MHz DDR2)  
 Disk Subsystem: 73GB SAS, 10K RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	610	32.0	608	32.2	<b>609</b>	<b>32.1</b>	2	518	37.7	<b>521</b>	<b>37.5</b>	521	37.5
401.bzip2	2	882	21.9	<b>869</b>	<b>22.2</b>	867	22.3	2	818	23.6	<b>820</b>	<b>23.5</b>	824	23.4
403.gcc	2	<b>544</b>	<b>29.6</b>	549	29.3	539	29.9	2	<b>548</b>	<b>29.4</b>	548	29.4	542	29.7
429.mcf	2	585	31.2	<b>586</b>	<b>31.1</b>	587	31.1	2	<b>560</b>	<b>32.6</b>	561	32.5	560	32.6
445.gobmk	2	677	31.0	<b>677</b>	<b>31.0</b>	677	31.0	2	619	33.9	619	33.9	<b>619</b>	<b>33.9</b>
456.hammer	2	<b>760</b>	<b>24.6</b>	758	24.6	760	24.6	2	442	42.3	<b>442</b>	<b>42.3</b>	441	42.3
458.sjeng	2	851	28.4	<b>852</b>	<b>28.4</b>	855	28.3	2	<b>768</b>	<b>31.5</b>	772	31.3	768	31.5
462.libquantum	2	1358	30.5	1353	30.6	<b>1355</b>	<b>30.6</b>	1	405	51.1	<b>405</b>	<b>51.1</b>	406	51.1
464.h264ref	2	<b>928</b>	<b>47.7</b>	929	47.7	928	47.7	2	<b>877</b>	<b>50.5</b>	877	50.5	877	50.4
471.omnetpp	2	534	23.4	533	23.4	<b>533</b>	<b>23.4</b>	2	499	25.0	500	25.0	<b>500</b>	<b>25.0</b>
473.astar	2	<b>662</b>	<b>21.2</b>	662	21.2	661	21.2	2	604	23.3	<b>603</b>	<b>23.3</b>	602	23.3
483.xalancbmk	2	365	37.9	<b>365</b>	<b>37.8</b>	366	37.7	2	365	37.9	<b>365</b>	<b>37.8</b>	366	37.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3  
  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

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## Peak Optimization Flags (Continued)

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-ia32-intel64-linux-flags.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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