



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon processor E5450,  
3.0 GHz

SPECint<sup>®</sup>\_rate2006 = 129

SPECint\_rate\_base2006 = 105

CPU2006 license: 22

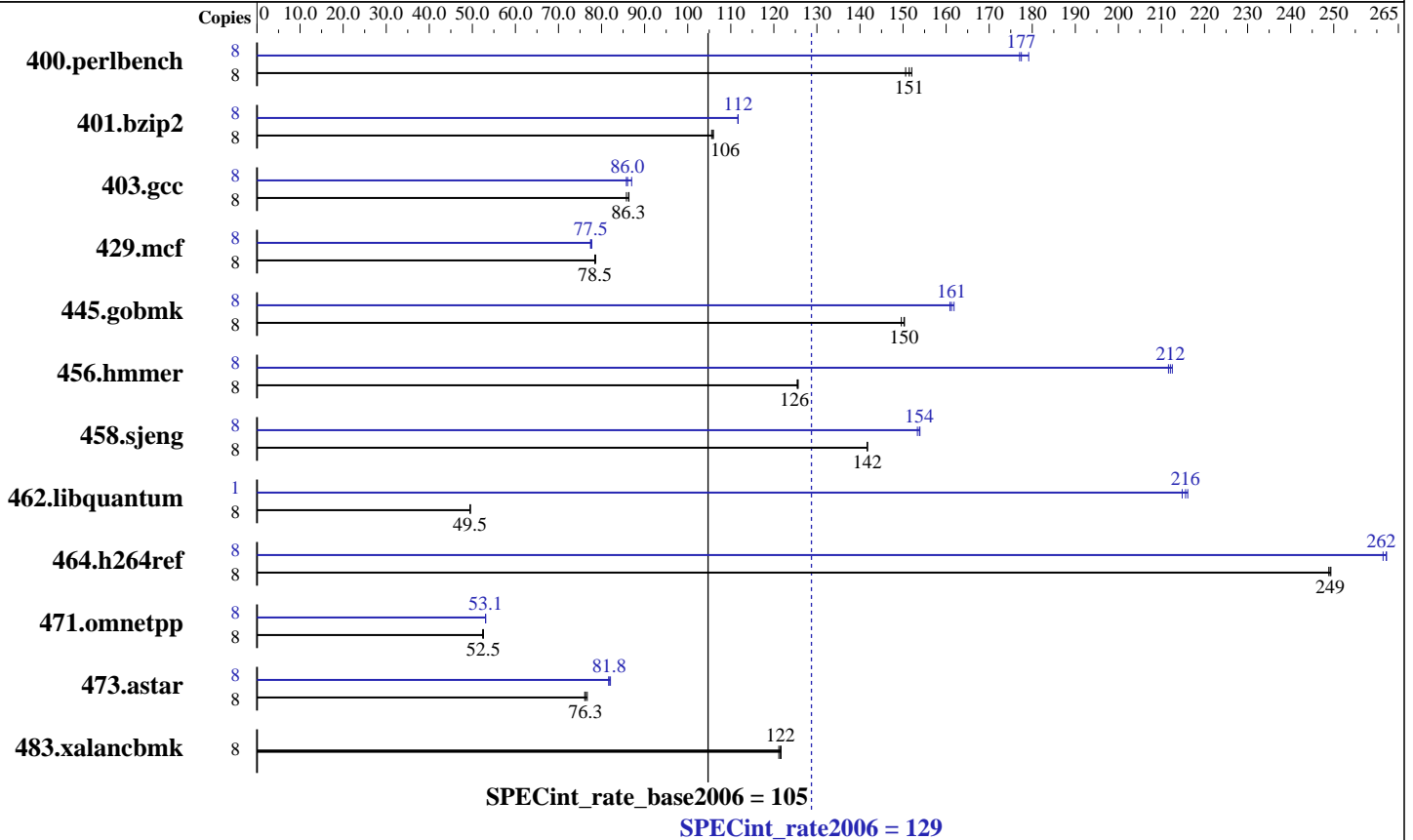
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5450  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725  
 Auto Parallel: Yes  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1  
 binutils-2.17.tar.gz, Version 2.17



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu Siemens Computers

PRIMERGY BX620 S4, Intel Xeon processor E5450,  
3.0 GHz

SPECint\_rate2006 = 129

SPECint\_rate\_base2006 = 105

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007

## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	519	151	<u>516</u>	<u>151</u>	514	152	8	436	179	<u>440</u>	<u>177</u>	441	177
401.bzip2	8	731	106	<u>730</u>	<u>106</u>	728	106	8	<u>691</u>	<u>112</u>	691	112	692	112
403.gcc	8	<b>746</b>	<b>86.3</b>	751	85.8	746	86.4	8	740	87.0	<b>749</b>	<b>86.0</b>	751	85.8
429.mcf	8	930	78.4	<u>930</u>	<u>78.5</u>	928	78.6	8	939	77.7	<u>941</u>	<u>77.5</u>	942	77.4
445.gobmk	8	558	150	561	150	<u>559</u>	<u>150</u>	8	522	161	<u>520</u>	<u>161</u>	519	162
456.hmmmer	8	595	125	594	126	<u>594</u>	<u>126</u>	8	<u>352</u>	<u>212</u>	351	213	353	212
458.sjeng	8	<b>683</b>	<b>142</b>	683	142	683	142	8	631	153	<u>629</u>	<u>154</u>	629	154
462.libquantum	8	<u>3349</u>	<u>49.5</u>	3348	49.5	3349	49.5	1	96.5	215	<u>96.1</u>	<u>216</u>	95.9	216
464.h264ref	8	710	249	<u>711</u>	<u>249</u>	711	249	8	675	262	677	261	<b>675</b>	<b>262</b>
471.omnetpp	8	954	52.4	<u>952</u>	<u>52.5</u>	951	52.5	8	942	53.1	<u>942</u>	<u>53.1</u>	942	53.1
473.astar	8	738	76.1	732	76.7	<b>736</b>	<b>76.3</b>	8	688	81.6	684	82.1	<b>687</b>	<b>81.8</b>
483.xalancbmk	8	454	122	456	121	<u>454</u>	<u>122</u>	8	454	122	456	121	<u>454</u>	<u>122</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:  
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:  
<http://www.fujitsu-siemens.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu Siemens Computers**

PRIMERGY BX620 S4, Intel Xeon processor E5450,  
3.0 GHz

**SPECint\_rate2006 = 129**

**SPECint\_rate\_base2006 = 105**

**CPU2006 license:** 22

**Test sponsor:** Fujitsu Siemens Computers

**Tested by:** Fujitsu Siemens Computers

**Test date:** Nov-2007

**Hardware Availability:** Dec-2007

**Software Availability:** Nov-2007

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu Siemens Computers**

PRIMERGY BX620 S4, Intel Xeon processor E5450,  
3.0 GHz

**SPECint\_rate2006 = 129**

**SPECint\_rate\_base2006 = 105**

**CPU2006 license:** 22

**Test sponsor:** Fujitsu Siemens Computers

**Tested by:** Fujitsu Siemens Computers

**Test date:** Nov-2007

**Hardware Availability:** Dec-2007

**Software Availability:** Nov-2007

## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-alloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmarheap

483.xalanbmk: basepeak = yes



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu Siemens Computers**

PRIMERGY BX620 S4, Intel Xeon processor E5450,  
3.0 GHz

**SPECint\_rate2006 = 129**

**SPECint\_rate\_base2006 = 105**

**CPU2006 license:** 22

**Test sponsor:** Fujitsu Siemens Computers

**Tested by:** Fujitsu Siemens Computers

**Test date:** Nov-2007

**Hardware Availability:** Dec-2007

**Software Availability:** Nov-2007

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 14:30:10 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 27 November 2007.