SPEC® CINT2006 Result

Fujitsu Siemens Computers

PRIMERGY RX300 S4, Intel Xeon processor 5148, 2.33 GHz

SPECint®_rate2006 =　58.2

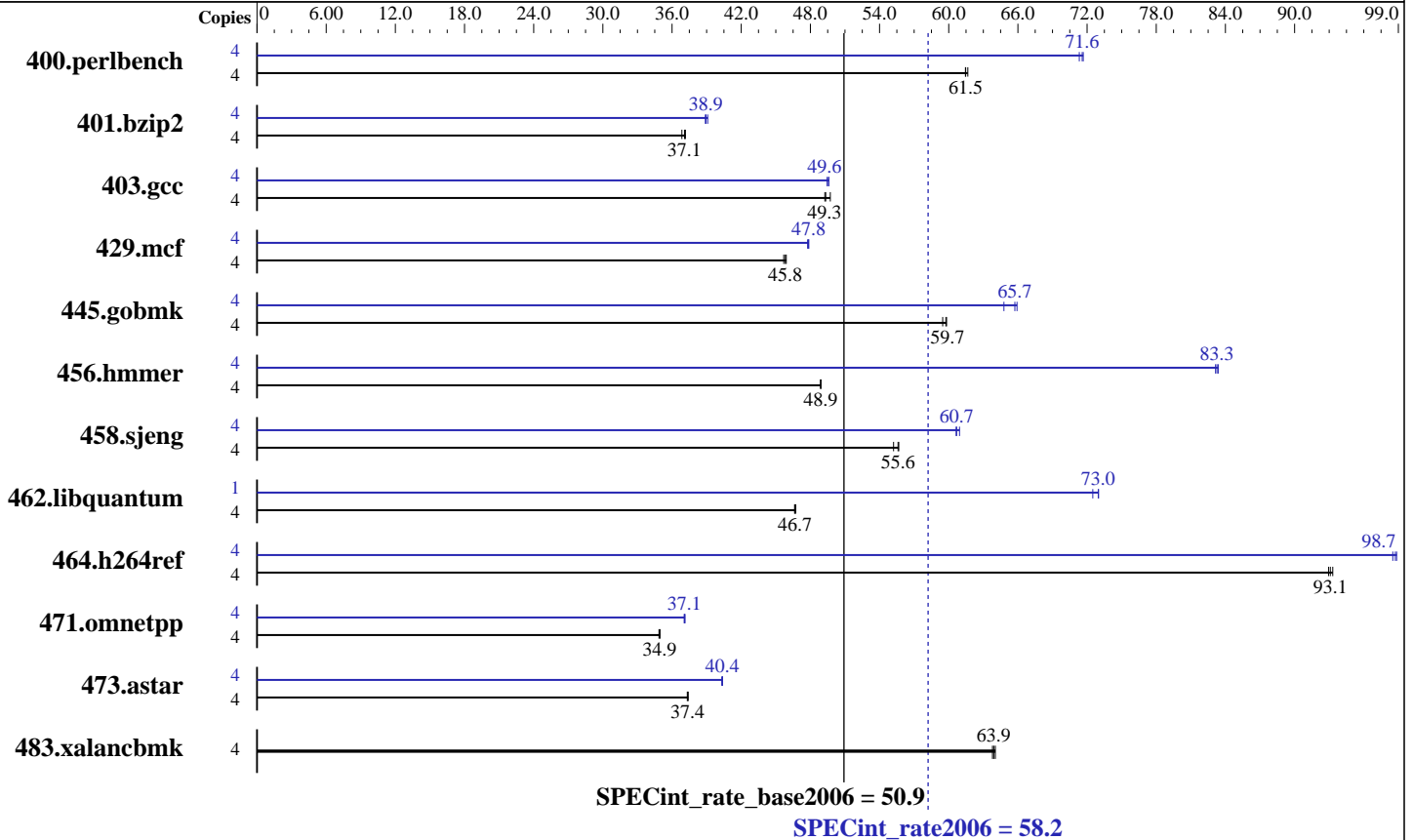SPECint_rate_base2006 = 50.9

CPU2006 license: 22　　　　　　　　　　　　　　　　　　　　　　**Test date:** Nov-2007
Test sponsor: Fujitsu Siemens Computers　　　　　　　　　**Hardware Availability:** Dec-2007
Tested by: Fujitsu Siemens Computers　　　　　　　　　　　**Software Availability:** Nov-2007

Benchmark	Copies	Result
400.perlbench	4	71.6
400.perlbench	4	61.5
401.bzip2	4	38.9
401.bzip2	4	37.1
403.gcc	4	49.6
403.gcc	4	49.3
429.mcf	4	47.8
429.mcf	4	45.8
445.gobmk	4	65.7
445.gobmk	4	59.7
456.hmmer	4	83.3
456.hmmer	4	48.9
458.sjeng	4	60.7
458.sjeng	4	55.6
462.libquantum	1	73.0
462.libquantum	4	46.7
464.h264ref	4	98.7
464.h264ref	4	93.1
471.omnetpp	4	37.1
471.omnetpp	4	34.9
473.astar	4	40.4
473.astar	4	37.4
483.xalancbmk	4	63.9

SPECint_rate_base2006 = 50.9
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Hardware

CPU Name:	Intel Xeon 5148
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel:	Yes
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	636	61.4	**636**	**61.5**	634	61.7	4	548	71.3	545	71.7	**546**	**71.6**
401.bzip2	4	1039	37.2	**1041**	**37.1**	1047	36.9	4	987	39.1	**991**	**38.9**	993	38.9
403.gcc	4	648	49.7	654	49.3	**653**	**49.3**	4	**650**	**49.6**	651	49.4	649	49.6
429.mcf	4	795	45.9	**796**	**45.8**	798	45.7	4	762	47.9	764	47.8	**763**	**47.8**
445.gobmk	4	705	59.5	**702**	**59.7**	702	59.8	4	**638**	**65.7**	636	65.9	648	64.8
456.hmmer	4	763	48.9	**763**	**48.9**	763	48.9	4	448	83.4	**448**	**83.3**	449	83.1
458.sjeng	4	869	55.7	877	55.2	**870**	**55.6**	4	798	60.6	794	60.9	**798**	**60.7**
462.libquantum	4	1773	46.7	1777	46.6	**1776**	**46.7**	1	286	72.5	284	73.0	**284**	**73.0**
464.h264ref	4	**951**	**93.1**	952	92.9	949	93.3	4	899	98.5	895	98.9	**897**	**98.7**
471.omnetpp	4	717	34.9	**716**	**34.9**	715	34.9	4	673	37.1	675	37.1	**674**	**37.1**
473.astar	4	751	37.4	752	37.3	**751**	**37.4**	4	697	40.3	**695**	**40.4**	695	40.4
483.xalancbmk	4	**432**	**63.9**	431	64.0	433	63.8	4	**432**	**63.9**	431	64.0	433	63.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
          -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
          -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
          -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
          -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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