



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 64.3

PowerEdge 2950 (Intel Xeon 5150, 2.66 GHz)

SPECint\_rate\_base2006 = 56.2

CPU2006 license: 55

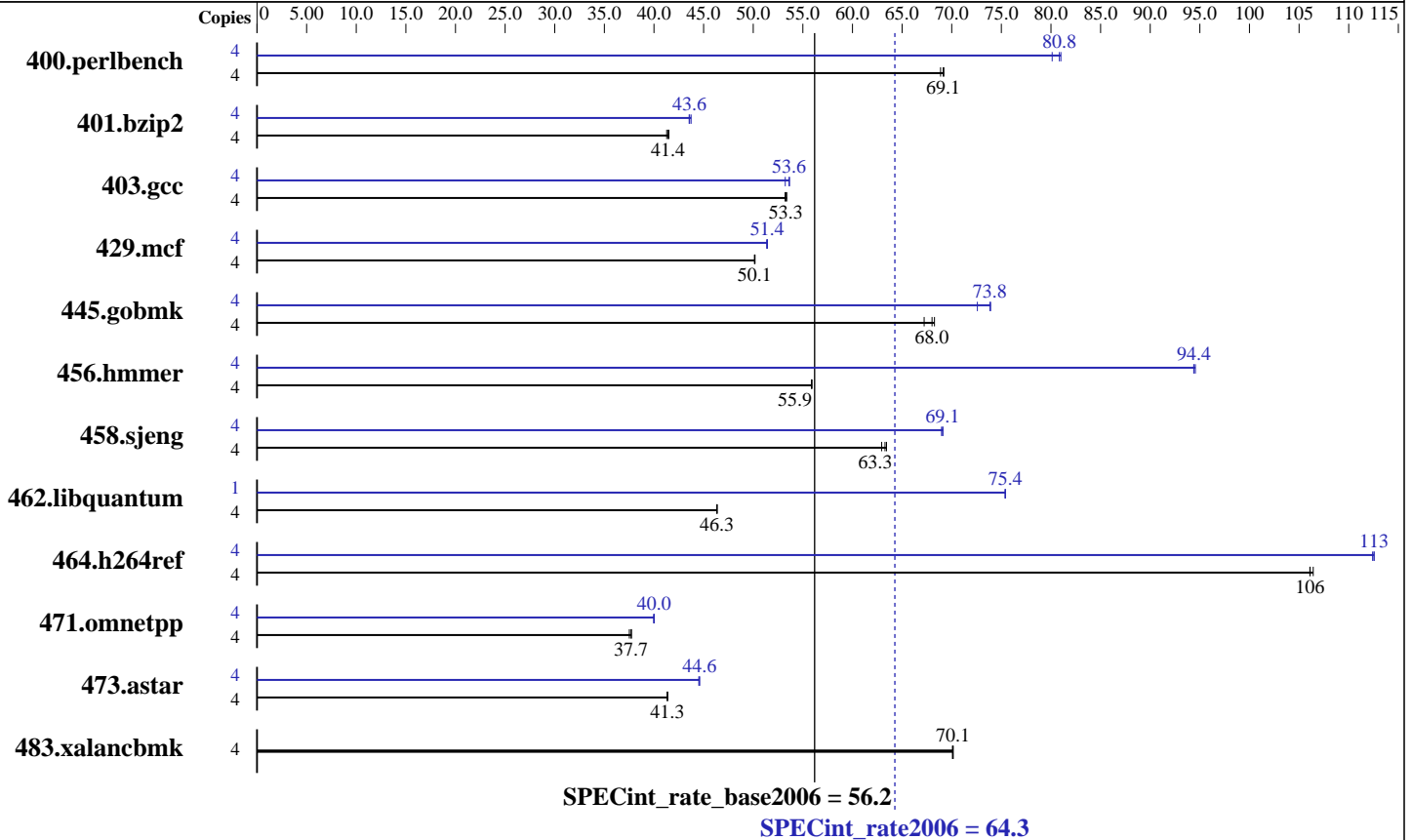
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Oct-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007



## Hardware

CPU Name: Intel Xeon 5150  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB 667 MHz ECC CL5 FB-DIMM)  
 Disk Subsystem: 1 x 73 GB SAS 15k RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	567	68.9	<u>565</u>	<u>69.1</u>	565	69.2	4	488	80.1	482	81.0	<u>483</u>	<u>80.8</u>
401.bzip2	4	930	41.5	<u>932</u>	<u>41.4</u>	935	41.3	4	886	43.5	882	43.7	<u>885</u>	<u>43.6</u>
403.gcc	4	603	53.4	605	53.2	<u>604</u>	<u>53.3</u>	4	605	53.2	600	53.7	<u>601</u>	<u>53.6</u>
429.mcf	4	<u>727</u>	<u>50.1</u>	728	50.1	727	50.2	4	709	51.4	<u>709</u>	<u>51.4</u>	710	51.4
445.gobmk	4	624	67.2	615	68.3	<u>617</u>	<u>68.0</u>	4	568	73.9	<u>568</u>	<u>73.8</u>	578	72.6
456.hammer	4	667	55.9	668	55.9	<u>668</u>	<u>55.9</u>	4	395	94.5	395	94.4	<u>395</u>	<u>94.4</u>
458.sjeng	4	<u>765</u>	<u>63.3</u>	769	62.9	763	63.4	4	<u>701</u>	<u>69.1</u>	702	69.0	700	69.1
462.libquantum	4	1787	46.4	<u>1788</u>	<u>46.3</u>	1789	46.3	1	275	75.4	<u>275</u>	<u>75.4</u>	275	75.4
464.h264ref	4	<u>834</u>	<u>106</u>	834	106	832	106	4	<u>787</u>	<u>113</u>	786	113	788	112
471.omnetpp	4	<u>663</u>	<u>37.7</u>	663	37.7	666	37.5	4	626	40.0	625	40.0	<u>625</u>	<u>40.0</u>
473.astar	4	679	41.4	679	41.3	<u>679</u>	<u>41.3</u>	4	<u>630</u>	<u>44.6</u>	630	44.6	631	44.5
483.xalancbmk	4	393	70.2	394	70.1	<u>394</u>	<u>70.1</u>	4	393	70.2	394	70.1	<u>394</u>	<u>70.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_STACK\_SIZE set to 64M  
KMP\_AFFINITY set to physical,0

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## Platform Notes

BIOS Settings:  
Adjacent Cache Line Prefetch = Disabled (default Enabled)  
Hardware Prefetcher = Enabled (default Enabled)

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.17.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.17.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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