SPEC® CINT2006 Result
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Bull SAS

NovaScale T860
(Intel Xeon processor E5335, 2.00GHz)

SPECint®_rate2006 =   91.3
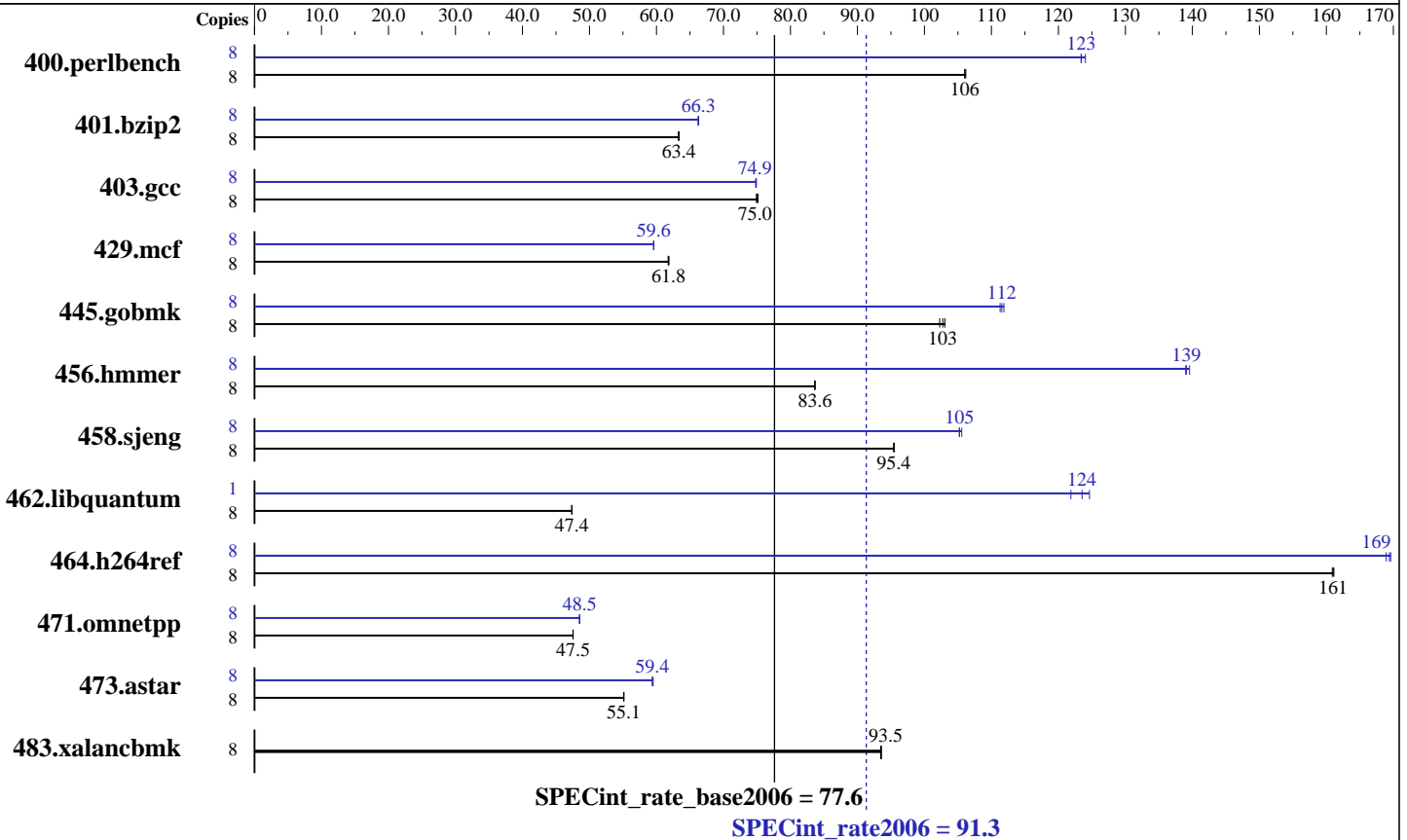
SPECint_rate_base2006 = 77.6

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Oct-2007

Hardware Availability: Mar-2007

Software Availability: Nov-2007

		Copies		
400.perlbench	8		123	
	8		106	
401.bzip2	8	66.3		
	8	63.4		
403.gcc	8	74.9		
	8	75.0		
429.mcf	8	59.6		
	8	61.8		
445.gobmk	8		112	
	8		103	
456.hmmer	8		139	
	8	83.6		
458.sjeng	8		105	
	8	95.4		
462.libquantum	1		124	
	8	47.4		
464.h264ref	8		169	
	8	161		
471.omnetpp	8	48.5		
	8	47.5		
473.astar	8	59.4		
	8	55.1		
483.xalancbmk	8	93.5		

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Hardware

CPU Name: Intel Xeon E5335
CPU Characteristics: 2.00 GHz, 8 MB L2, 1333 MHz system bus
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1 to 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x147 GB SAS, 15000 RPM
Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10
Kernel 2.6.16.21-0.8-smp for x86_64
Compiler: Intel C++ Compiler for Linux32 and Linux64
version 10.1
Build 20070725
Auto Parallel: Yes
File System: ext2
System State: Multi-user run level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap library V8.1
Binutils 2.17.50.0.15

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	737	106	**737**	**106**	736	106	8	630	124	633	123	**633**	**123**
401.bzip2	8	1220	63.3	1218	63.4	**1218**	**63.4**	8	**1165**	**66.3**	1166	66.2	1164	66.3
403.gcc	8	**859**	**75.0**	860	74.9	857	75.2	8	**860**	**74.9**	860	74.9	861	74.8
429.mcf	8	1180	61.8	**1180**	**61.8**	1182	61.7	8	1224	59.6	**1224**	**59.6**	1225	59.6
445.gobmk	8	820	102	**817**	**103**	814	103	8	750	112	754	111	**752**	**112**
456.hmmer	8	893	83.6	**892**	**83.6**	892	83.7	8	535	140	**537**	**139**	537	139
458.sjeng	8	**1014**	**95.4**	1014	95.5	1015	95.4	8	**920**	**105**	920	105	917	106
462.libquantum	8	3497	47.4	**3498**	**47.4**	3502	47.3	1	**168**	**124**	166	125	170	122
464.h264ref	8	1100	161	1099	161	**1100**	**161**	8	1048	169	1044	170	**1045**	**169**
471.omnetpp	8	1052	47.5	**1052**	**47.5**	1051	47.6	8	1030	48.6	**1030**	**48.5**	1031	48.5
473.astar	8	1019	55.1	**1019**	**55.1**	1019	55.1	8	**945**	**59.4**	944	59.5	946	59.4
483.xalancbmk	8	590	93.6	591	93.5	**590**	**93.5**	8	590	93.6	591	93.5	**590**	**93.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer
for peak, are compiled in 64-bit mode
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

```
456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
           -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
           -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

  400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast` `-ansi-alias`
`-prefetch`

     401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast` `-prefetch`

       403.gcc: `-fast` `-inline-calloc` `-opt-malloc-options=3`

       429.mcf: `-fast` `-prefetch`

     445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT` `-O2` `-ipo`
`-no-prec-div` `-ansi-alias`

     456.hmmer: `-fast` `-unroll2` `-ansi-alias` `-opt-multi-version-aggressive`

      458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast` `-unroll4`

  462.libquantum: `-fast` `-unroll4` `-Ob0` `-prefetch`
`-opt-streaming-stores always` `-vec-guard-write`
`-opt-malloc-options=3` `-parallel` `-par-runtime-control`

     464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast` `-unroll2`
`-ansi-alias`

C++ benchmarks:

    471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT` `-O3` `-ipo`
`-no-prec-div` `-ansi-alias` `-opt-ra-region-strategy=block`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

       473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT` `-O3` `-ipo`
`-no-prec-div` `-ansi-alias` `-opt-ra-region-strategy=routine`
`-Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

    483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

       403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/EM64T_Intel101_flags.20090714.00.html

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You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/EM64T_Intel101_flags.20090714.00.xml

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