



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 67.9

Acer Altos R520 (Intel Xeon E5310)

SPECint_rate_base2006 = 62.7

CPU2006 license: 97

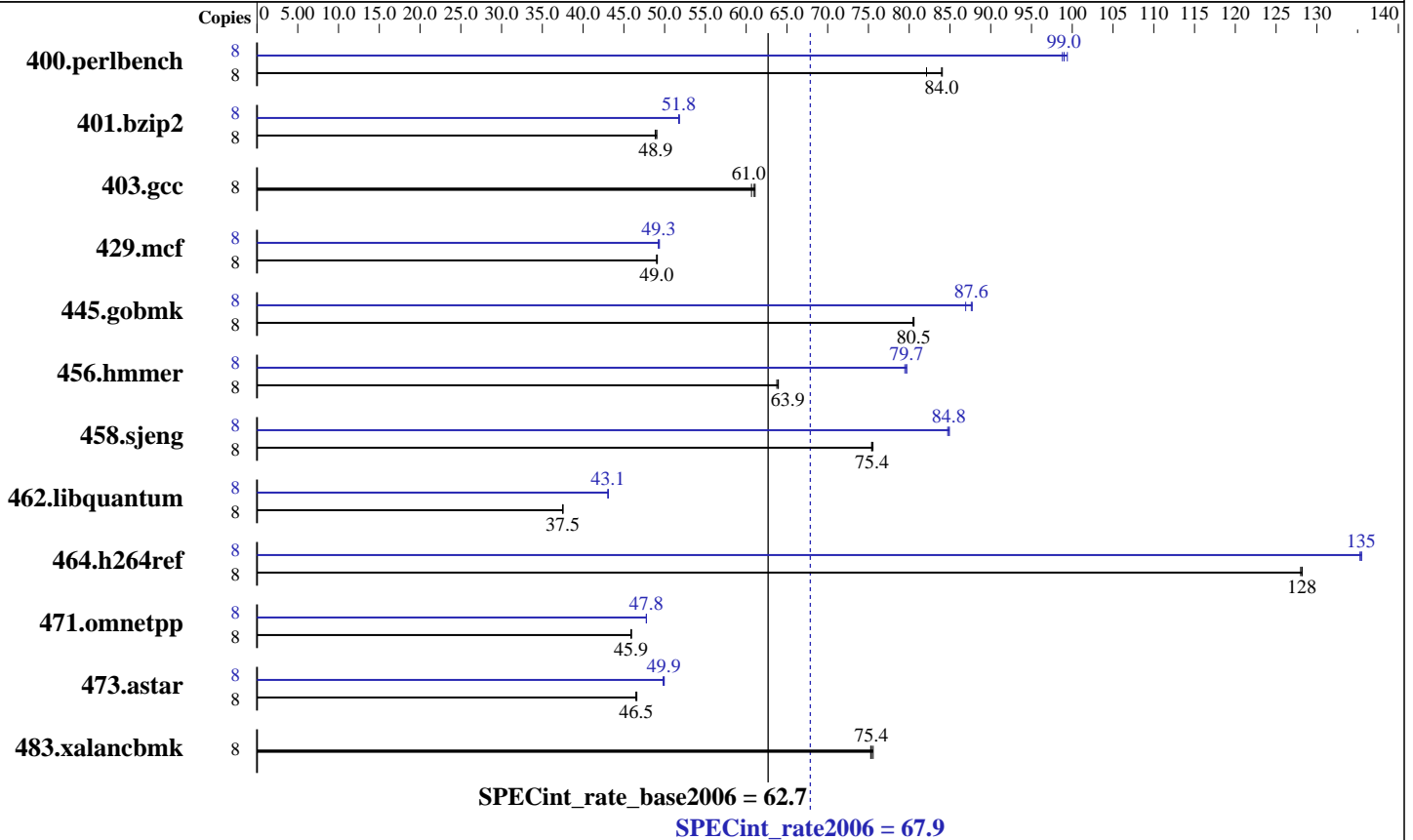
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Aug-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5310
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
 Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 EM64T kernel
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.0
 Build 20070426 Package ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	952	82.1	930	84.1	<u>931</u>	<u>84.0</u>	8	<u>789</u>	<u>99.0</u>	791	98.8	786	99.4
401.bzip2	8	1574	49.1	<u>1578</u>	<u>48.9</u>	1581	48.8	8	1490	51.8	1493	51.7	<u>1492</u>	<u>51.8</u>
403.gcc	8	1054	61.1	<u>1056</u>	<u>61.0</u>	1062	60.7	8	1054	61.1	<u>1056</u>	<u>61.0</u>	1062	60.7
429.mcf	8	1486	49.1	<u>1488</u>	<u>49.0</u>	1488	49.0	8	1478	49.4	<u>1480</u>	<u>49.3</u>	1482	49.2
445.gobmk	8	1041	80.6	<u>1042</u>	<u>80.5</u>	1043	80.5	8	957	87.7	<u>958</u>	<u>87.6</u>	965	86.9
456.hammer	8	1167	64.0	1171	63.8	<u>1168</u>	<u>63.9</u>	8	939	79.5	937	79.7	<u>937</u>	<u>79.7</u>
458.sjeng	8	1281	75.6	1284	75.4	<u>1283</u>	<u>75.4</u>	8	<u>1141</u>	<u>84.8</u>	1143	84.7	1140	84.9
462.libquantum	8	4418	37.5	4420	37.5	<u>4419</u>	<u>37.5</u>	8	<u>3850</u>	<u>43.1</u>	3846	43.1	3851	43.0
464.h264ref	8	1381	128	1383	128	<u>1381</u>	<u>128</u>	8	1307	135	1309	135	<u>1307</u>	<u>135</u>
471.omnetpp	8	1090	45.9	1089	45.9	<u>1090</u>	<u>45.9</u>	8	1047	47.8	1047	47.7	<u>1047</u>	<u>47.8</u>
473.astar	8	1208	46.5	<u>1207</u>	<u>46.5</u>	1205	46.6	8	1127	49.8	1124	50.0	<u>1125</u>	<u>49.9</u>
483.xalancbmk	8	733	75.3	730	75.6	<u>732</u>	<u>75.4</u>	8	733	75.3	730	75.6	<u>732</u>	<u>75.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Acer Altos R520
The Altos R720 and Altos R520 are electronically equivalent.

BIOS Settings

Snoop Filter set to OFF

Snoop Filter preserves cache coherency while
minimizing snoops to remote nodes.

Adjacent Cache Line Prefetch set to OFF

Prefetch data in order to shorten execution cycles
and maximize data processing efficiency.

Optimization for high-frequency FSB applications set to ON

taskset was used to bind copies to cores

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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