



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint®_rate2006 = 79.0

PRIMERGY TX200 S3, Intel Xeon processor E5345, 2.33 GHz

SPECint_rate_base2006 = 73.4

CPU2006 license: 22

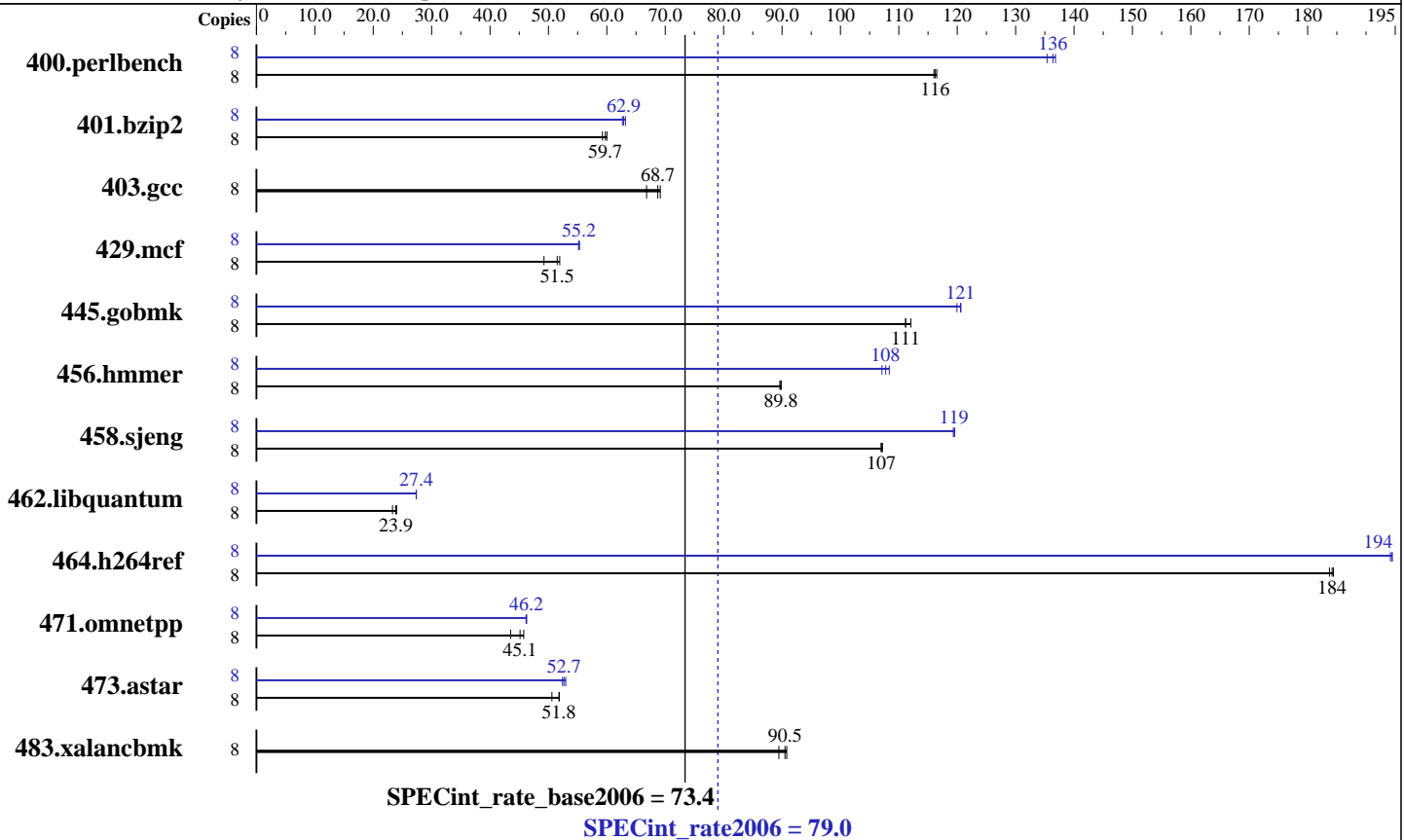
Test date: Aug-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Jan-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST336754SS (SAS, 36GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: I_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY TX200 S3, Intel Xeon processor E5345,
2.33 GHz

SPECint_rate2006 = 79.0

SPECint_rate_base2006 = 73.4

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Aug-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	674	116	672	116	671	116	8	577	135	573	136	571	137
401.bzip2	8	1292	59.7	1303	59.2	1286	60.0	8	1222	63.2	1232	62.7	1228	62.9
403.gcc	8	964	66.8	937	68.7	931	69.2	8	964	66.8	937	68.7	931	69.2
429.mcf	8	1482	49.2	1417	51.5	1405	51.9	8	1319	55.3	1321	55.2	1322	55.2
445.gobmk	8	755	111	749	112	755	111	8	696	121	700	120	696	121
456.hmmmer	8	831	89.8	833	89.6	831	89.8	8	697	107	689	108	693	108
458.sjeng	8	905	107	904	107	903	107	8	811	119	810	119	810	120
462.libquantum	8	7114	23.3	6946	23.9	6909	24.0	8	6053	27.4	6056	27.4	6059	27.4
464.h264ref	8	960	184	961	184	964	184	8	910	194	910	194	912	194
471.omnetpp	8	1149	43.5	1108	45.1	1092	45.8	8	1081	46.3	1083	46.2	1082	46.2
473.astar	8	1110	50.6	1083	51.9	1084	51.8	8	1066	52.7	1072	52.4	1060	53.0
483.xalancbmk	8	617	89.5	610	90.5	608	90.8	8	617	89.5	610	90.5	608	90.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY TX200 S3, Intel Xeon processor E5345,
2.33 GHz

SPECint_rate2006 = 79.0

SPECint_rate_base2006 = 73.4

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Aug-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -O3 -ipo -no-prec-div -ansi-alias

-L/opt/SmartHeap_8_1/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY TX200 S3, Intel Xeon processor E5345,
2.33 GHz

SPECint_rate2006 = 79.0

SPECint_rate_base2006 = 73.4

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Aug-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_1/lib -lsmarheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2

458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4

462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-opt-streaming-stores always

464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY TX200 S3, Intel Xeon processor E5345,
2.33 GHz

SPECint_rate2006 = 79.0

SPECint_rate_base2006 = 73.4

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Aug-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:05:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 4 September 2007.