SPEC® CINT2006 Result
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Lenovo Group Limited
(Test Sponsor: Intel Corporation)

$SPECint^{®}\_rate2006 =$ Not Run

Lenovo ThinkPad T61(Intel Core 2 Duo T7700)

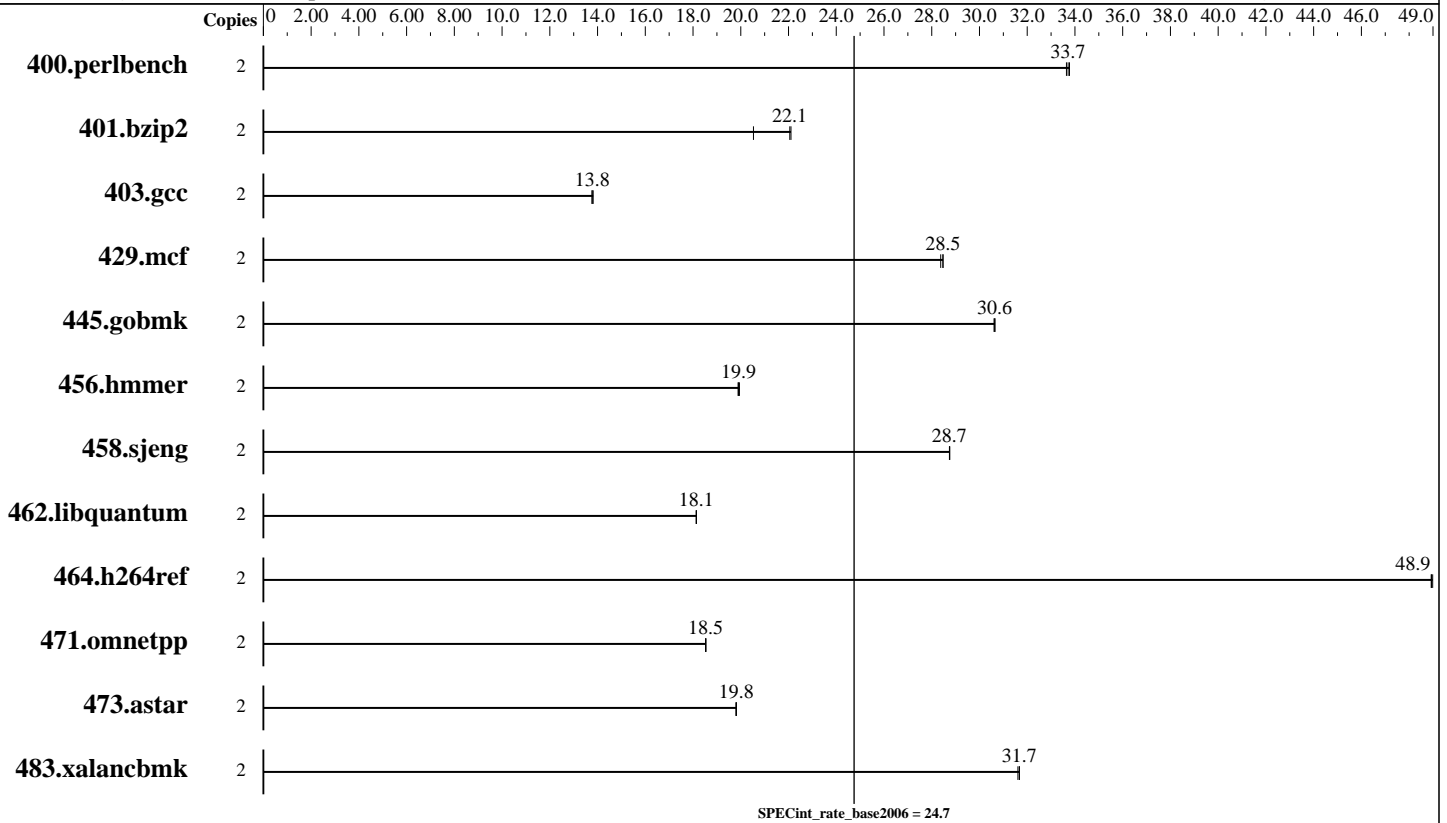SPECint_rate_base2006 = 24.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: Feb-2007

Benchmark	Copies	Result
400.perlbench	2	33.7
401.bzip2	2	22.1
403.gcc	2	13.8
429.mcf	2	28.5
445.gobmk	2	30.6
456.hmmer	2	19.9
458.sjeng	2	28.7
462.libquantum	2	18.1
464.h264ref	2	48.9
471.omnetpp	2	18.5
473.astar	2	19.8
483.xalancbmk	2	31.7

SPECint_rate_base2006 = 24.7

Hardware

CPU Name:	Intel Core 2 Duo T7700
CPU Characteristics:	2.40 GHz, 4MB L2, 800 MHz Bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Qimonda* PC-5300 DDR2-667 5-5-5-12)
Disk Subsystem:	Hitachi* HTS721010G9 SATA 100GB 7200RPM
Other Hardware:	None

Software

Operating System:	Windows XP Professional w/ SP2
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	SmartHeap library V7.4 from Microquill

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	579	33.8	**580**	**33.7**	581	33.6							
401.bzip2	2	940	20.5	**875**	**22.1**	873	22.1							
403.gcc	2	**1169**	**13.8**	1169	13.8	1165	13.8							
429.mcf	2	643	28.4	**641**	**28.5**	641	28.5							
445.gobmk	2	684	30.7	685	30.6	**685**	**30.6**							
456.hmmer	2	**937**	**19.9**	938	19.9	936	19.9							
458.sjeng	2	842	28.7	**842**	**28.7**	842	28.8							
462.libquantum	2	2285	18.1	2284	18.1	**2285**	**18.1**							
464.h264ref	2	**904**	**48.9**	905	48.9	904	49.0							
471.omnetpp	2	675	18.5	**675**	**18.5**	674	18.5							
473.astar	2	709	19.8	**709**	**19.8**	709	19.8							
483.xalancbmk	2	436	31.7	**436**	**31.7**	437	31.6							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
XP Power Profile set to AC/High Performance
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

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Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
        -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:29:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 August 2007.