



SPEC® CINT2006 Result

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Fujitsu Siemens Computers

PRIMERGY TX200 S3, Intel Xeon processor E5310, 1.60 GHz

SPECint®_rate2006 = 36.3

SPECint_rate_base2006 = 33.7

CPU2006 license: 22

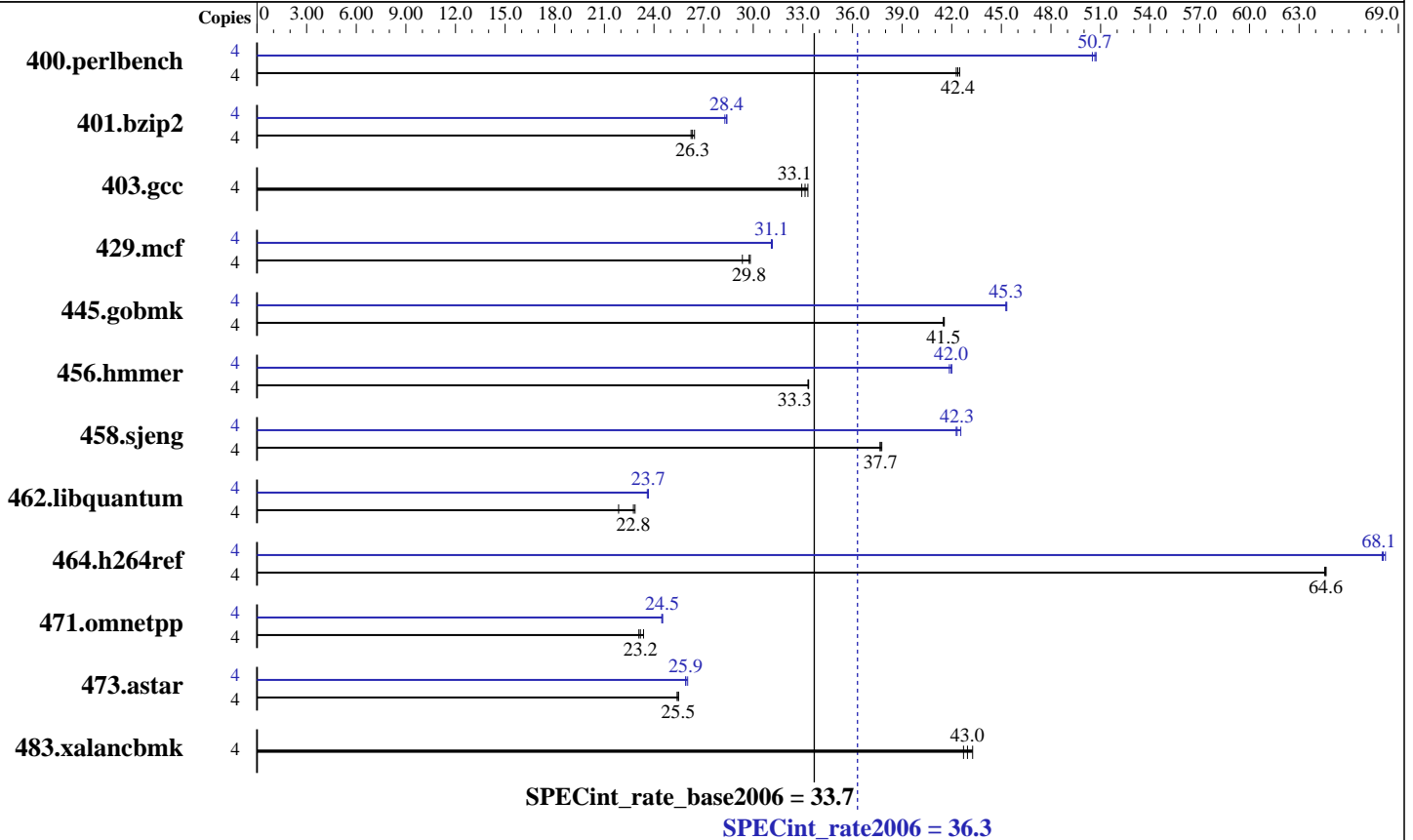
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jul-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5310
 CPU Characteristics: 1067 MHz system bus
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
 Disk Subsystem: Seagate ST336754SS (SAS, 36GB, 15000rpm)
 Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: I_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Smart Heap Library, Version 8.1
 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>922</u>	<u>42.4</u>	920	42.5	924	42.3	4	770	50.7	774	50.5	<u>771</u>	<u>50.7</u>
401.bzip2	4	1471	26.2	<u>1466</u>	<u>26.3</u>	1459	26.4	4	1365	28.3	<u>1359</u>	<u>28.4</u>	1359	28.4
403.gcc	4	978	32.9	<u>972</u>	<u>33.1</u>	967	33.3	4	978	32.9	<u>972</u>	<u>33.1</u>	967	33.3
429.mcf	4	1243	29.3	<u>1226</u>	<u>29.8</u>	1223	29.8	4	1173	31.1	1171	31.2	<u>1173</u>	<u>31.1</u>
445.gobmk	4	1011	41.5	<u>1011</u>	<u>41.5</u>	1010	41.6	4	927	45.3	926	45.3	<u>927</u>	<u>45.3</u>
456.hmmmer	4	1121	33.3	1119	33.3	<u>1119</u>	<u>33.3</u>	4	889	42.0	<u>889</u>	<u>42.0</u>	892	41.9
458.sjeng	4	1281	37.8	1285	37.7	<u>1284</u>	<u>37.7</u>	4	<u>1144</u>	<u>42.3</u>	1145	42.3	1138	42.5
462.libquantum	4	3791	21.9	<u>3642</u>	<u>22.8</u>	3626	22.9	4	3513	23.6	<u>3504</u>	<u>23.7</u>	3503	23.7
464.h264ref	4	<u>1370</u>	<u>64.6</u>	1371	64.5	1370	64.6	4	<u>1300</u>	<u>68.1</u>	1301	68.0	1298	68.2
471.omnetpp	4	1083	23.1	1070	23.4	<u>1079</u>	<u>23.2</u>	4	<u>1020</u>	<u>24.5</u>	1022	24.5	1020	24.5
473.astar	4	1106	25.4	<u>1103</u>	<u>25.5</u>	1102	25.5	4	<u>1083</u>	<u>25.9</u>	1078	26.0	1083	25.9
483.xalancbmk	4	638	43.3	<u>643</u>	<u>43.0</u>	646	42.7	4	638	43.3	<u>643</u>	<u>43.0</u>	646	42.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -O3 -ipo -no-prec-div -ansi-alias

-L/opt/SmartHeap_8_1/lib -lsmarheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-I/opt/intel/cce/10.0.023/include
-L/opt/intel/cce/10.0.023/lib

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-L/opt/SmartHeap_8_1/lib -lsmarheap

445.gobmk: Same as 400.perlbench

456.hmmer: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2

458.sjeng: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll4

462.libquantum: -prof_gen(pass 1) -prof_use(pass 2) -fast -prefetch
-opt-streaming-stores always

464.h264ref: -prof_gen(pass 1) -prof_use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml



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