SPEC® CINT2006 Result
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Fujitsu Siemens Computers

PRIMERGY TX200 S3, Intel Xeon processor 5110, 1.60 GHz

SPECint®_rate2006 =   21.2
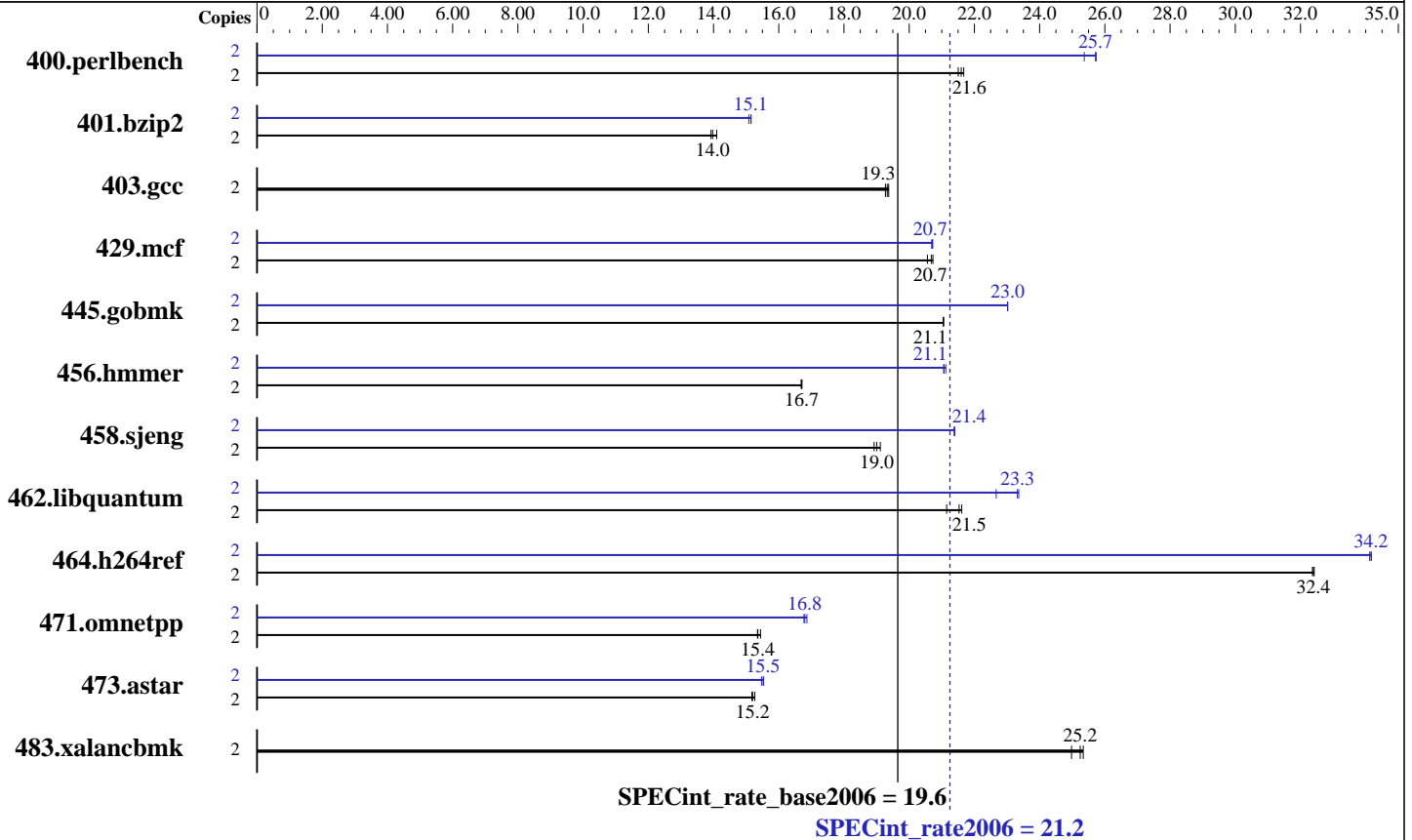
SPECint_rate_base2006 = 19.6

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jul-2007
Hardware Availability: Jul-2006
Software Availability: Jun-2007

	Copies		
400.perlbench	2	25.7	
	2	21.6	
401.bzip2	2	15.1	
	2	14.0	
403.gcc	2	19.3	
429.mcf	2	20.7	
	2	20.7	
445.gobmk	2	23.0	
	2	21.1	
456.hmmer	2	21.1	
	2	16.7	
458.sjeng	2	21.4	
	2	19.0	
462.libquantum	2	23.3	
	2	21.5	
464.h264ref	2	34.2	
	2	32.4	
471.omnetpp	2	16.8	
	2	15.4	
473.astar	2	15.5	
	2	15.2	
483.xalancbmk	2	25.2	

SPECint_rate_base2006 = 19.6

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Hardware

CPU Name: Intel Xeon 5110
CPU Characteristics: 1067 MHz system bus
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: Seagate ST336754SS (SAS, 36GB, 15000rpm)
Other Hardware: None

Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070308, Package-ID: l_cc_p_10.0.023
Auto Parallel: No
File System: ext2
System State: Multiuser, Runlevel 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Smart Heap Library, Version 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	909	21.5	**905**	**21.6**	902	21.7	2	759	25.7	770	25.4	**760**	**25.7**
401.bzip2	2	**1381**	**14.0**	1369	14.1	1386	13.9	2	1274	15.2	1280	15.1	**1274**	**15.1**
403.gcc	2	835	19.3	**833**	**19.3**	831	19.4	2	835	19.3	**833**	**19.3**	831	19.4
429.mcf	2	887	20.6	**882**	**20.7**	880	20.7	2	**881**	**20.7**	880	20.7	882	20.7
445.gobmk	2	997	21.0	**997**	**21.1**	997	21.1	2	911	23.0	**911**	**23.0**	911	23.0
456.hmmer	2	1117	16.7	**1117**	**16.7**	1118	16.7	2	**886**	**21.1**	883	21.1	886	21.1
458.sjeng	2	1279	18.9	**1273**	**19.0**	1266	19.1	2	1131	21.4	1132	21.4	**1132**	**21.4**
462.libquantum	2	1959	21.1	1917	21.6	**1925**	**21.5**	2	**1777**	**23.3**	1828	22.7	1774	23.4
464.h264ref	2	1365	32.4	1368	32.4	**1366**	**32.4**	2	1297	34.1	1295	34.2	**1295**	**34.2**
471.omnetpp	2	**814**	**15.4**	814	15.3	810	15.4	2	**744**	**16.8**	745	16.8	741	16.9
473.astar	2	**924**	**15.2**	925	15.2	920	15.3	2	**905**	**15.5**	904	15.5	907	15.5
483.xalancbmk	2	545	25.3	**547**	**25.2**	552	25.0	2	545	25.3	**547**	**25.2**	552	25.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All binaries were built with 32-bit Intel compiler except:
401.bzip2 and 456.hmmer in peak were built with 64-bit Intel
compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-fast
```

C++ benchmarks:
```
-xT -O3 -ipo -no-prec-div -ansi-alias
-L/opt/SmartHeap_8_1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
    401.bzip2: /opt/intel/cce/10.0.023/bin/icc
               -I/opt/intel/cce/10.0.023/include
               -L/opt/intel/cce/10.0.023/lib

   456.hmmer: /opt/intel/cce/10.0.023/bin/icc
              -I/opt/intel/cce/10.0.023/include
              -L/opt/intel/cce/10.0.023/lib
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
 400.perlbench: -DSPEC_CPU_LINUX_IA32
     401.bzip2: -DSPEC_CPU_LP64
     456.hmmer: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

    400.perlbench: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

        401.bzip2: `-fast`

         403.gcc: basepeak = yes

         429.mcf: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch`
                `-L/opt/SmartHeap_8_1/lib -lsmartheap`

       445.gobmk: `Same as 400.perlbench`

       456.hmmer: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2`

       458.sjeng: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll4`

    462.libquantum: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -prefetch`
                `-opt-streaming-stores always`

      464.h264ref: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -unroll2`
                `-ansi-alias`

  C++ benchmarks:

      471.omnetpp: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast -ansi-alias`
                `-L/opt/SmartHeap_8_1/lib -lsmartheap`

        473.astar: `Same as 471.omnetpp`

     483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

         403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/FSC_Intel_flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/FSC_Intel_flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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