



SPEC® CINT2006 Result

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Bull SAS

SPECint®_rate2006 = 78.4

NovaScale R460
(Intel Xeon processor X5355,2.66GHz)

SPECint_rate_base2006 = 76.1

CPU2006 license: 20

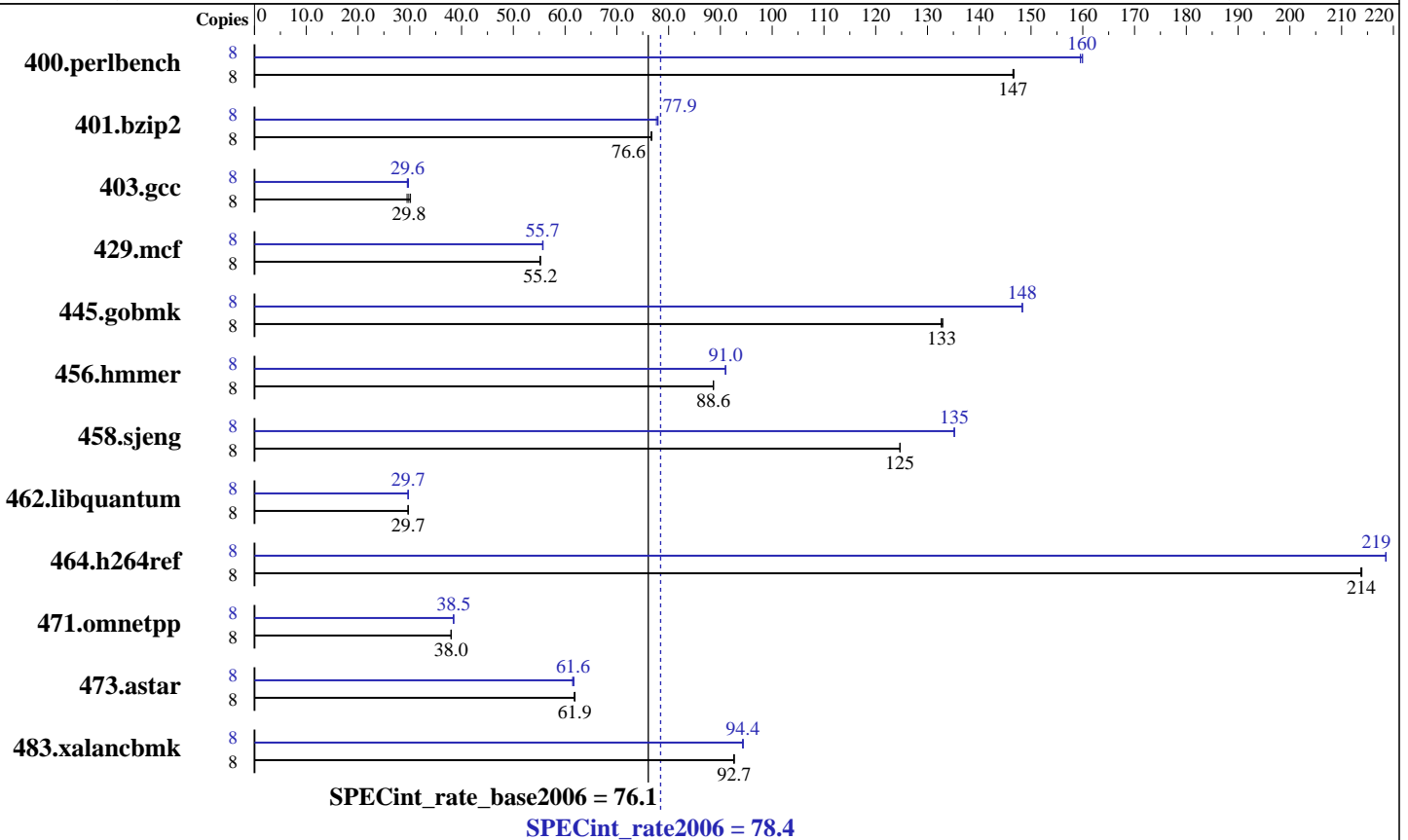
Test date: May-2007

Test sponsor: Bull SAS

Hardware Availability: Mar-2007

Tested by: Bull SAS

Software Availability: Dec-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66 GHz, 8 MB L2, 1333 MHz bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1 to 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 24 GB (12x2 GB) FB-DIMM PC2-5300F ECC CL5
 Disk Subsystem: 1x73 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 R2 Enterprise X64 Edition Service Pack1
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.033 Build no 20061103Z
 Microsoft Visual Studio .NET 2003 (lib & linker)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0 (shIW32M.lib)



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	533	147	<u>533</u>	<u>147</u>	533	147	8	<u>489</u>	<u>160</u>	490	160	489	160
401.bzip2	8	1006	76.7	1007	76.6	<u>1007</u>	<u>76.6</u>	8	<u>991</u>	<u>77.9</u>	991	77.9	994	77.6
403.gcc	8	2185	29.5	<u>2160</u>	<u>29.8</u>	2137	30.1	8	2182	29.5	<u>2176</u>	<u>29.6</u>	2165	29.7
429.mcf	8	1323	55.1	1321	55.2	<u>1321</u>	<u>55.2</u>	8	<u>1310</u>	<u>55.7</u>	1309	55.7	1311	55.6
445.gobmk	8	631	133	633	133	<u>632</u>	<u>133</u>	8	<u>566</u>	<u>148</u>	566	148	566	148
456.hammer	8	<u>842</u>	<u>88.6</u>	842	88.6	842	88.7	8	820	91.0	820	91.0	<u>820</u>	<u>91.0</u>
458.sjeng	8	776	125	<u>776</u>	<u>125</u>	776	125	8	<u>716</u>	<u>135</u>	716	135	716	135
462.libquantum	8	<u>5585</u>	<u>29.7</u>	5585	29.7	5584	29.7	8	5580	29.7	<u>5583</u>	<u>29.7</u>	5590	29.7
464.h264ref	8	828	214	<u>828</u>	<u>214</u>	828	214	8	810	219	<u>810</u>	<u>219</u>	810	218
471.omnetpp	8	1315	38.0	<u>1316</u>	<u>38.0</u>	1317	38.0	8	1300	38.5	<u>1300</u>	<u>38.5</u>	1300	38.5
473.astar	8	<u>907</u>	<u>61.9</u>	909	61.8	907	61.9	8	<u>912</u>	<u>61.6</u>	910	61.7	914	61.4
483.xalancbmk	8	<u>596</u>	<u>92.7</u>	596	92.6	596	92.7	8	<u>585</u>	<u>94.4</u>	585	94.4	585	94.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The NovaScale R440 and the NovaScale R460 models are electronically equivalent.
The results have been measured on a NovaScale R440 model.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32



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Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags.20090714.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags.20090714.00.xml>

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For other inquiries, please contact webmaster@spec.org.

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