



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY RX200 S3, Intel Xeon processor 5120,  
1.86 GHz

**SPECint®\_rate2006 = 41.0**

**SPECint\_rate\_base2006 = 39.1**

CPU2006 license: 22

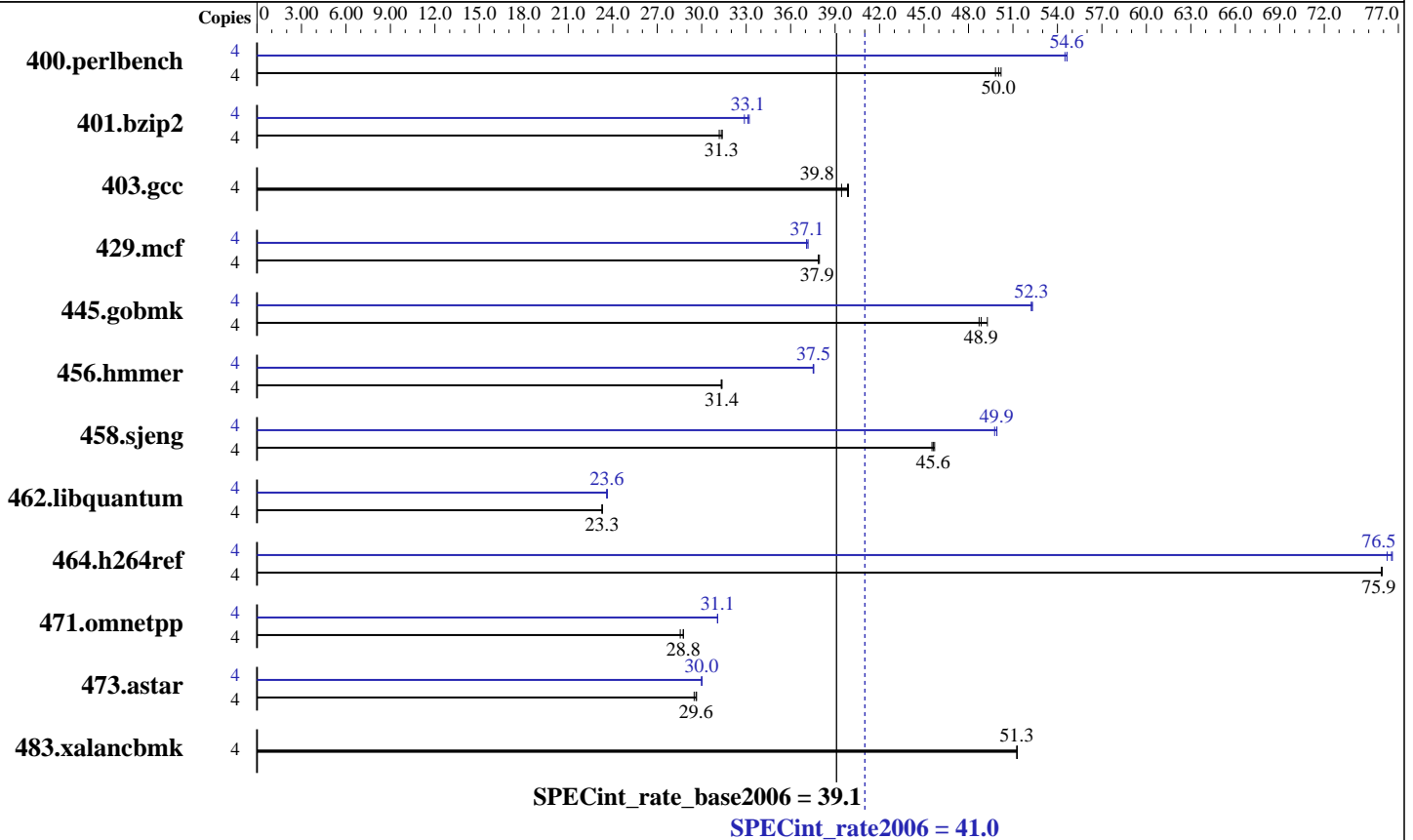
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: May-2007

Hardware Availability: Jul-2006

Software Availability: Feb-2007



### Hardware

CPU Name: Intel Xeon 5120  
 CPU Characteristics: 1067 MHz system bus  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: SAS (73GB 15400 rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	784	49.8	<b>781</b>	<b>50.0</b>	779	50.2	4	717	54.5	<b>715</b>	<b>54.6</b>	715	54.6
401.bzip2	4	<b>1232</b>	<b>31.3</b>	1230	31.4	1238	31.2	4	<b>1166</b>	<b>33.1</b>	1162	33.2	1174	32.9
403.gcc	4	807	39.9	817	39.4	<b>808</b>	<b>39.8</b>	4	807	39.9	817	39.4	<b>808</b>	<b>39.8</b>
429.mcf	4	<b>963</b>	<b>37.9</b>	961	37.9	963	37.9	4	981	37.2	984	37.1	<b>983</b>	<b>37.1</b>
445.gobmk	4	852	49.3	861	48.7	<b>859</b>	<b>48.9</b>	4	803	52.2	<b>803</b>	<b>52.3</b>	802	52.3
456.hmmer	4	<b>1190</b>	<b>31.4</b>	1192	31.3	1190	31.4	4	994	37.6	995	37.5	<b>994</b>	<b>37.5</b>
458.sjeng	4	1059	45.7	1063	45.5	<b>1061</b>	<b>45.6</b>	4	970	49.9	973	49.8	<b>970</b>	<b>49.9</b>
462.libquantum	4	3558	23.3	<b>3559</b>	<b>23.3</b>	3560	23.3	4	3514	23.6	<b>3510</b>	<b>23.6</b>	3507	23.6
464.h264ref	4	1166	75.9	<b>1167</b>	<b>75.9</b>	1167	75.9	4	1161	76.2	1156	76.6	<b>1157</b>	<b>76.5</b>
471.omnetpp	4	876	28.6	869	28.8	<b>869</b>	<b>28.8</b>	4	804	31.1	805	31.1	<b>805</b>	<b>31.1</b>
473.astar	4	<b>948</b>	<b>29.6</b>	952	29.5	947	29.7	4	<b>936</b>	<b>30.0</b>	936	30.0	936	30.0
483.xalancbmk	4	538	51.3	<b>538</b>	<b>51.3</b>	538	51.3	4	538	51.3	<b>538</b>	<b>51.3</b>	538	51.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Adjacent Sector Prefetch = Disable

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## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmartheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 11:51:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 29 May 2007.