SPEC® CINT2006 Result
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Supermicro Motherboard PDSM4+

$SPECint^®\_rate2006 =$ 19.2

SPECint_rate_base2006 = 18.4

Benchmark	Copies	Result (peak)	Result (base)
400.perlbench	2 / 2	27.5	24.8
401.bzip2	2 / 2	13.2	12.8
403.gcc	2 / 2	11.1	10.8
429.mcf	2		19.9
445.gobmk	2 / 2	25.2	23.1
456.hmmer	2 / 2	15.1	14.8
458.sjeng	2 / 2	23.3	22.0
462.libquantum	2 / 2	17.9	17.7
464.h264ref	2 / 2	36.1	35.2
471.omnetpp	2 / 2	15.9	14.8
473.astar	2 / 2	13.8	12.9
483.xalancbmk	2 / 2	24.9	24.8

SPECint_rate_base2006 = 18.4

SPECint_rate2006 = 19.2

Hardware

CPU Name:	Intel Core 2 Duo E4300
CPU Characteristics:	1.8GHz 800MHz bus
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (4 X 512MB, DDR2 667MHz, CL5, ECC)
Disk Subsystem:	WD2500YS-01SHB1 250GB SATA II, 7200RPM
Other Hardware:	None

Software

Operating System:	Windows XP Professional w/ SP2
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20070322Z
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.0 from http://www.microquill.com/

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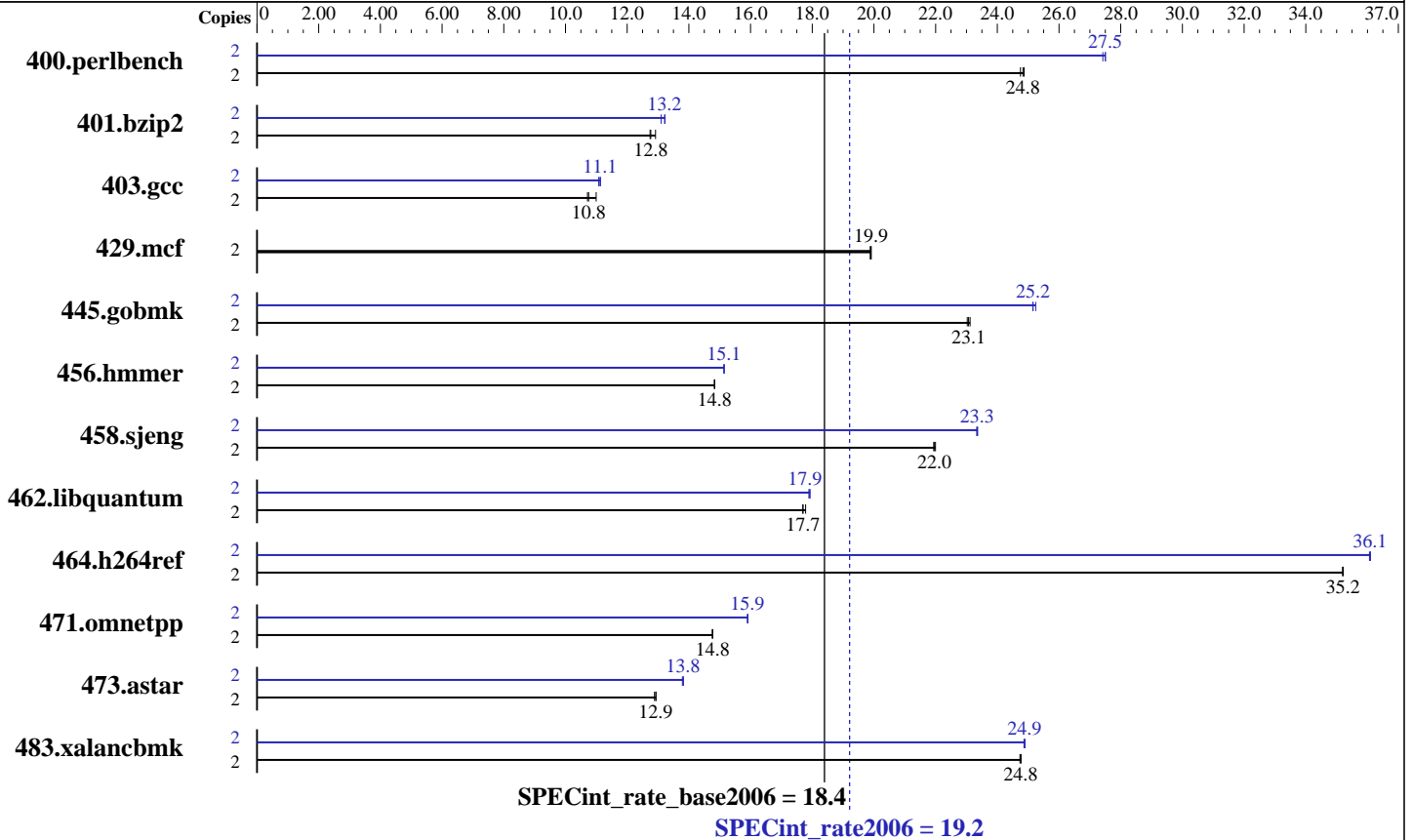
SPECint_rate_base2006 = 18.4

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2007
Hardware Availability: Apr-2007
Software Availability: Apr-2007

Results Table

Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
				Base							**Peak**			
400.perlbench	2	790	24.7	786	24.9	**787**	**24.8**	2	713	27.4	710	27.5	**710**	**27.5**
401.bzip2	2	1515	12.7	**1512**	**12.8**	1494	12.9	2	**1461**	**13.2**	1473	13.1	1459	13.2
403.gcc	2	1464	11.0	**1498**	**10.8**	1503	10.7	2	**1448**	**11.1**	1453	11.1	1446	11.1
429.mcf	2	918	19.9	**916**	**19.9**	916	19.9	2	918	19.9	**916**	**19.9**	916	19.9
445.gobmk	2	911	23.0	908	23.1	**910**	**23.1**	2	831	25.2	**834**	**25.2**	834	25.2
456.hmmer	2	1259	14.8	1257	14.8	**1258**	**14.8**	2	1232	15.1	1233	15.1	**1232**	**15.1**
458.sjeng	2	**1102**	**22.0**	1103	21.9	1100	22.0	2	1037	23.3	**1036**	**23.3**	1036	23.4
462.libquantum	2	2330	17.8	**2341**	**17.7**	2342	17.7	2	**2315**	**17.9**	2316	17.9	2311	17.9
464.h264ref	2	**1257**	**35.2**	1257	35.2	1258	35.2	2	1226	36.1	1227	36.1	**1226**	**36.1**
471.omnetpp	2	**846**	**14.8**	846	14.8	847	14.8	2	786	15.9	785	15.9	**786**	**15.9**
473.astar	2	1086	12.9	1090	12.9	**1086**	**12.9**	2	**1016**	**13.8**	1018	13.8	1016	13.8
483.xalancbmk	2	557	24.8	558	24.7	**557**	**24.8**	2	554	24.9	555	24.9	**555**	**24.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with CSE-823S-R500LP case,
For a general system, a 420W (minimum) ATX12V power supply [8-pin +12V AND 24-pin is recommended to assure system stability].
Product description located as of http://www.supermicro.com/products/motherboard/Xeon3000/3010/PDSM4+.cfm
The system bus runs at 800 MHz

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

Continued on next page

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Base Optimization Flags (Continued)

C++ benchmarks:
```
    -fast -Qcxx_features /F512000000 shlw32m.lib
              -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:
```
    icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
    icl -Qvc7.1
```

Peak Portability Flags

```
        403.gcc: -DSPEC_CPU_WIN32
      464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
              shlw32m.lib            -link /FORCE:MULTIPLE
```

```
       401.bzip2: Same as 400.perlbench
```

```
         403.gcc: Same as 400.perlbench
```

```
         429.mcf: basepeak = yes
```

```
       445.gobmk: Same as 400.perlbench
```

```
       456.hmmer: Same as 400.perlbench
```

```
        458.sjeng: Same as 400.perlbench
```

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features`
`/F512000000 shlw32m.lib          -link /FORCE:MULTIPLE`

473.astar: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo`
`-Qprec-div- -Qunroll4 -Ob2 -Qsfalign16 -Qcxx_features`
`/F512000000 shlw32m.lib          -link /FORCE:MULTIPLE`

483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml

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