



SPEC® CINT2006 Result

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Bull SAS

NovaScale R440
(Intel Xeon processor E5310,1.60GHz)

SPECint®_rate2006 = 50.7

SPECint_rate_base2006 = 48.9

CPU2006 license: 20

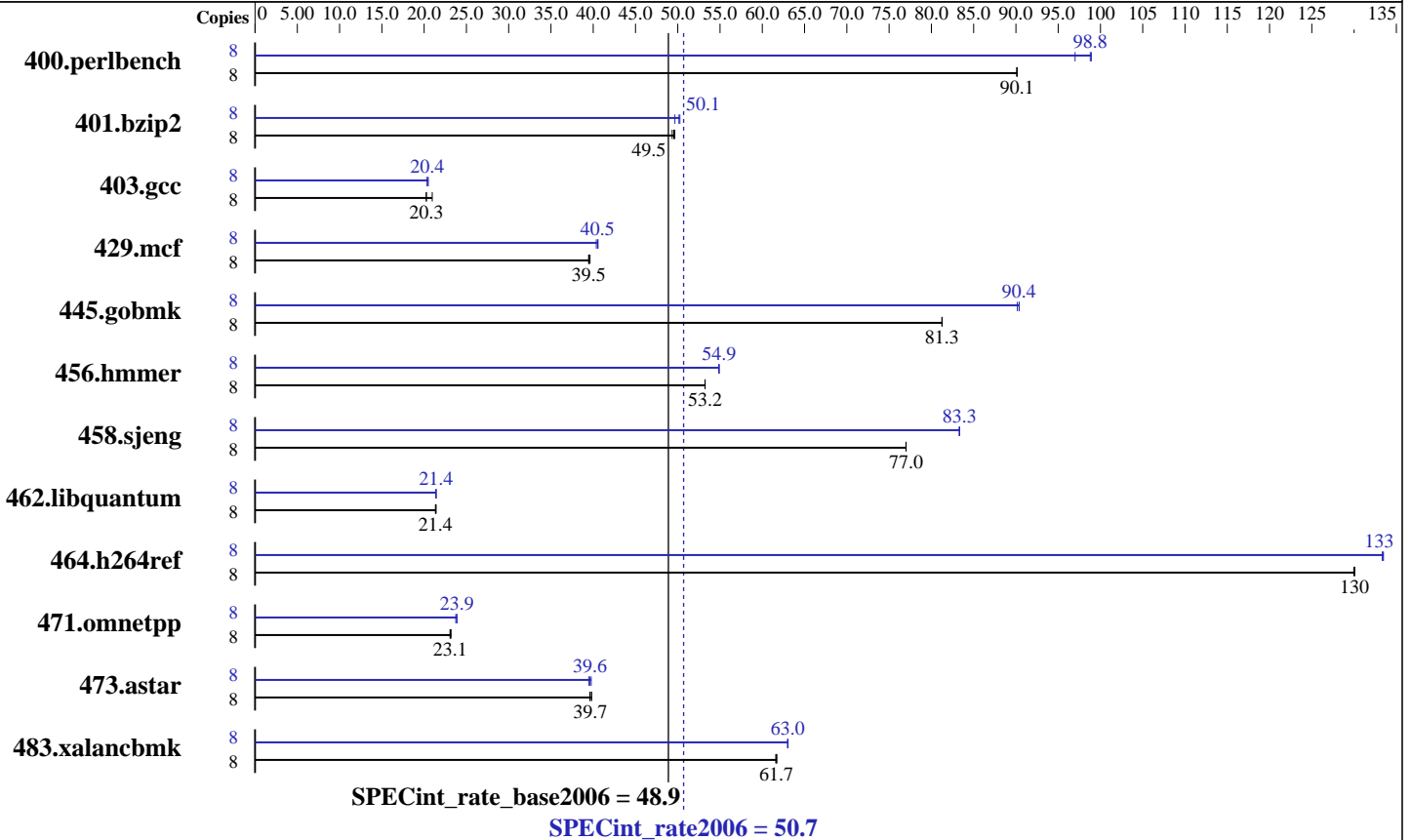
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Apr-2007

Hardware Availability: Mar-2007

Software Availability: Dec-2006



Hardware

CPU Name: Intel Xeon E5310
 CPU Characteristics: 1.60 GHz, 8MB L2, 1066MHz bus
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1 to 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 12 GB (1GB DIMMx12, FB-DIMM PC2-5300F ECC CL5)
 Disk Subsystem: 73 GB SAS, 10000RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition Service Pack 1
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.033 Build no 20061103Z
 Microsoft Visual Studio .NET 2003 (lib & linker)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0 (shIW32M.lib)



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	867	90.1	867	90.1	867	90.1	8	806	97.0	790	98.9	791	98.8
401.bzip2	8	1565	49.3	1555	49.6	1558	49.5	8	1555	49.7	1536	50.3	1540	50.1
403.gcc	8	3073	21.0	3180	20.3	3178	20.3	8	3160	20.4	3164	20.4	3143	20.5
429.mcf	8	1848	39.5	1841	39.6	1849	39.5	8	1798	40.6	1800	40.5	1808	40.4
445.gobmk	8	1032	81.3	1033	81.3	1033	81.2	8	931	90.2	929	90.4	929	90.4
456.hammer	8	1402	53.2	1402	53.2	1402	53.2	8	1361	54.9	1360	54.9	1360	54.9
458.sjeng	8	1257	77.0	1257	77.0	1257	77.0	8	1162	83.3	1162	83.3	1161	83.3
462.libquantum	8	7739	21.4	7763	21.4	7764	21.4	8	7739	21.4	7760	21.4	7738	21.4
464.h264ref	8	1362	130	1361	130	1361	130	8	1327	133	1328	133	1326	133
471.omnetpp	8	2167	23.1	2166	23.1	2156	23.2	8	2106	23.7	2095	23.9	2094	23.9
473.astar	8	1416	39.7	1418	39.6	1410	39.8	8	1423	39.5	1418	39.6	1412	39.8
483.xalancbmk	8	894	61.7	896	61.6	895	61.7	8	876	63.0	876	63.0	876	63.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

The NovaScale R440 and the NovaScale R460 models are electronically equivalent.
The results have been measured on a NovaScale R460 model.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32



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Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/flags.20090714.00.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/flags.20090714.00.xml>

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