



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor E5335, 2.0 GHz

SPECint®\_rate2006 = 77.6

SPECint\_rate\_base2006 = 73.7

CPU2006 license: 22

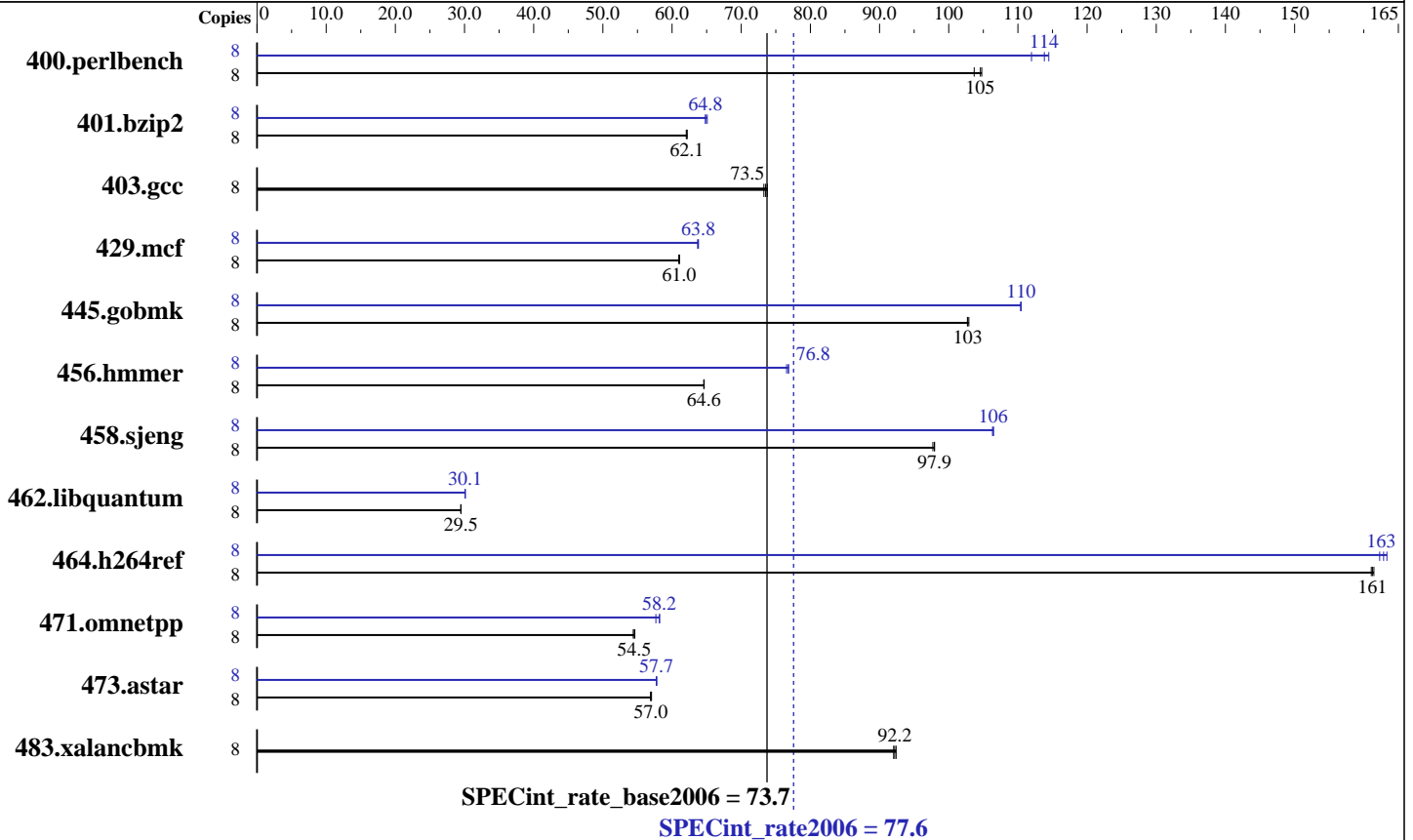
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Apr-2007

Hardware Availability: Feb-2007

Software Availability: Feb-2007



### Hardware

CPU Name: Intel Xeon E5335  
 CPU Characteristics: 1333 MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)  
 Disk Subsystem: SAS (73GB 15400 rpm)  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l\_cc\_p\_9.1.047  
 Auto Parallel: No  
 File System: ext2  
 System State: Multiuser, Runlevel 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Smart Heap Library, Version 8.1



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	746	105	<b>747</b>	<b>105</b>	754	104	8	683	114	<b>687</b>	<b>114</b>	698	112
401.bzip2	8	<b>1242</b>	<b>62.1</b>	1244	62.1	1241	62.2	8	<b>1191</b>	<b>64.8</b>	1191	64.8	1186	65.1
403.gcc	8	873	73.8	879	73.3	<b>876</b>	<b>73.5</b>	8	873	73.8	879	73.3	<b>876</b>	<b>73.5</b>
429.mcf	8	<b>1196</b>	<b>61.0</b>	1195	61.1	1196	61.0	8	<b>1144</b>	<b>63.8</b>	1143	63.8	1145	63.7
445.gobmk	8	815	103	817	103	<b>817</b>	<b>103</b>	8	<b>760</b>	<b>110</b>	760	110	760	110
456.hmmer	8	1155	64.6	1156	64.6	<b>1155</b>	<b>64.6</b>	8	975	76.6	<b>972</b>	<b>76.8</b>	971	76.8
458.sjeng	8	991	97.7	<b>989</b>	<b>97.9</b>	988	98.0	8	909	106	<b>909</b>	<b>106</b>	910	106
462.libquantum	8	<b>5623</b>	<b>29.5</b>	5621	29.5	5624	29.5	8	5507	30.1	5509	30.1	<b>5508</b>	<b>30.1</b>
464.h264ref	8	1096	161	<b>1098</b>	<b>161</b>	1099	161	8	<b>1087</b>	<b>163</b>	1091	162	1084	163
471.omnetpp	8	<b>917</b>	<b>54.5</b>	915	54.6	919	54.4	8	867	57.7	859	58.2	<b>860</b>	<b>58.2</b>
473.astar	8	<b>986</b>	<b>57.0</b>	987	56.9	985	57.0	8	973	57.7	972	57.8	<b>973</b>	<b>57.7</b>
483.xalancbmk	8	597	92.4	<b>599</b>	<b>92.2</b>	600	92.0	8	597	92.4	<b>599</b>	<b>92.2</b>	600	92.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

The system bus runs at 1333 MHz

All binaries were built with 32-bit Intel compiler except:  
401.bzip2, 456.hmmer and 462.libquantum in peak were built with  
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:  
Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and  
the PRIMERGY TX300 S3 are electronically equivalent.

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## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

456.hmmer: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

462.libquantum: /opt/intel/cce/9.1.047/bin/icc  
-I/opt/intel/cce/9.1.047/include  
-L/opt/intel/cce/9.1.047/lib

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof\_gen(pass 1) -prof\_use(pass 2) -fast

401.bzip2: -fast

403.gcc: basepeak = yes

429.mcf: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

445.gobmk: Same as 429.mcf

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 429.mcf

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof\_gen(pass 1) -prof\_use(pass 2) -xP -O3 -ipo  
-no-prec-div -L/opt/SmartHeap\_8\_1/lib -lsmarheap

473.astar: -prof\_gen(pass 1) -prof\_use(pass 2) -fast  
-L/opt/SmartHeap\_8\_1/lib -lsmarheap

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.09.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml)



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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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