



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

SPECint®\_rate2006 = 82.3

ProLiant DL380 G5  
(2.66 GHz, Intel Xeon processor X5355)

SPECint\_rate\_base2006 = 79.6

CPU2006 license: 3

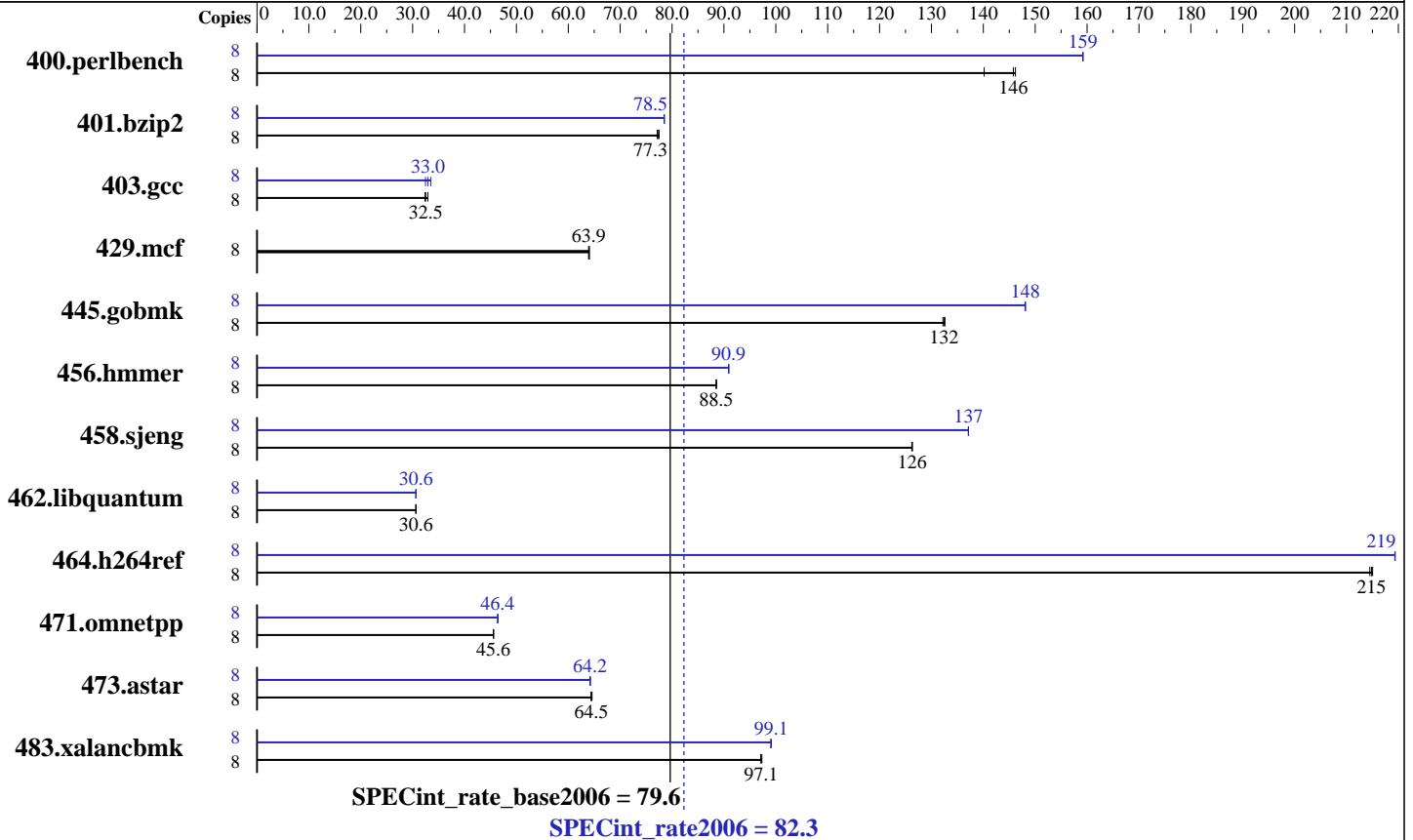
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2007

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



### Hardware

CPU Name: Intel Xeon X5355  
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2 GB PC2-5300 CL5)  
 Disk Subsystem: 4x36 GB 10 K SAS  
 Other Hardware: None

### Software

Operating System: Windows Server 2003 Enterprise x64 Edition SP1  
 Compiler: Intel C++ Compiler for 32-bit applications, Version 9.1, Build 20061103Z  
 Package ID: W\_CC\_C\_9.1.033  
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: MicroQuill SmartHeap Library 8.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	558	140	535	146	<b>536</b>	<b>146</b>	8	<b>491</b>	<b>159</b>	491	159	491	159
401.bzip2	8	1001	77.1	<b>999</b>	<b>77.3</b>	996	77.5	8	983	78.5	984	78.5	<b>984</b>	<b>78.5</b>
403.gcc	8	1956	32.9	1988	32.4	<b>1981</b>	<b>32.5</b>	8	<b>1954</b>	<b>33.0</b>	1921	33.5	1982	32.5
429.mcf	8	1139	64.1	<b>1141</b>	<b>63.9</b>	1141	63.9	8	1139	64.1	<b>1141</b>	<b>63.9</b>	1141	63.9
445.gobmk	8	635	132	633	133	<b>634</b>	<b>132</b>	8	567	148	567	148	<b>567</b>	<b>148</b>
456.hammer	8	843	88.5	843	88.5	<b>843</b>	<b>88.5</b>	8	<b>821</b>	<b>90.9</b>	821	90.9	821	90.9
458.sjeng	8	766	126	767	126	<b>766</b>	<b>126</b>	8	<b>706</b>	<b>137</b>	706	137	706	137
462.libquantum	8	5410	30.6	5411	30.6	<b>5411</b>	<b>30.6</b>	8	<b>5411</b>	<b>30.6</b>	5411	30.6	5411	30.6
464.h264ref	8	825	214	823	215	<b>824</b>	<b>215</b>	8	807	219	<b>807</b>	<b>219</b>	807	219
471.omnetpp	8	1097	45.6	<b>1097</b>	<b>45.6</b>	1097	45.6	8	1077	46.4	<b>1077</b>	<b>46.4</b>	1078	46.4
473.astar	8	870	64.6	872	64.4	<b>871</b>	<b>64.5</b>	8	875	64.2	<b>875</b>	<b>64.2</b>	873	64.3
483.xalancbmk	8	567	97.3	568	97.1	<b>568</b>	<b>97.1</b>	8	557	99.1	557	99.0	<b>557</b>	<b>99.1</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.  
Adjacent Sector Prefetch Disabled in BIOS.

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

## Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

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## Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof\_gen(pass 1) -Qprof\_use(pass 2) -fast -Qcxx\_features  
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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