



SPEC[®] CINT2006 Result

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Itautec

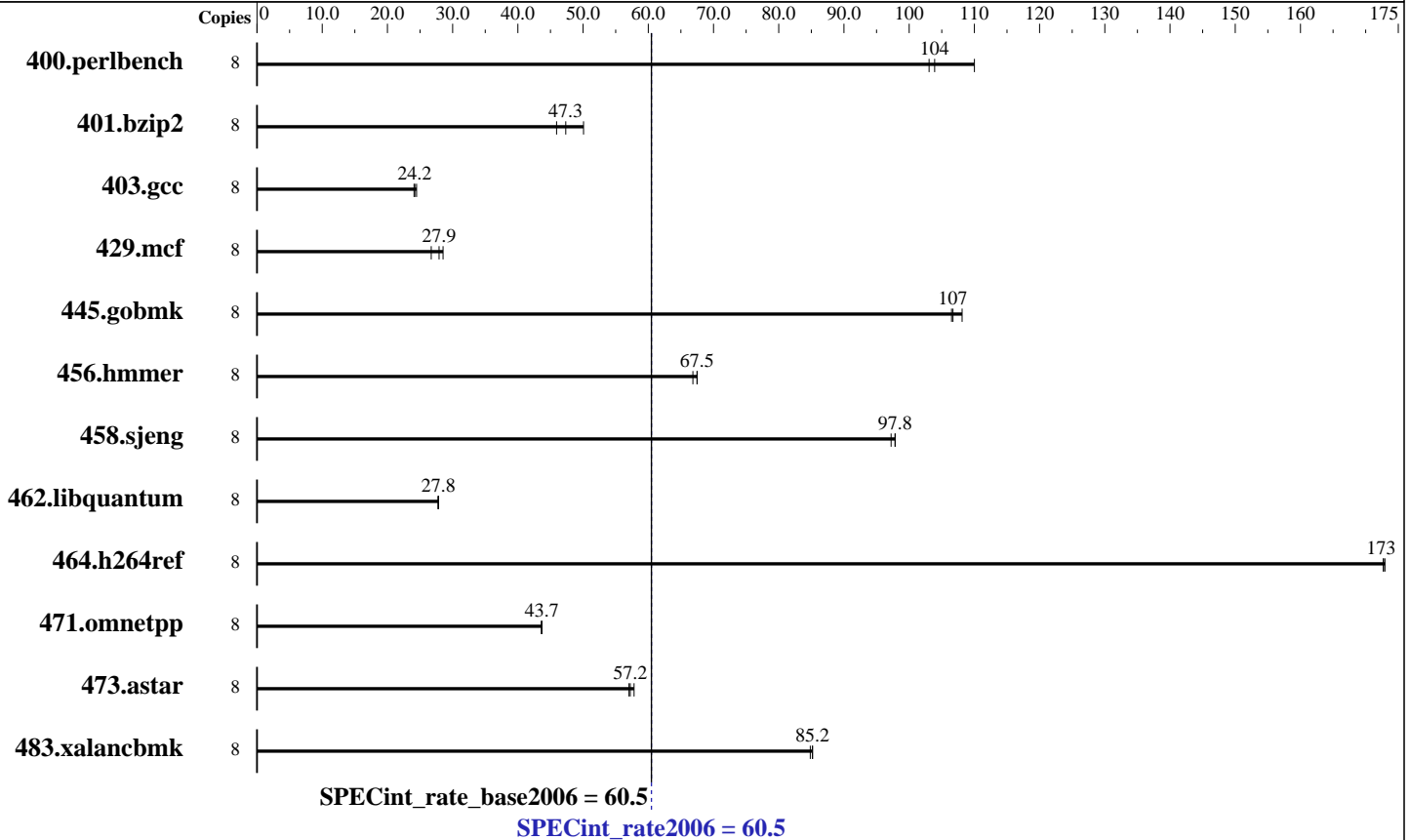
SPECint[®]_rate2006 = 60.5

Servidor Itautec LX211 (Intel Xeon X5355)

SPECint_rate_base2006 = 60.5

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2660
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1-2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 6 GB (6x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
 Disk Subsystem: 120 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.025 Build no 20060519Z
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Microquill SmartHeap Library v.8.0 for SMP



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	711	110	<u>752</u>	<u>104</u>	758	103	8	711	110	<u>752</u>	<u>104</u>	758	103
401.bzip2	8	1542	50.1	<u>1631</u>	<u>47.3</u>	1681	45.9	8	1542	50.1	<u>1631</u>	<u>47.3</u>	1681	45.9
403.gcc	8	<u>2661</u>	<u>24.2</u>	2632	24.5	2674	24.1	8	<u>2661</u>	<u>24.2</u>	2632	24.5	2674	24.1
429.mcf	8	<u>2613</u>	<u>27.9</u>	2731	26.7	2557	28.5	8	<u>2613</u>	<u>27.9</u>	2731	26.7	2557	28.5
445.gobmk	8	<u>786</u>	<u>107</u>	788	106	776	108	8	<u>786</u>	<u>107</u>	788	106	776	108
456.hammer	8	1117	66.8	1106	67.5	<u>1106</u>	<u>67.5</u>	8	1117	66.8	1106	67.5	<u>1106</u>	<u>67.5</u>
458.sjeng	8	<u>989</u>	<u>97.8</u>	989	97.8	996	97.2	8	<u>989</u>	<u>97.8</u>	989	97.8	996	97.2
462.libquantum	8	5953	27.8	5975	27.7	<u>5955</u>	<u>27.8</u>	8	5953	27.8	5975	27.7	<u>5955</u>	<u>27.8</u>
464.h264ref	8	1024	173	<u>1025</u>	<u>173</u>	1025	173	8	1024	173	<u>1025</u>	<u>173</u>	1025	173
471.omnetpp	8	1145	43.7	1146	43.6	<u>1145</u>	<u>43.7</u>	8	1145	43.7	1146	43.6	<u>1145</u>	<u>43.7</u>
473.astar	8	985	57.0	971	57.8	<u>981</u>	<u>57.2</u>	8	985	57.0	971	57.8	<u>981</u>	<u>57.2</u>
483.xalancbmk	8	648	85.2	651	84.8	<u>648</u>	<u>85.2</u>	8	648	85.2	651	84.8	<u>648</u>	<u>85.2</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlSMPMt.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F512000000 shlSMPMt.lib
-link /FORCE:MULTIPLE



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Optimization Flags

C benchmarks:

400.perlbench: basepeak = yes

401.bzip2: basepeak = yes

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml>



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