



# SPEC® CINT2006 Result

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## Fujitsu Siemens Computers

## SPECint®\_rate2006 = 49.1

## CELSIUS V840, AMD Opteron 2218

## SPECint\_rate\_base2006 = 43.8

CPU2006 license: 22

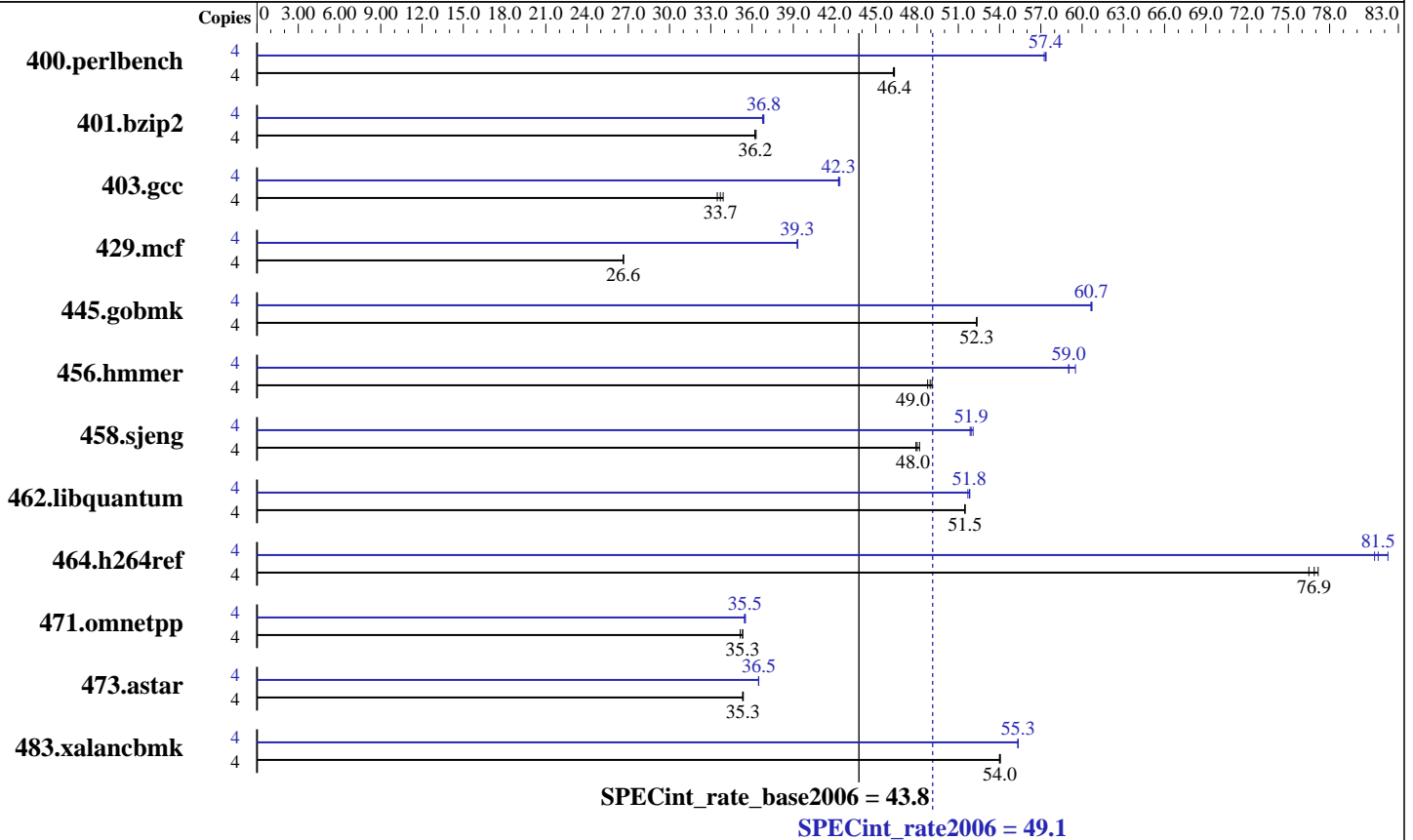
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Jan-2007

Hardware Availability: Nov-2006

Software Availability: Aug-2006



### Hardware

CPU Name: AMD Opteron 2218  
 CPU Characteristics: Dual Core, 2.6 GHz, 2 MB L2-cache  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x2GB DDR2-667 CL5 dual rank ECC DIMMs registered)  
 Disk Subsystem: SATA II, 80 GB  
 Other Hardware: None

### Software

Operating System: SLES 10 for AMD64/EM64T  
 Compiler: QLogic PathScale Compiler Suite, Release 2.5  
 SmartHeap 8.0 32 bit Library for Linux  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User SuSE Run Level 3  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	845	46.3	843	46.4	<b><u>843</u></b>	<b><u>46.4</u></b>	4	683	57.2	<b><u>681</u></b>	<b><u>57.4</u></b>	681	57.4
401.bzip2	4	1063	36.3	1066	36.2	<b><u>1065</u></b>	<b><u>36.2</u></b>	4	<b><u>1048</u></b>	<b><u>36.8</u></b>	1050	36.8	1047	36.8
403.gcc	4	950	33.9	<b><u>956</u></b>	<b><u>33.7</u></b>	962	33.5	4	761	42.3	<b><u>761</u></b>	<b><u>42.3</u></b>	760	42.4
429.mcf	4	<b><u>1369</u></b>	<b><u>26.6</u></b>	1369	26.7	1370	26.6	4	928	39.3	928	39.3	<b><u>928</u></b>	<b><u>39.3</u></b>
445.gobmk	4	802	52.3	802	52.3	<b><u>802</u></b>	<b><u>52.3</u></b>	4	<b><u>691</u></b>	<b><u>60.7</u></b>	692	60.6	691	60.7
456.hammer	4	765	48.8	760	49.1	<b><u>762</u></b>	<b><u>49.0</u></b>	4	627	59.5	<b><u>632</u></b>	<b><u>59.0</u></b>	632	59.0
458.sjeng	4	<b><u>1008</u></b>	<b><u>48.0</u></b>	1004	48.2	1010	47.9	4	929	52.1	<b><u>932</u></b>	<b><u>51.9</u></b>	933	51.9
462.libquantum	4	<b><u>1610</u></b>	<b><u>51.5</u></b>	1610	51.5	1610	51.5	4	1599	51.8	<b><u>1600</u></b>	<b><u>51.8</u></b>	1603	51.7
464.h264ref	4	1157	76.5	1147	77.2	<b><u>1151</u></b>	<b><u>76.9</u></b>	4	<b><u>1086</u></b>	<b><u>81.5</u></b>	1076	82.2	1089	81.3
471.omnetpp	4	<b><u>708</u></b>	<b><u>35.3</u></b>	712	35.1	708	35.3	4	705	35.4	<b><u>705</u></b>	<b><u>35.5</u></b>	704	35.5
473.astar	4	795	35.3	794	35.4	<b><u>795</u></b>	<b><u>35.3</u></b>	4	770	36.5	<b><u>770</u></b>	<b><u>36.5</u></b>	770	36.5
483.xalancbmk	4	511	54.1	511	54.0	<b><u>511</u></b>	<b><u>54.0</u></b>	4	499	55.4	<b><u>499</u></b>	<b><u>55.3</u></b>	499	55.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

powersave -f is applied to set CPU to maximum frequency prior to run  
 stacksize is set to unlimited prior to run  
 taskset utility used to bind CPU(s) to processes  
 numactl utility used to assign to each chip its local memory

## General Notes

Binaries supplied by AMD  
 Compilation done on an Opteron-based system  
 running SuSE 9.3 (Kernel 2.6.5.7)

BIOS default settings with  
 Node memory interleave disabled, SRAT enabled

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## Base Compiler Invocation

C benchmarks:  
 pathcc

C++ benchmarks:  
 pathCC



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-Ofast

C++ benchmarks:

-Ofast -m32

-L/spec/mpaton/6v1.0/amd512K8.lib/microquill/lib -lsmartheap

## Base Other Flags

C benchmarks:

-IPA:max\_jobs=2

C++ benchmarks:

-IPA:max\_jobs=2

## Peak Compiler Invocation

C benchmarks:

pathcc

C++ benchmarks:

pathCC

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 445.gobmk: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast  
 401.bz2: -O3 -LNO:ou\_prod\_max=10 -OPT:Ofast -OPT:alias=disjoint  
 403.gcc: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -m32 -O3  
 -OPT:Ofast  
 429.mcf: -m32 -O2 -ipa  
 -L/spec/mpaton/6v1.0/amd512K8.lib/microquill/lib -lsmarheap  
 445.gobmk: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -O3  
 -OPT:alias=disjoint -LNO:simd=0 -LNO:minvariant=off  
 -WOPT:retype\_expr=on  
 456.hmmcr: -O2 -OPT:alias=disjoint -WOPT:aggstr=0 -CG:cflow=0  
 458.sjeng: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -O3  
 -IPA:plimit=50000 -IPA:pu\_reorder=2  
 462.libquantum: -O3 -ipa -CG:local\_fwd\_sched=on -IPA:space=1000  
 464.h264ref: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -O3  
 -IPA:plimit=20000 -OPT:alias=disjoint -LNO:prefetch=0

C++ benchmarks:

471.omnetpp: -Ofast -IPA:pu\_reorder=2 -CG:gcm=off -m32  
 -L/spec/mpaton/6v1.0/amd512K8.lib/microquill/lib -lsmarheap  
 473.astar: -fb\_create fbdata(pass 1) -fb\_opt fbdata(pass 2) -Ofast  
 -m32  
 -L/spec/mpaton/6v1.0/amd512K8.lib/microquill/lib -lsmarheap  
 483.xalancbmk: -Ofast -m32 -OPT:unroll\_times\_max=8  
 -L/spec/mpaton/6v1.0/amd512K8.lib/microquill/lib -lsmarheap



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## Peak Other Flags

C benchmarks:

-IPA:max\_jobs=2

C++ benchmarks:

-IPA:max\_jobs=2

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.25.html](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.25.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/CPU2006\\_flags.20090714.25.xml](http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.25.xml)

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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