



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint®_rate2006 = 355

HP Integrity rx8640
(1.6GHz/24MB Dual-Core Intel Itanium 2)

SPECint_rate_base2006 = 336

CPU2006 license: 03

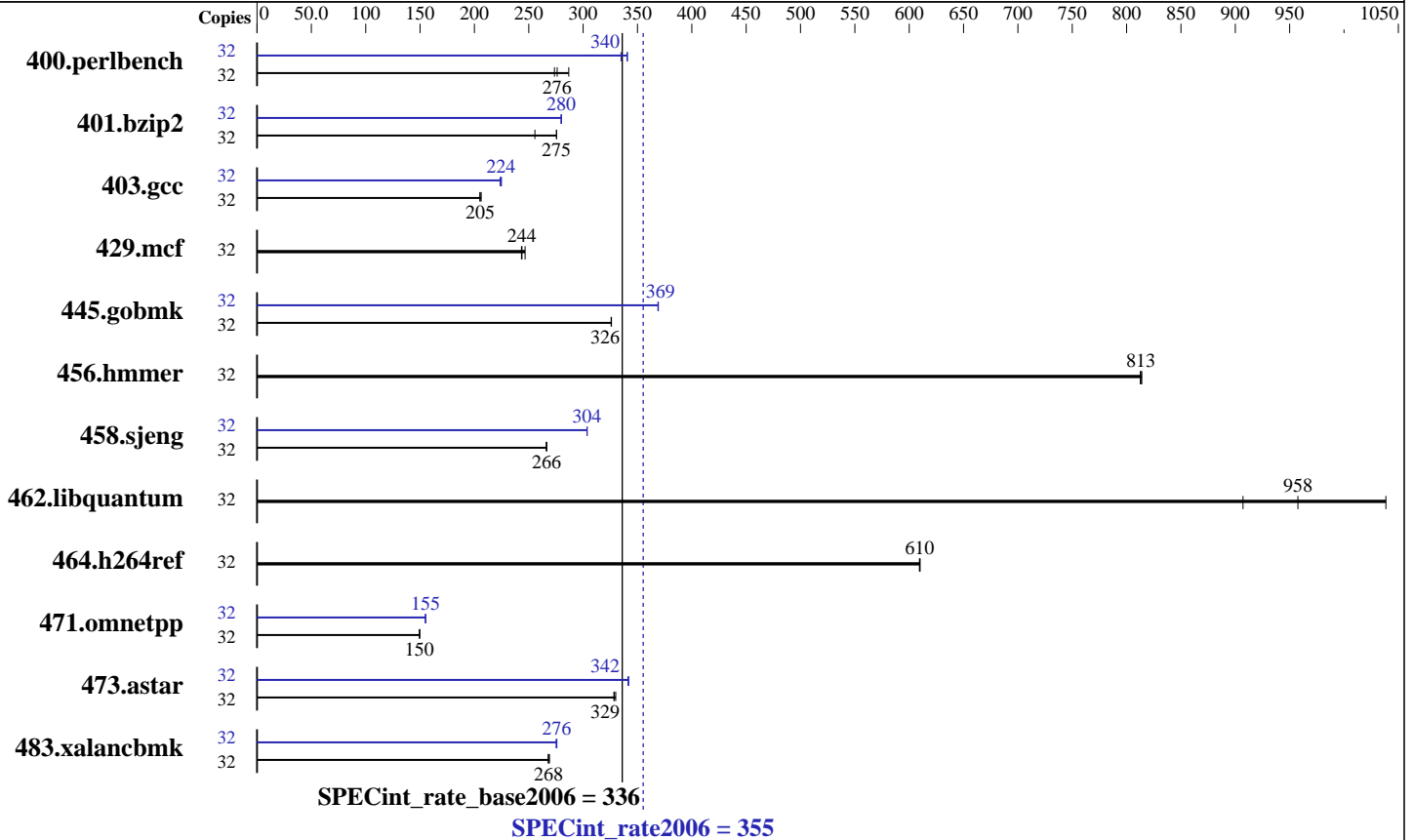
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Dec-2006

Hardware Availability: Sep-2006

Software Availability: Nov-2006



Hardware

CPU Name: Dual-Core Intel Itanium 2 9050
 CPU Characteristics: 1.6GHz/24MB, 533MHz FSB
 CPU MHz: 1600
 FPU: Integrated
 CPU(s) enabled: 32 cores, 16 chips, 2 cores/chip
 CPU(s) orderable: 1-16 chips
 Primary Cache: 16 KB I + 16 KB D on chip per core
 Secondary Cache: 1 MB I + 256 KB D on chip per core
 L3 Cache: 12 MB I+D on chip per core
 Other Cache: None
 Memory: 128 GB (64x2GB DIMMs)
 Disk Subsystem: 73GB 15K RPM SCSI
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux AS release 4 (Update 4)
 Compiler: Intel C++ Compiler 9.1 for Linux (Build 20061105)
 Auto Parallel: No
 File System: ext3
 System State: Multi-user
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other Software: MicroQuill Smartheap 8.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 355

HP Integrity rx8640
(1.6GHz/24MB Dual-Core Intel Itanium 2)

SPECint_rate_base2006 = 336

CPU2006 license: 03

Test date: Dec-2006

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	1142	274	<u>1132</u>	<u>276</u>	1090	287	32	917	341	934	335	<u>918</u>	<u>340</u>
401.bzip2	32	1120	276	1208	256	<u>1121</u>	<u>275</u>	32	1103	280	<u>1103</u>	<u>280</u>	1104	280
403.gcc	32	1256	205	<u>1254</u>	<u>205</u>	1249	206	32	<u>1148</u>	<u>224</u>	1145	225	1152	224
429.mcf	32	1200	243	<u>1197</u>	<u>244</u>	1183	247	32	1200	243	<u>1197</u>	<u>244</u>	1183	247
445.gobmk	32	1029	326	<u>1030</u>	<u>326</u>	1031	326	32	909	369	910	369	<u>910</u>	<u>369</u>
456.hammer	32	367	813	<u>367</u>	<u>813</u>	367	814	32	367	813	<u>367</u>	<u>813</u>	367	814
458.sjeng	32	1455	266	1453	267	<u>1454</u>	<u>266</u>	32	<u>1275</u>	<u>304</u>	1276	304	1275	304
462.libquantum	32	638	1040	<u>692</u>	<u>958</u>	731	907	32	638	1040	<u>692</u>	<u>958</u>	731	907
464.h264ref	32	1161	610	<u>1162</u>	<u>610</u>	1162	609	32	1161	610	<u>1162</u>	<u>610</u>	1162	609
471.omnetpp	32	1338	150	1337	150	<u>1337</u>	<u>150</u>	32	1289	155	1291	155	<u>1290</u>	<u>155</u>
473.astar	32	681	330	<u>683</u>	<u>329</u>	684	328	32	657	342	<u>657</u>	<u>342</u>	658	341
483.xalancbmk	32	821	269	<u>823</u>	<u>268</u>	825	268	32	802	275	800	276	<u>801</u>	<u>276</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

stacksize set to unlimited prior to run

Platform Notes

System was configured as a single partition with 4 cells and 4 processors (8 cores) per cell. Memory was configured as 100% cell local.

The following config file entry was used to bind processes to cores using the Linux "numactl" utility:
submit = let "MYNUM=\$SPECCOPYNUM" ; let "NODE=\$MYNUM/8" ; numactl --cpubind \$NODE --membind \$NODE \$command

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 355

HP Integrity rx8640
(1.6GHz/24MB Dual-Core Intel Itanium 2)

SPECint_rate_base2006 = 336

CPU2006 license: 03

Test date: Dec-2006

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_IA64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

-fast -IPF_fp_relaxed -ansi-alias

C++ benchmarks:

```

-fast -IPF_fp_relaxed -ansi-alias -Wl,-z,muldefs
/opt/SmartHeap_8/lib/libsmartheapC64.a
/opt/SmartHeap_8/lib/libsmartheap64.a

```

Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
-ansi-alias

```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 355

HP Integrity rx8640
(1.6GHz/24MB Dual-Core Intel Itanium 2)

SPECint_rate_base2006 = 336

CPU2006 license: 03

Test date: Dec-2006

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006

Peak Optimization Flags (Continued)

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmmer: basepeak = yes

458.sjeng: Same as 400.perlbench

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
-ansi-alias -Wl,-z,muldefs
/opt/SmartHeap_8/lib/libsmartheapC64.a
/opt/SmartHeap_8/lib/libsmartheap64.a

473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast -IPF_fp_relaxed
-ansi-alias -inline-factor=150 -Wl,-z,muldefs
/opt/SmartHeap_8/lib/libsmartheapC64.a
/opt/SmartHeap_8/lib/libsmartheap64.a

483.xalancbmk: Same as 471.omnetpp

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/IPF_intel91_flags.20090715.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/IPF_intel91_flags.20090715.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:56:24 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 January 2007.