



SPEC® CINT2006 Result

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Tyan

(Test Sponsor: Advanced Micro Devices)

SPECint®_rate2006 = Not Run

Tyan Thunder n4250QE (S4985) (AMD Opteron 2220 SE)

SPECint_rate_base2006 = 40.2

CPU2006 license: 49

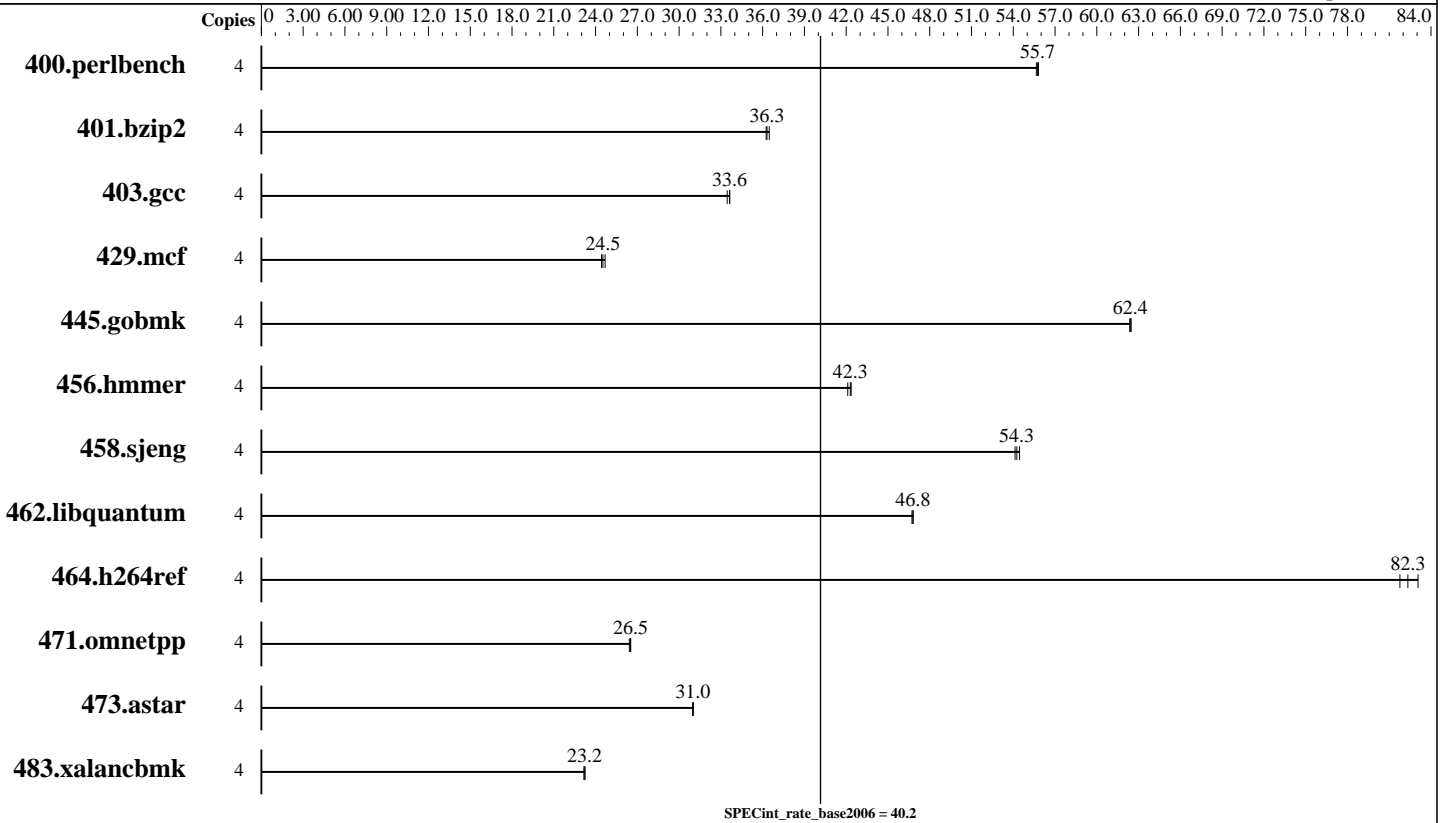
Test sponsor: Advanced Micro Devices

Tested by: Advanced Micro Devices

Test date: Sep-2006

Hardware Availability: Oct-2006

Software Availability: Apr-2006



Hardware

CPU Name: AMD Opteron 2220 SE
 CPU Characteristics:
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8 x 1 GB DDR2-667 CL5 ECC Reg)
 Disk Subsystem: 250GB SATA
 Other Hardware: None

Software

Operating System: SUSE Linux 10.1 (for x86_64)
 Compiler: SUSE gcc, g++ 4.1 (for x86_64)
 Auto Parallel: No
 File System: ext3
 System State: runlevel 3
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other Software: None



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	702	55.7	700	55.8	<u>701</u>	<u>55.7</u>							
401.bzip2	4	1058	36.5	1065	36.3	<u>1063</u>	<u>36.3</u>							
403.gcc	4	<u>958</u>	<u>33.6</u>	962	33.5	957	33.6							
429.mcf	4	<u>1487</u>	<u>24.5</u>	1478	24.7	1493	24.4							
445.gobmk	4	672	62.5	<u>672</u>	<u>62.4</u>	673	62.4							
456.hmmer	4	<u>882</u>	<u>42.3</u>	881	42.4	886	42.1							
458.sjeng	4	<u>892</u>	<u>54.3</u>	894	54.1	889	54.4							
462.libquantum	4	1774	46.7	1771	46.8	<u>1771</u>	<u>46.8</u>							
464.h264ref	4	<u>1075</u>	<u>82.3</u>	1066	83.1	1083	81.8							
471.omnetpp	4	<u>943</u>	<u>26.5</u>	943	26.5	946	26.4							
473.astar	4	907	31.0	905	31.0	<u>906</u>	<u>31.0</u>							
483.xalancbmk	4	1188	23.2	<u>1188</u>	<u>23.2</u>	1192	23.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested by Advanced Micro Devices

The system can be assembled using a Zippy 700W EPS12V power supply and any SSI-MEB case
All memory slots filled on all chips.

Other Configuration Notes

Taskset utility used to bind process to cores

Base Compiler Invocation

C benchmarks:

gcc

C++ benchmarks:

g++

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-O3 -fno-inline-functions -funroll-loops

C++ benchmarks:

-O3 -funroll-loops

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.04.xml

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For other inquiries, please contact webmaster@spec.org.

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