



# SPEC® CINT2006 Result

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## Intel Corporation

Intel DG965WH motherboard  
(2.93 GHz, Intel Core 2 Extreme processor  
X6800)

### SPECint®\_rate2006 = Not Run

### SPECint\_rate\_base2006 = 31.1

CPU2006 license: 13

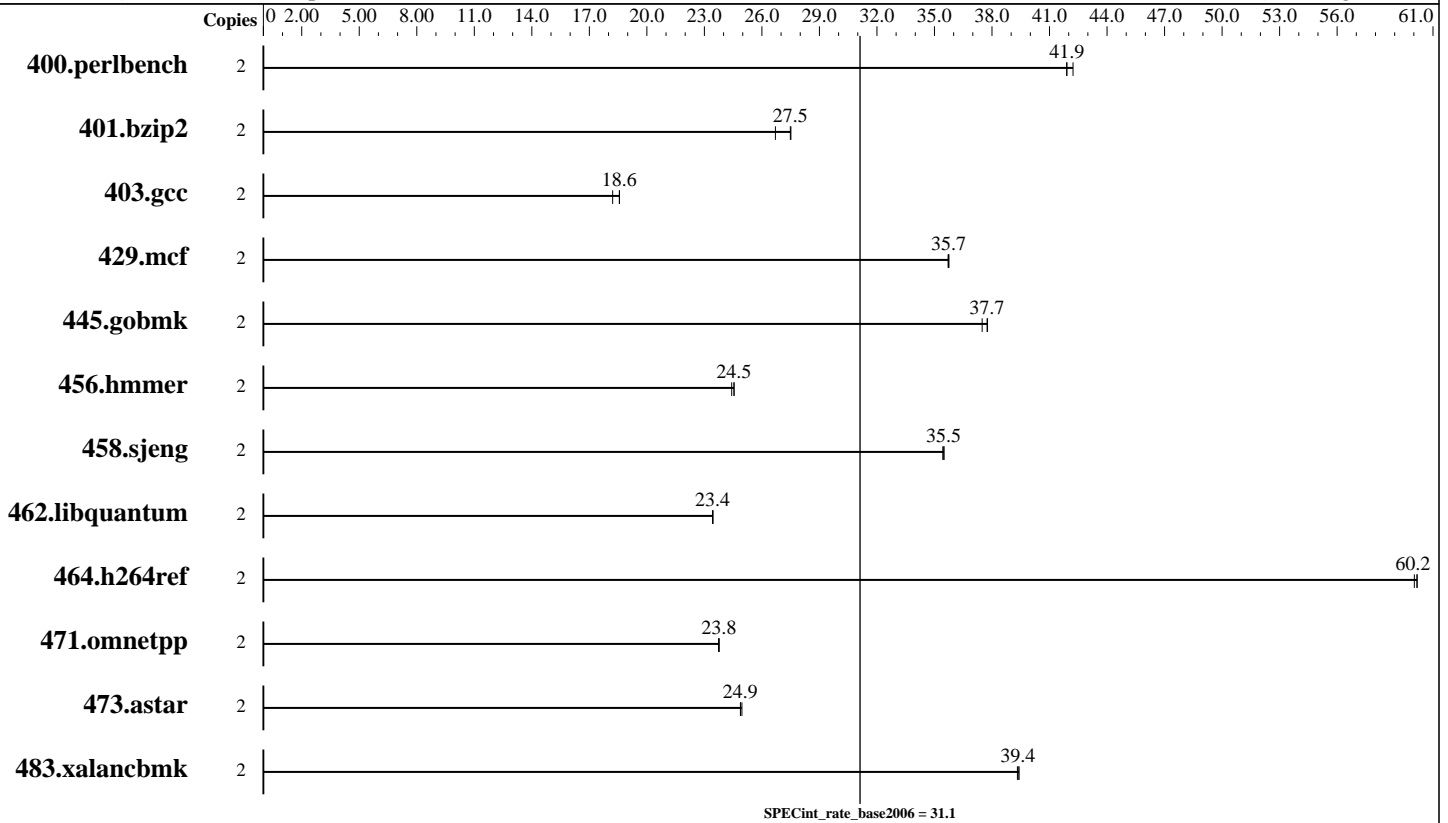
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2006

Hardware Availability: Jul-2006

Software Availability: Aug-2006



### Hardware

CPU Name: Intel Core 2 Extreme X6800  
 CPU Characteristics: 2.93 GHz, 1066 MHz bus  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 2 GB (2 1GB Corsair TWIN2X2048-8500C5 DDR2 1066 CL5-5-5)  
 Disk Subsystem: Maxtor DiamondMax 10 6B300S0 300GB NCQ Serial ATA (7200 RPM, 16MB cache)  
 Other Hardware: None

### Software

Operating System: Windows XP Professional SP2 32-bit  
 Compiler: Intel C++ Compiler 9.1 for IA32  
 Build 20060816Z  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 SmartHeap Library Version 8.0 from  
<http://www.microquill.com/>  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: None



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## Results Table

| Benchmark      | Base   |             |             |            |             |            |             | Peak   |         |       |         |       |         |       |
|----------------|--------|-------------|-------------|------------|-------------|------------|-------------|--------|---------|-------|---------|-------|---------|-------|
|                | Copies | Seconds     | Ratio       | Seconds    | Ratio       | Seconds    | Ratio       | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench  | 2      | 466         | 41.9        | <b>466</b> | <b>41.9</b> | 463        | 42.2        |        |         |       |         |       |         |       |
| 401.bzip2      | 2      | 723         | 26.7        | <b>702</b> | <b>27.5</b> | 702        | 27.5        |        |         |       |         |       |         |       |
| 403.gcc        | 2      | 867         | 18.6        | <b>867</b> | <b>18.6</b> | 884        | 18.2        |        |         |       |         |       |         |       |
| 429.mcf        | 2      | 511         | 35.7        | <b>510</b> | <b>35.7</b> | 510        | 35.8        |        |         |       |         |       |         |       |
| 445.gobmk      | 2      | 560         | 37.5        | <b>556</b> | <b>37.7</b> | 556        | 37.8        |        |         |       |         |       |         |       |
| 456.hammer     | 2      | 764         | 24.4        | 760        | 24.5        | <b>760</b> | <b>24.5</b> |        |         |       |         |       |         |       |
| 458.sjeng      | 2      | <b>682</b>  | <b>35.5</b> | 683        | 35.4        | 682        | 35.5        |        |         |       |         |       |         |       |
| 462.libquantum | 2      | <b>1768</b> | <b>23.4</b> | 1768       | 23.4        | 1768       | 23.4        |        |         |       |         |       |         |       |
| 464.h264ref    | 2      | 737         | 60.0        | <b>736</b> | <b>60.2</b> | 736        | 60.2        |        |         |       |         |       |         |       |
| 471.omnetpp    | 2      | 526         | 23.8        | <b>526</b> | <b>23.8</b> | 527        | 23.7        |        |         |       |         |       |         |       |
| 473.astar      | 2      | 563         | 25.0        | 564        | 24.9        | <b>564</b> | <b>24.9</b> |        |         |       |         |       |         |       |
| 483.xalancbmk  | 2      | 350         | 39.4        | 351        | 39.3        | <b>351</b> | <b>39.4</b> |        |         |       |         |       |         |       |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Memory timings set manually to DDR2-800 5-5-5-15 in the bios

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99

C++ benchmarks:  
icl -Qvc7.1

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32  
464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

## Base Optimization Flags

C benchmarks:  
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:  
-fast -Qcxx\_features /F512000000 shlw32m.lib  
-link /FORCE:MULTIPLE



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.03.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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