

# Large Hadron Collider

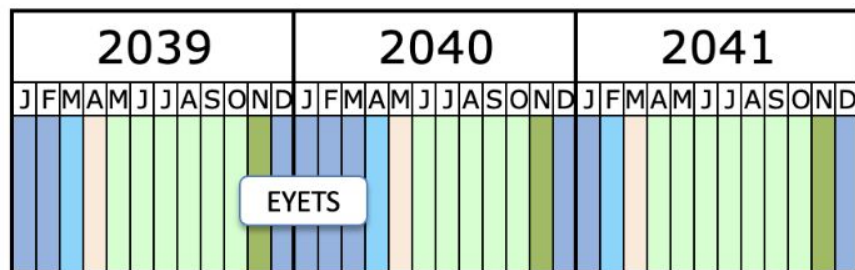
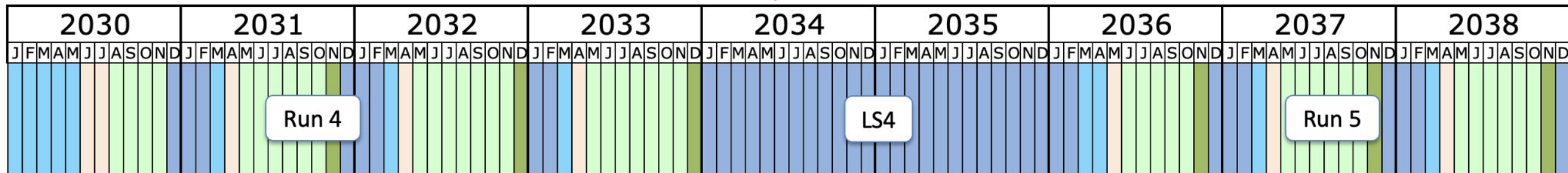
*the world's largest and most powerful particle accelerator*





# Large Hadron Collider

*the world's largest and most powerful particle accelerator*



- Shutdown/Technical stop
- Protons physics
- Ions (tbc after LS4)
- Commissioning with beam
- Hardware commissioning

Image from [here](#)

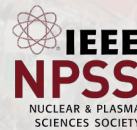


# The ATLAS ITk Pixel Detector: A great adventure from design to construction

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2024, Oct 31

2024 IEEE NSS MIC RTSD in Tampa, Florida



75<sup>TH</sup>  
Anniversary

[https://www.eventclass.org/contxt\\_ieee2024/scientific/online-program/session?s=N-27#e163](https://www.eventclass.org/contxt_ieee2024/scientific/online-program/session?s=N-27#e163)



Kim Doyeong 김도영

Argonne National Laboratory



**GOAL of the HL-LHC:**  
to increase the integrated luminosity by a factor of 10 beyond the LHC's design value

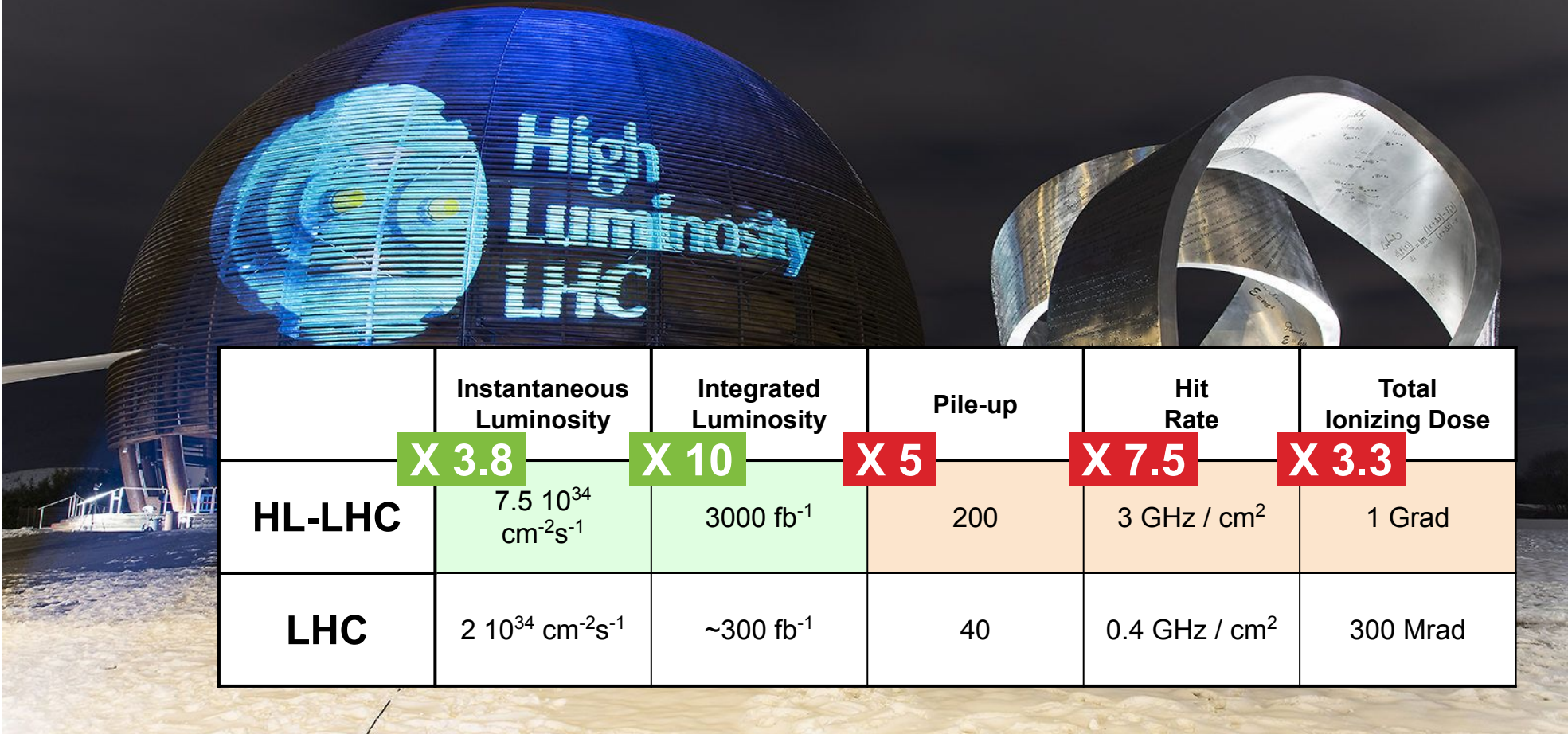
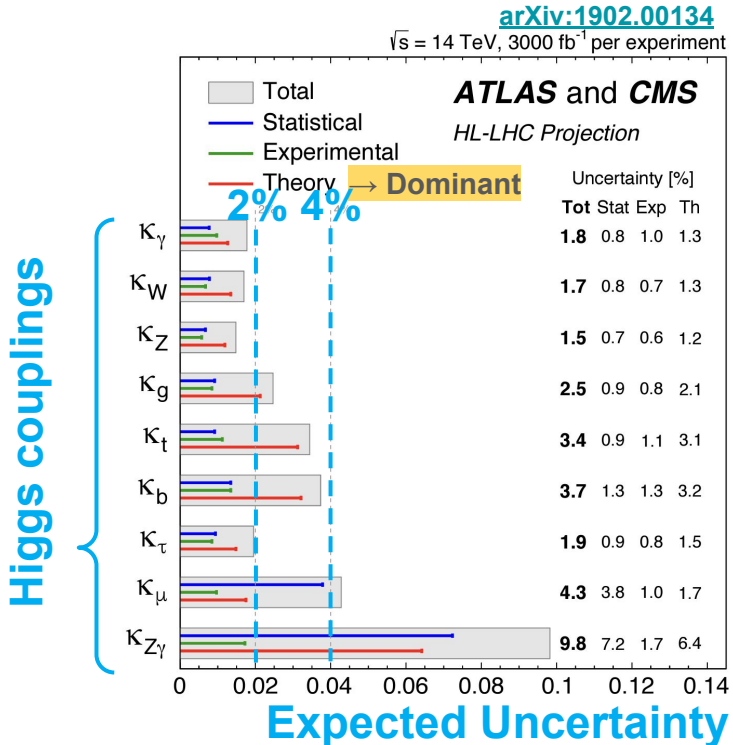


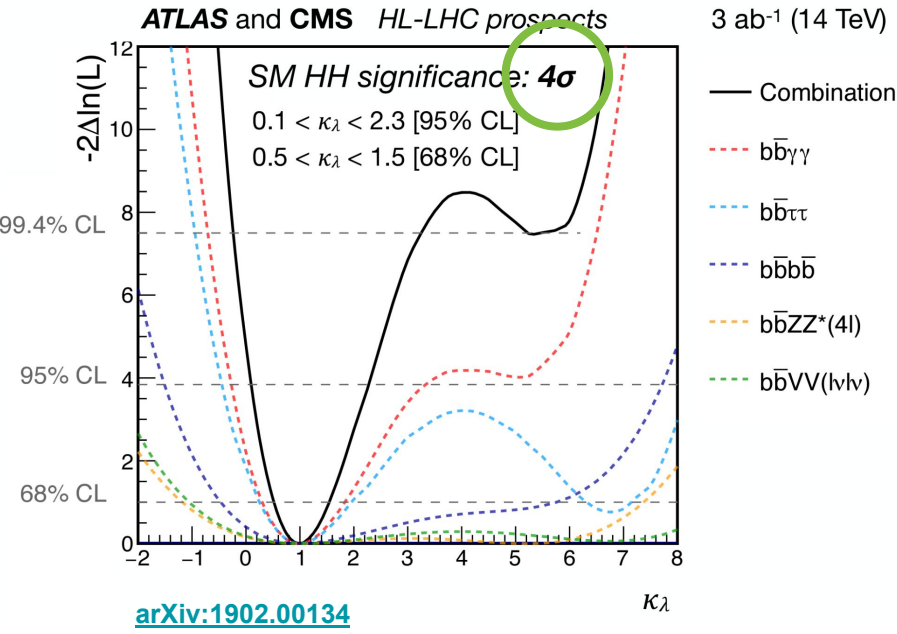
Photo by Maximilien Brice, CERN



- In all aspects, the upgraded tracker (ITk) at least matches—and in most cases surpasses—the performance achieved in LHC Run 2 (see Ewa Stanecka’s talk)
- This cutting-edge tracker will push the boundaries of precision and sensitivity, paving the way for exciting physics opportunities with future colliders



the Higgs couplings to SM particles is expected to reach the percent level

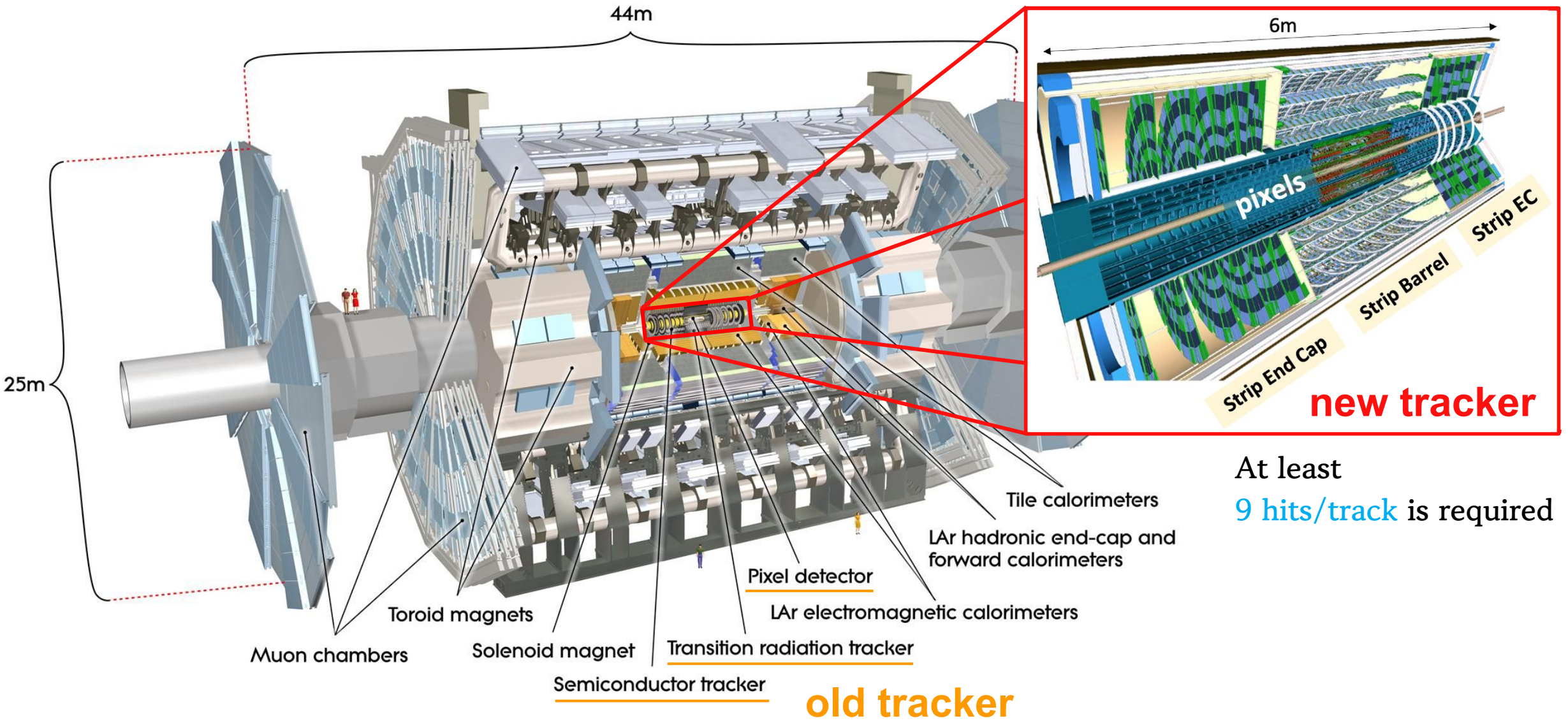


Combined signal strength significance  
 $\sim 4\sigma$  (stat. + syst.)

+ and many more  
 precision SM measurements  
 direct searches  
 ...



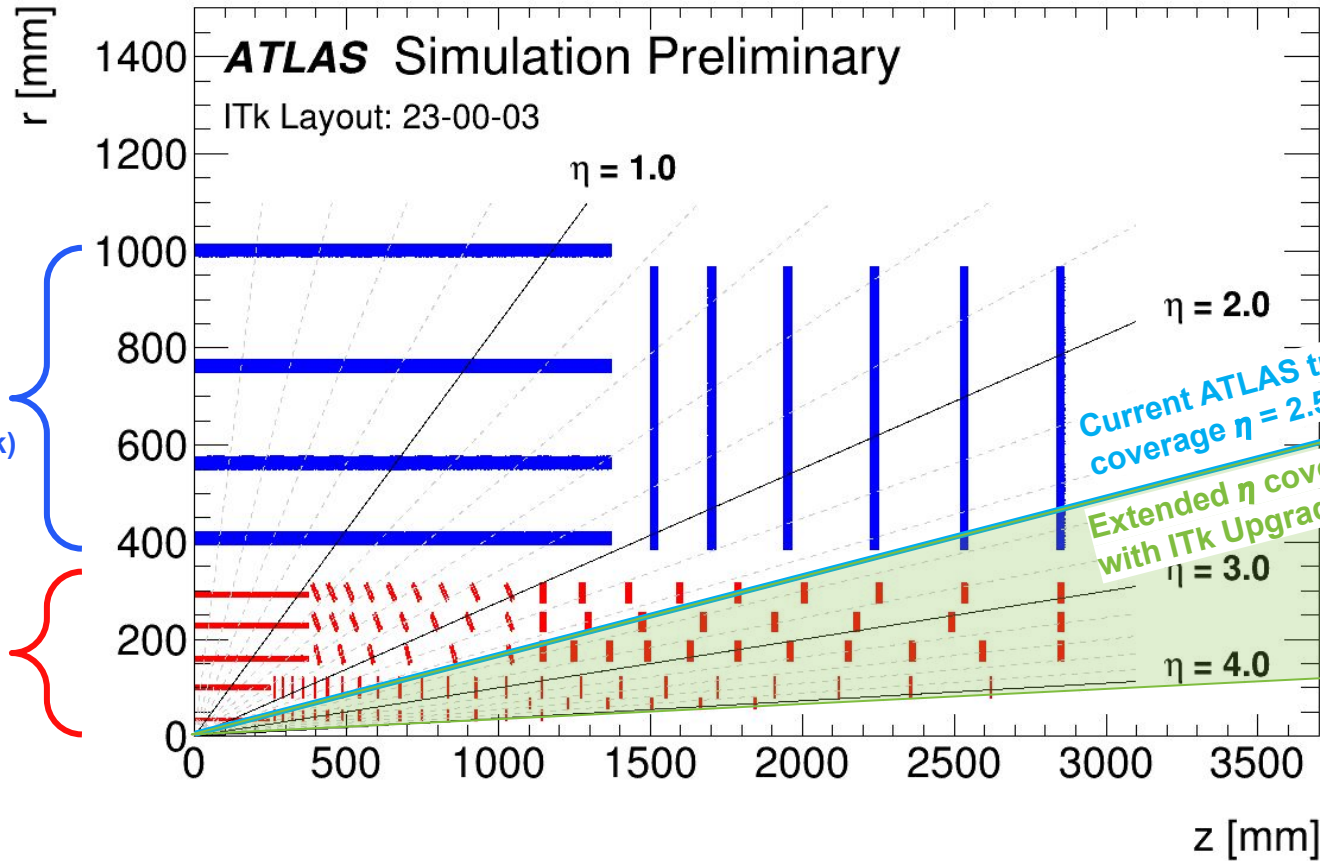
[ATLAS Open Data](#)





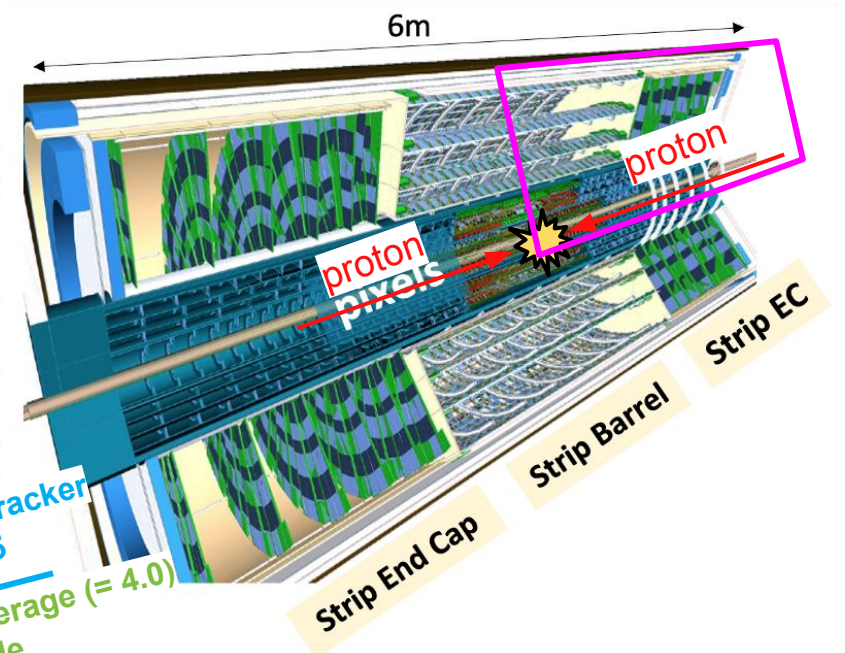
\* Only one quadrant is shown

ATL-PHYS-PUB-2021-024



strip  
(Ewa Stanecka's talk)

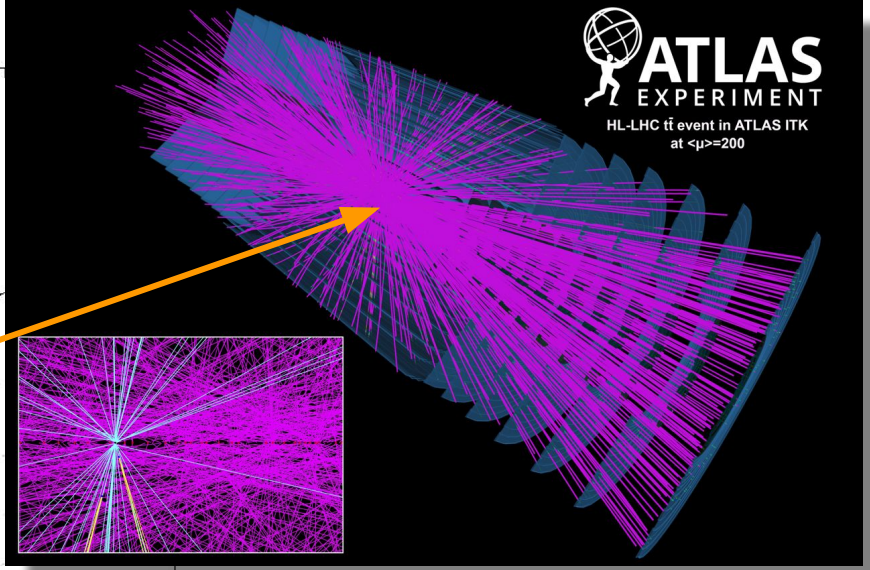
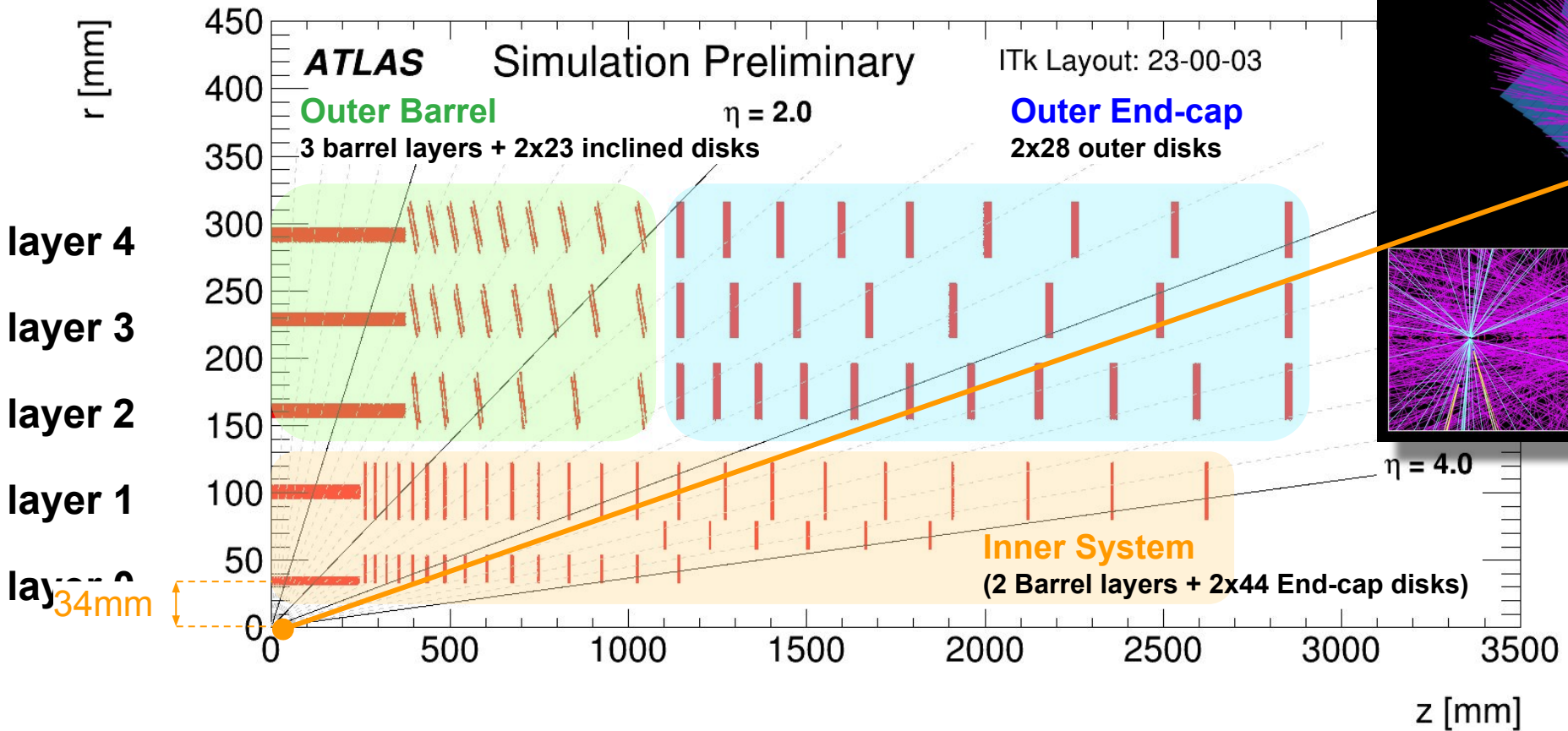
pixel  
(Focus of this talk)



HL-LHC upgrade will involve current ATLAS tracker being replaced by all-silicon Inner Tracker (ITk)



[ATLAS public briefings](#)

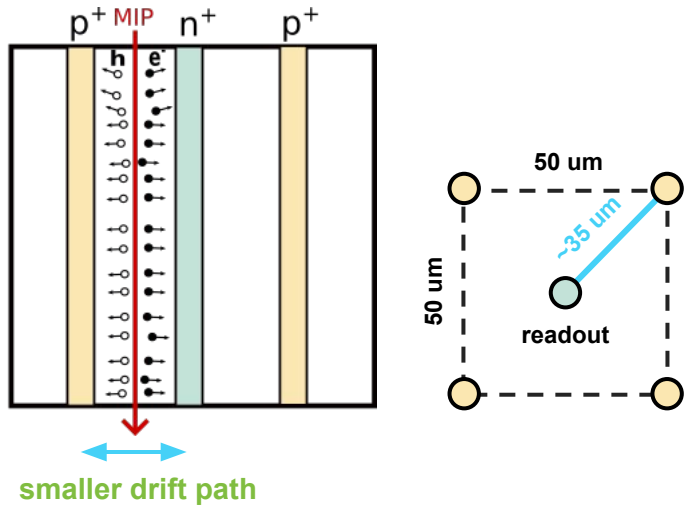


Higher Track Density  
Higher Radiation Levels

- The geometric structure of ITk creates non-uniform challenges
- The ITk Pixel Inner System (= layer 0 + layer 1) is located nearest to the collision point, resulting in the most demanding requirements

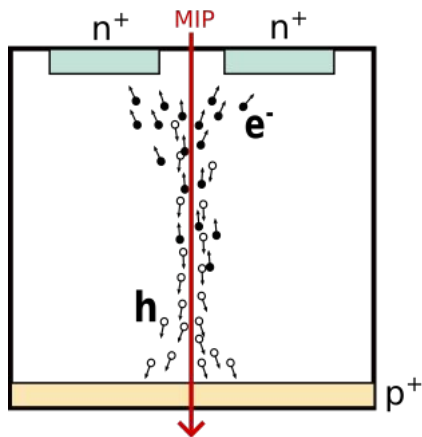


## Columnar 3D sensors (layer 0)



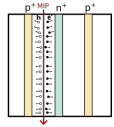
- **Smaller drift path independently of the sensor thickness**
    - smaller depletion voltage
    - less charge trapping
    - improved position resolution
  - **Technical complexity**
    - higher cost & limited yield
- lower power dissipation after irradiation

## Planar sensors (other layers)

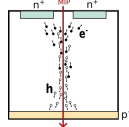


- **n-in-p wafer single side processed**
  - Simpler (i.e. cheaper) fabrication
- Radiation tolerance can be improved by reducing the thickness
  - **layer 1** (100um) **Required radiation tolerance: 3.2 MGy for integrated luminosity of 2000 fb<sup>-1</sup>**
  - **other layers** (150um) - higher yield **Required radiation tolerance: 3.5 MGy for integrated luminosity of 4000 fb<sup>-1</sup>**





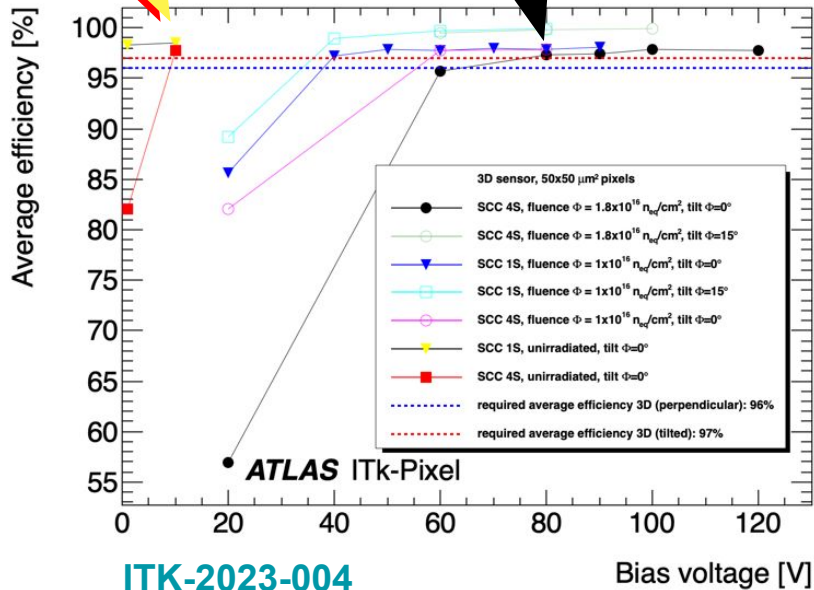
**Columnar 3D sensors**



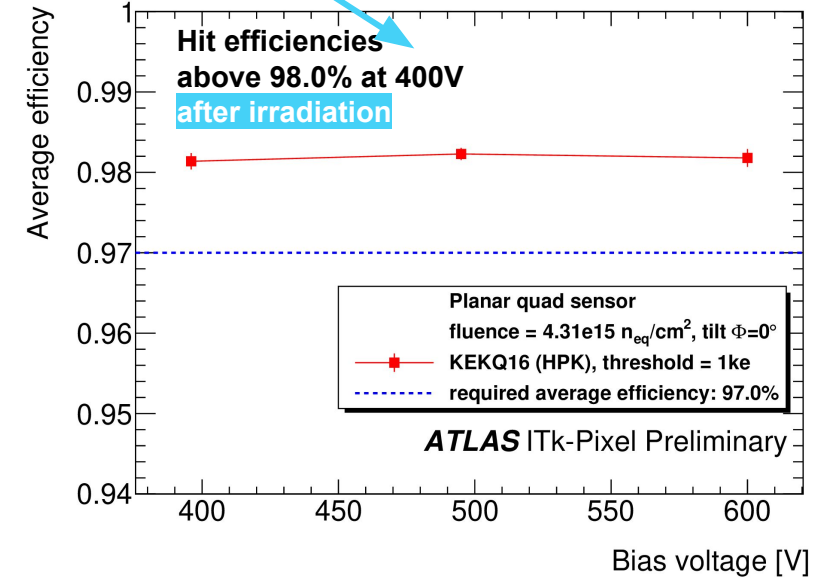
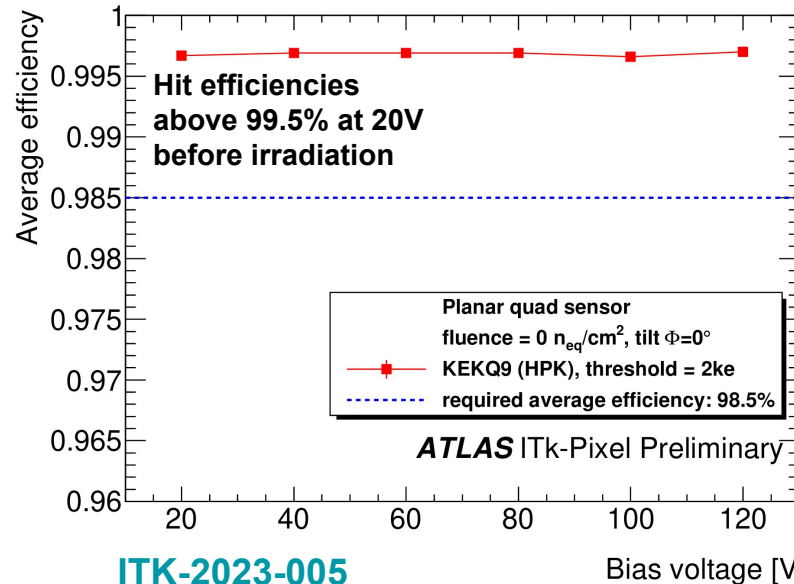
**Planar sensors**

Hit efficiencies above 97% at 10V before irradiation

Hit efficiencies above 97% at 80V after irradiation  
Irradiated at KIT ( $1.8 \times 10^{16} \text{ n}_{\text{eq}}/\text{cm}^2$  s 23 MeV protons)



*3D sensor shows better radiation tolerance*



Both sensor types meet the required hit efficiency (>97%) after irradiation

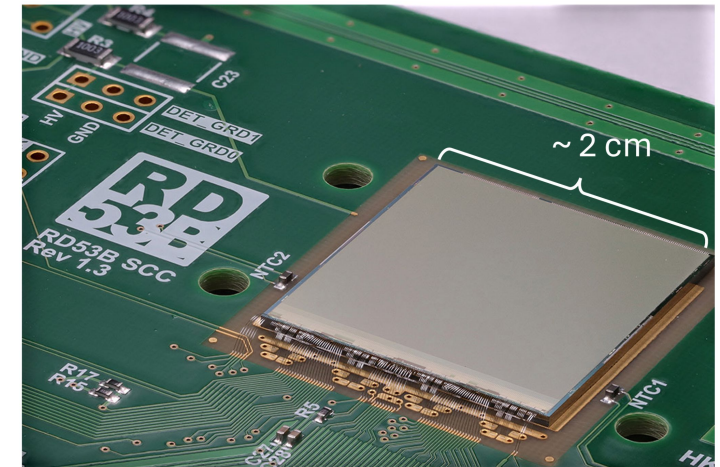


Unlike sensor, the same chip will be used everywhere in the detector

→ The design must respect the most severe requirement (inner system, central region)

## Technical requirements

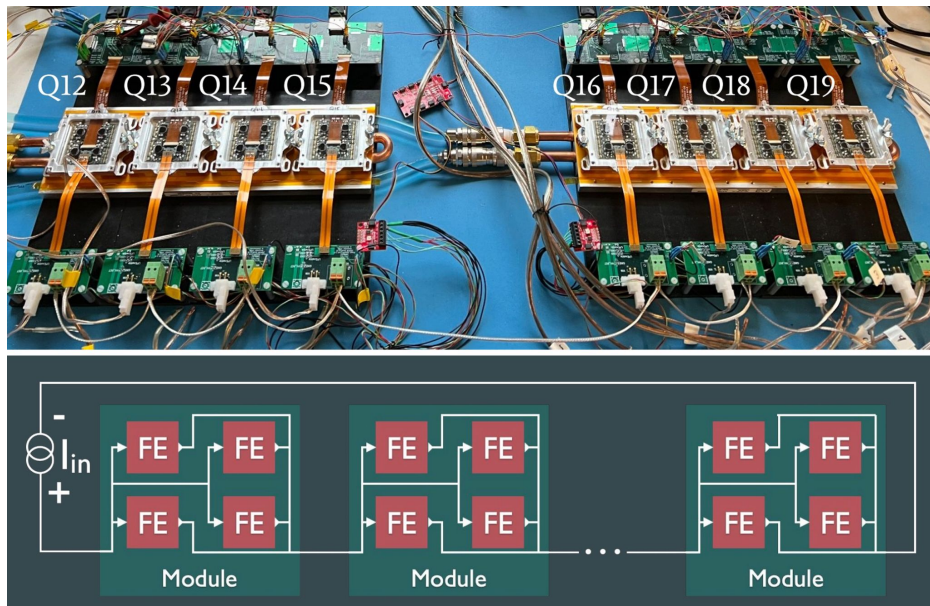
	New tracker FE chip	Current tracker FE chip
<b>Chip Size</b>	2 x 2 cm <sup>2</sup>	2 x 2 cm <sup>2</sup>
<b>Pixel Size</b>	50 x 50 μm <sup>2</sup>	50 x 250 μm <sup>2</sup>
<b>Pixel Hit Rate</b>	3 GHz / cm <sup>2</sup>	400 MHz / cm <sup>2</sup>
<b>Trigger Rate</b>	1 MHz	200 kHz
<b>Trigger Latency</b>	12.8 μs	6.4 μs
<b>Current Consumption</b>	< 8 μA / pixel	20 μA / pixel
<b>Radiation Tolerance</b>	0.5 - 1 Grad	300 Mrad
<b>Min. stable Threshold</b>	600 e	1500 e



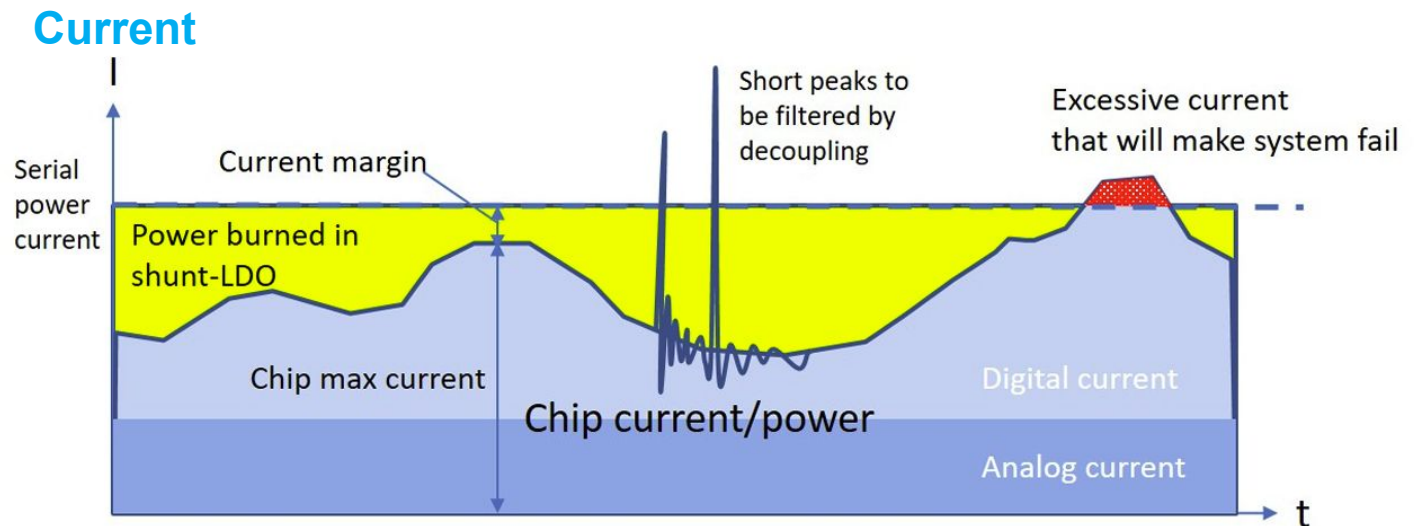
- Developed by [RD53](#) collaboration (ATLAS & CMS joint effort)
- 65 nm CMOS\* technology
  - low power (< 5 μW/pixel)
  - high logic density

The Shunt LDO\* power regulator (SLDO) provides constant current operation with multiple chips (and also modules) connected in parallel

- material reduction (cables)
- constant power consumption (important for mechanical stability)



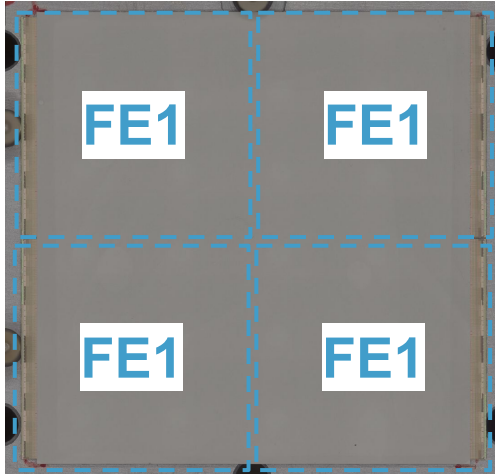
**Serial powering chain with quad modules**





## Bare Module

= sensor + 4 front-end chips  
bump bonded

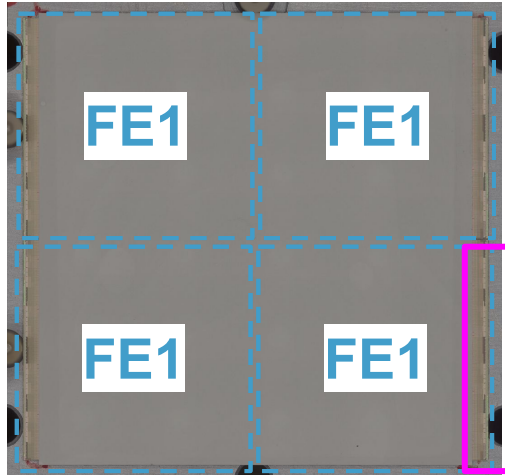


**Key challenges:** unprecedented bump density for ITk exceeds industry norms

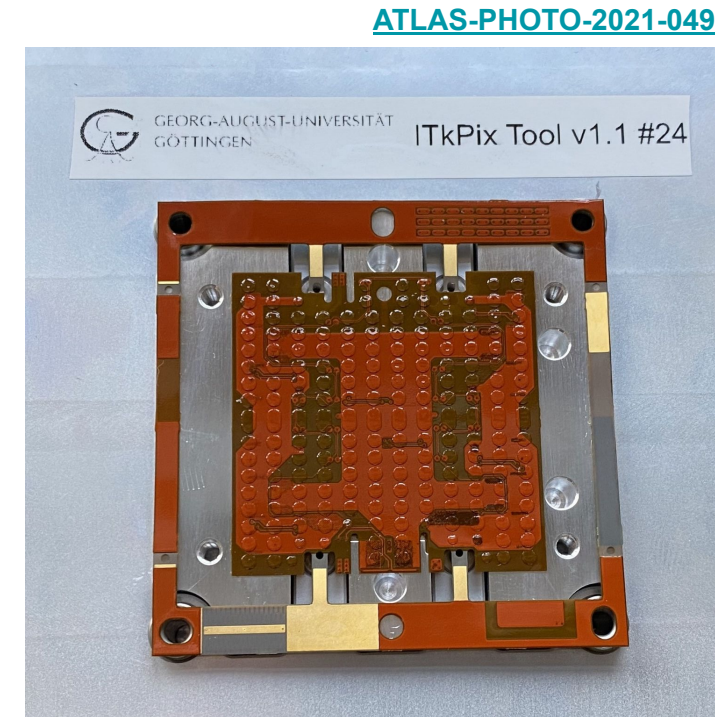
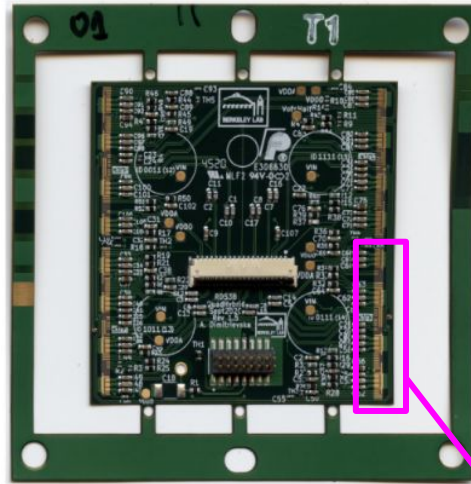
- Reduced sensor thickness: 100-150  $\mu\text{m}$  (vs. 250  $\mu\text{m}$  in current ATLAS Pixel)
- Thinner FE chip: 150  $\mu\text{m}$  (vs. 190  $\mu\text{m}$  in current ATLAS Pixel)
- 5x increase in bump density due to smaller pixel pitch (50  $\mu\text{m}$  x 50  $\mu\text{m}$  vs. 50  $\mu\text{m}$  x 250  $\mu\text{m}$ )
- 440 columns x 384 rows = **153,600 bump bonds / chip**

## Bare Module \*

= sensor + 4 front-end chips  
bump bonded



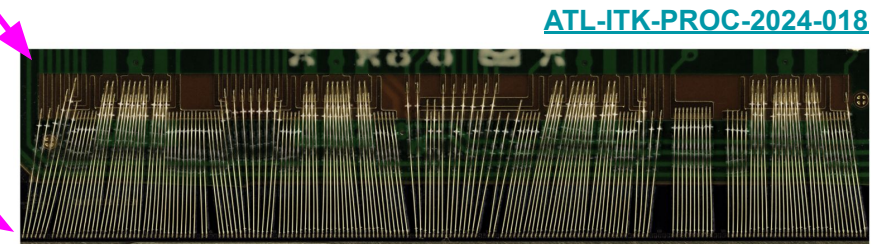
## Flex PCB \*



*Flex PCB with a pattern of glue dots before attaching it to the chip-sensor assembly*

Flex PCB glued to the backside of the sensor tile then wirebonded to the front-end chips

→ Traces that carries commands/data/power interfaced with connectors to service

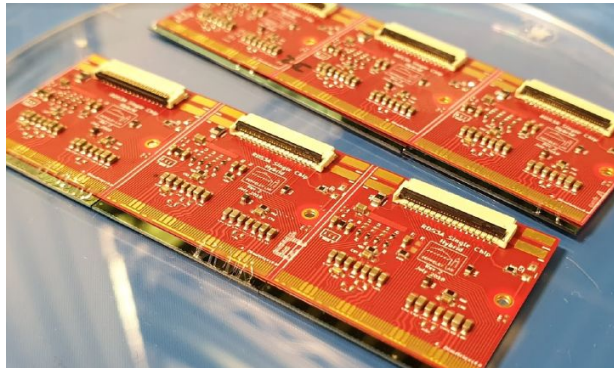


*Wire bondings for one front-end chip*

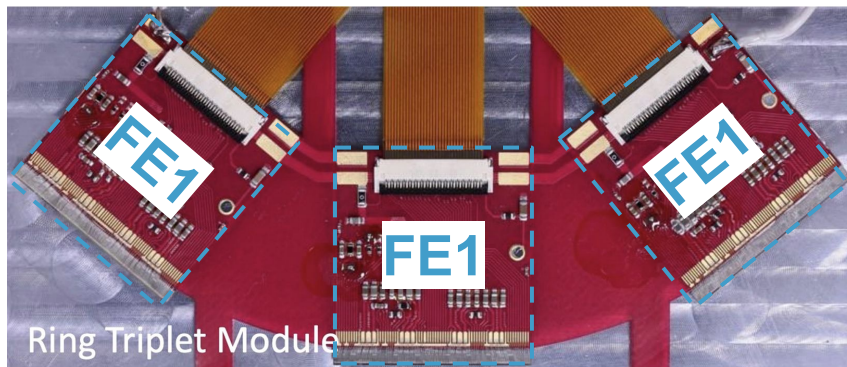
\* Bare module and Flex PCB are provided by industry



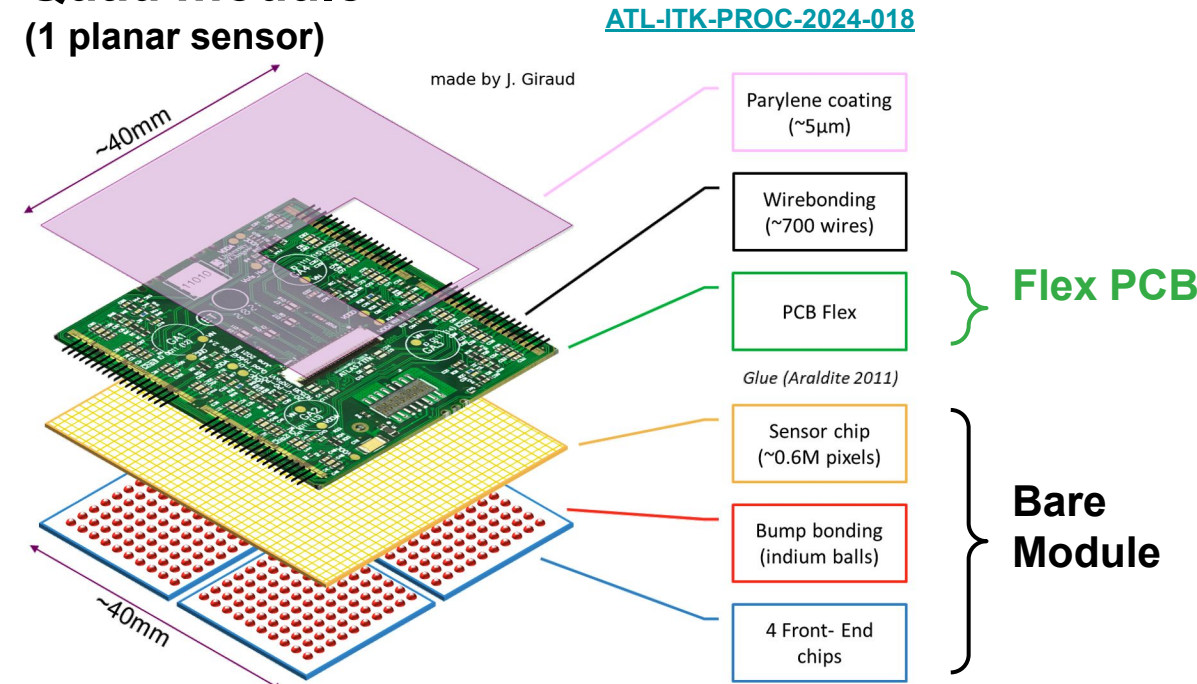
## Barrel triplet module (3 x 3D sensor)



## Ring triplet module (3 x 3D sensor)



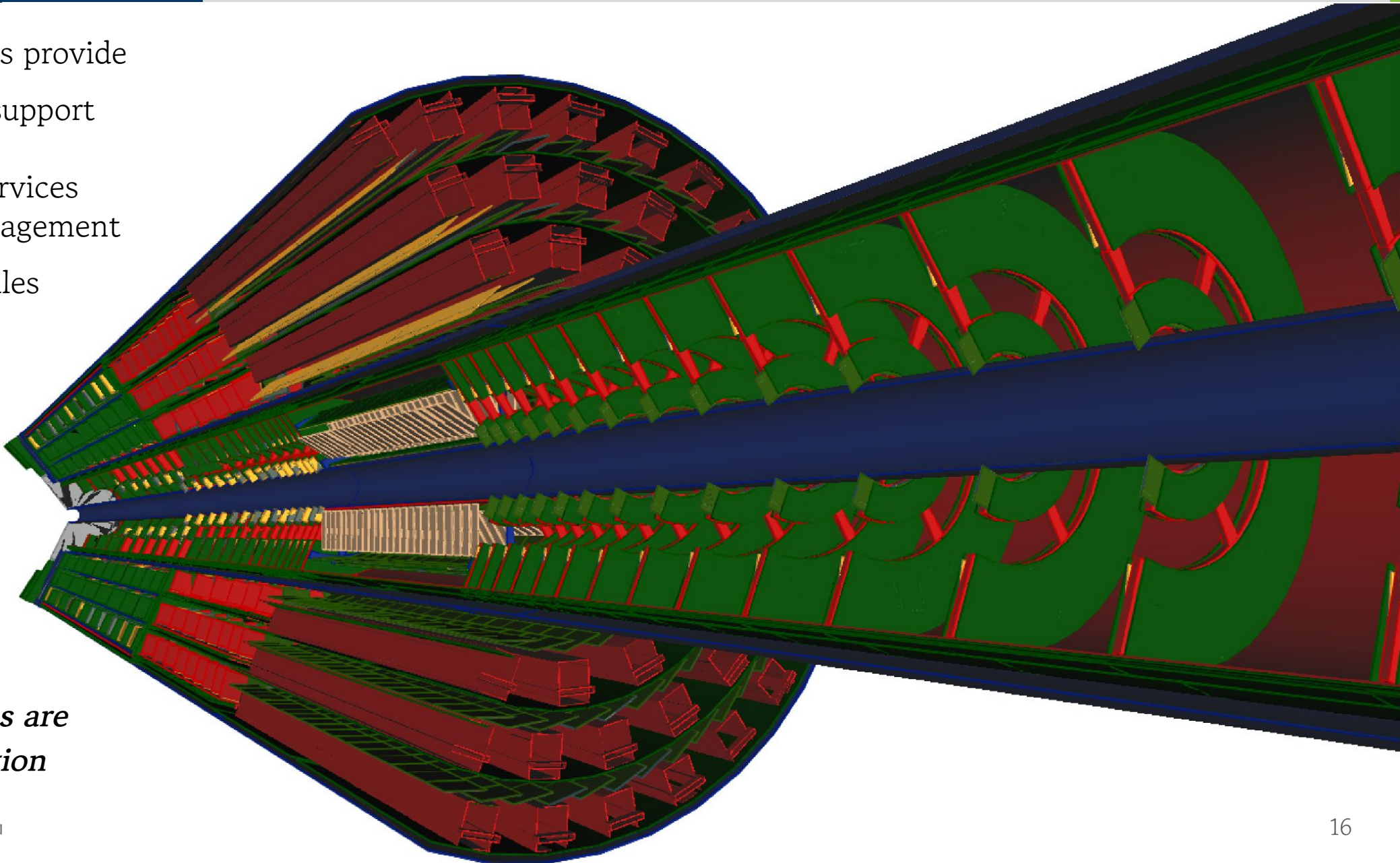
## Quad module (1 planar sensor)



The local supports provide

- mechanical support
- alignment
- routing of services
- thermal management

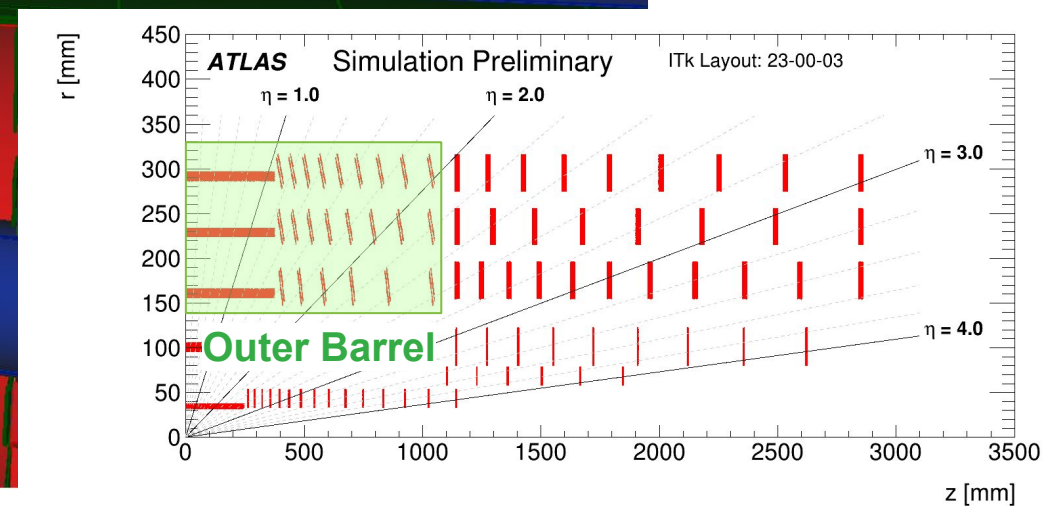
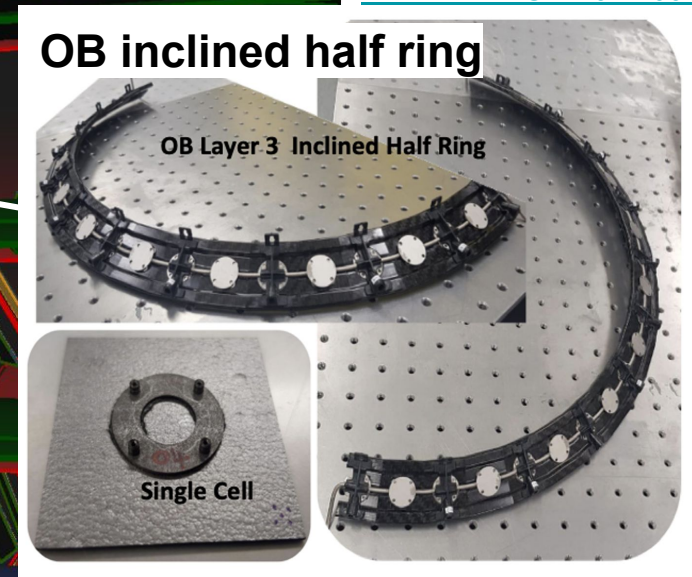
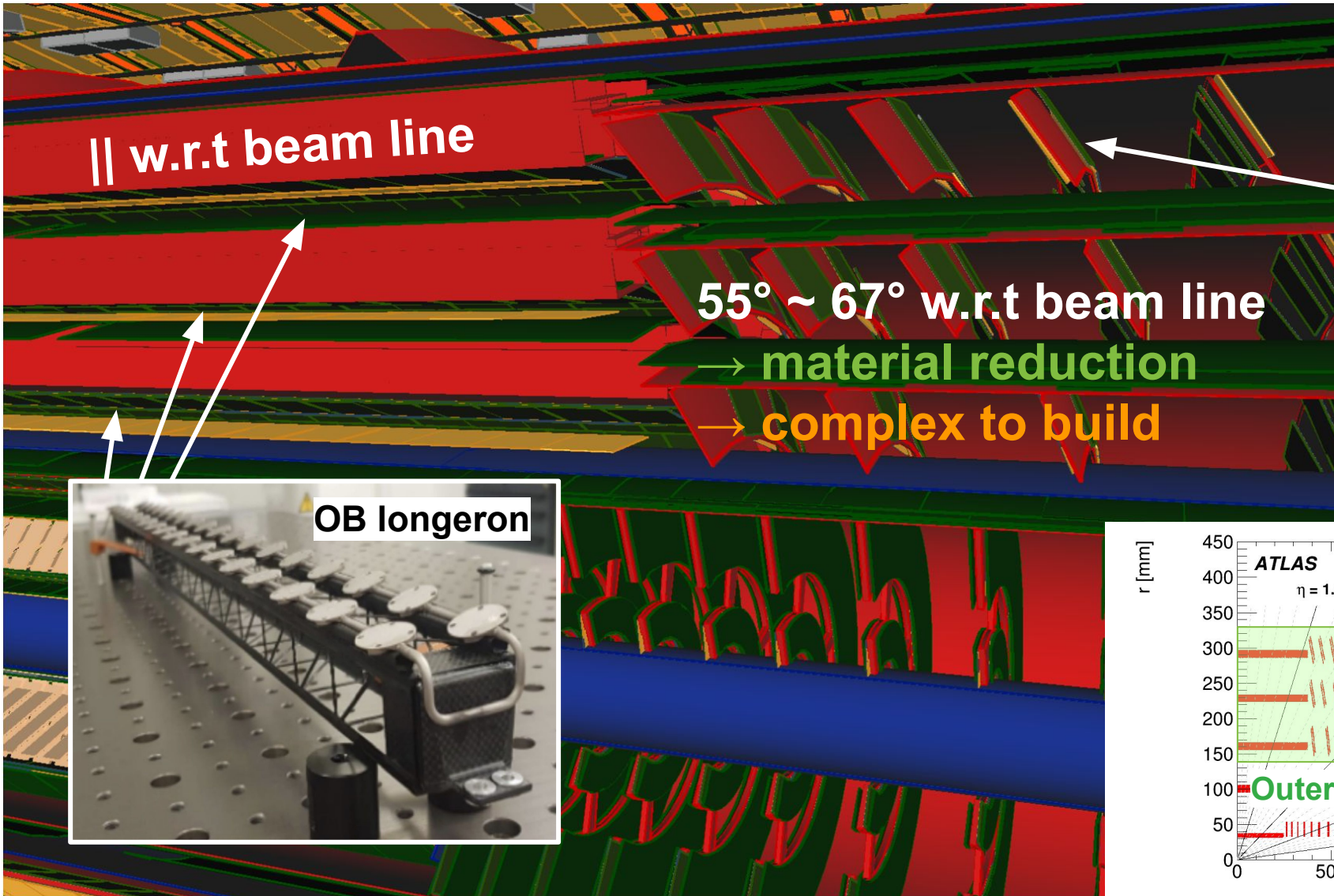
of the pixel modules



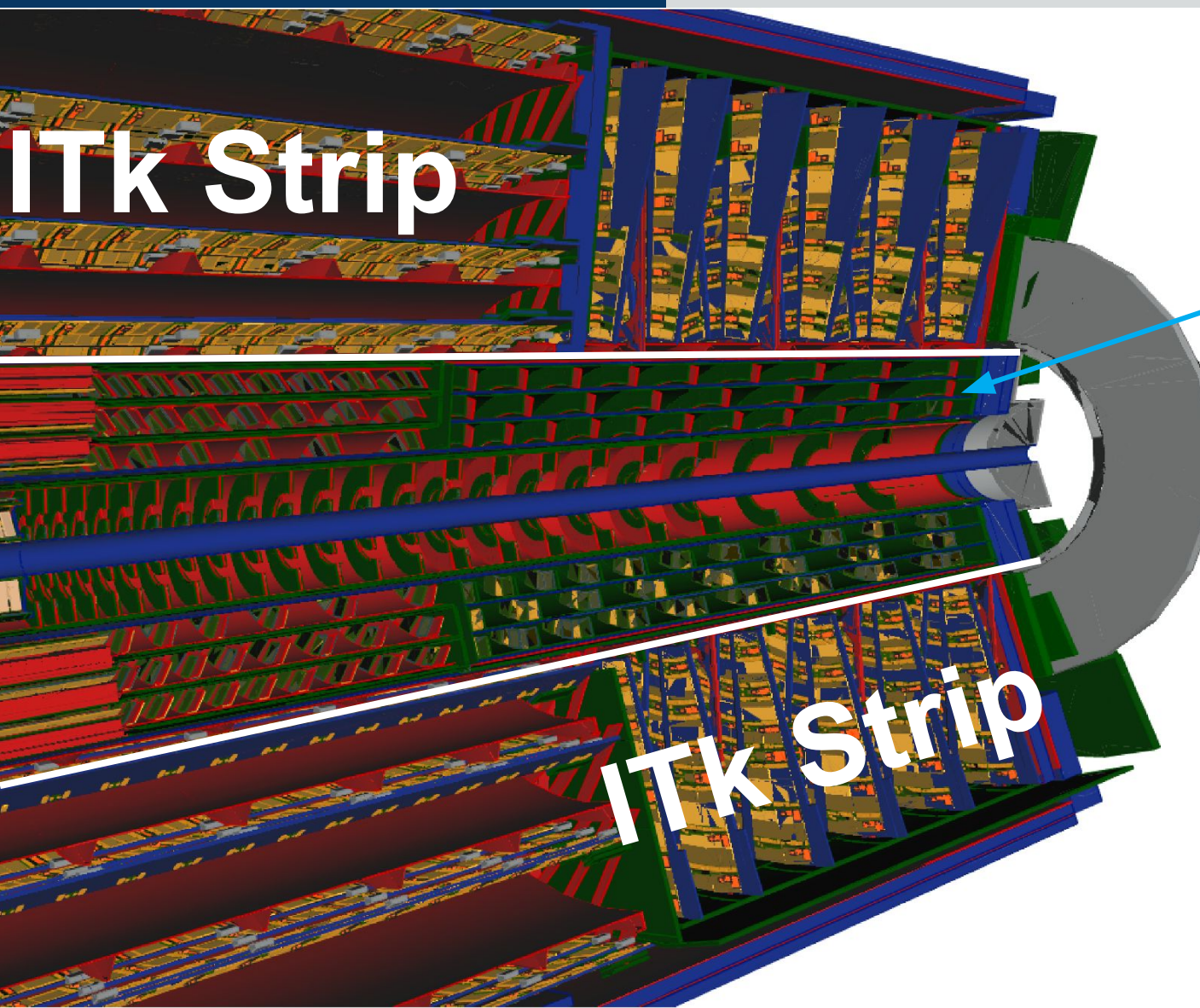
*Support structures are specialized by region*



ATL-ITK-PROC-2021-004



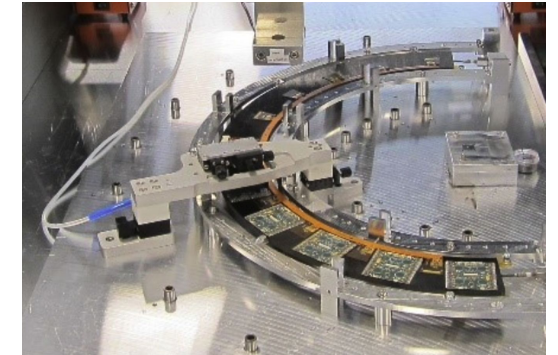




[ATL-ITK-PROC-2023-003](#)

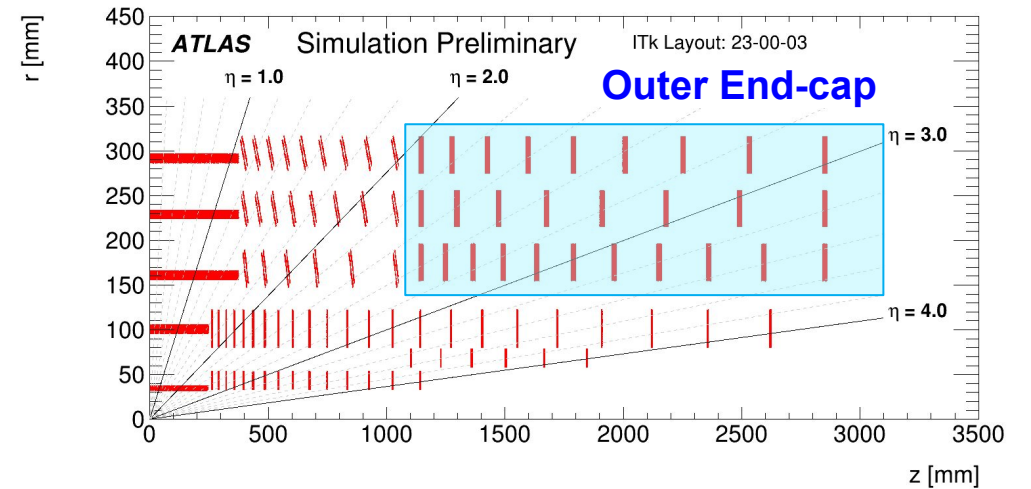


OEC half-ring

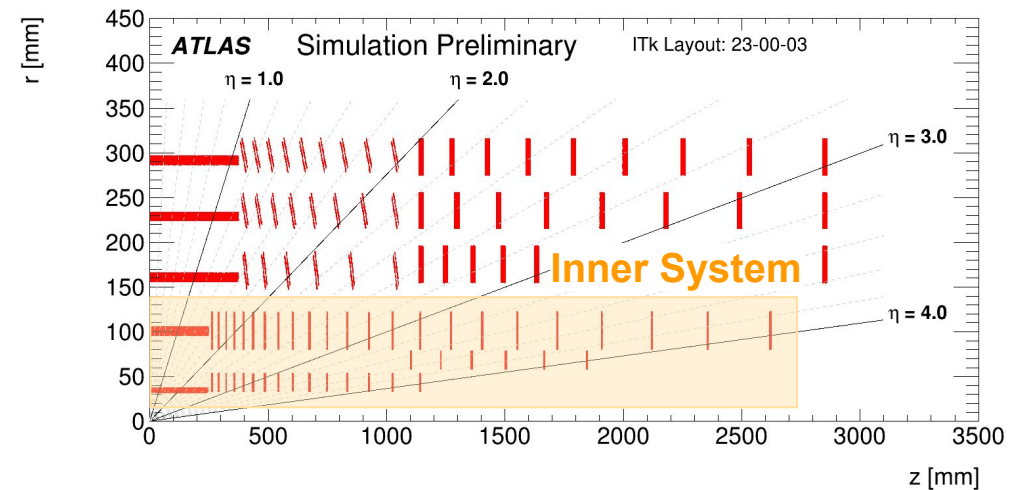
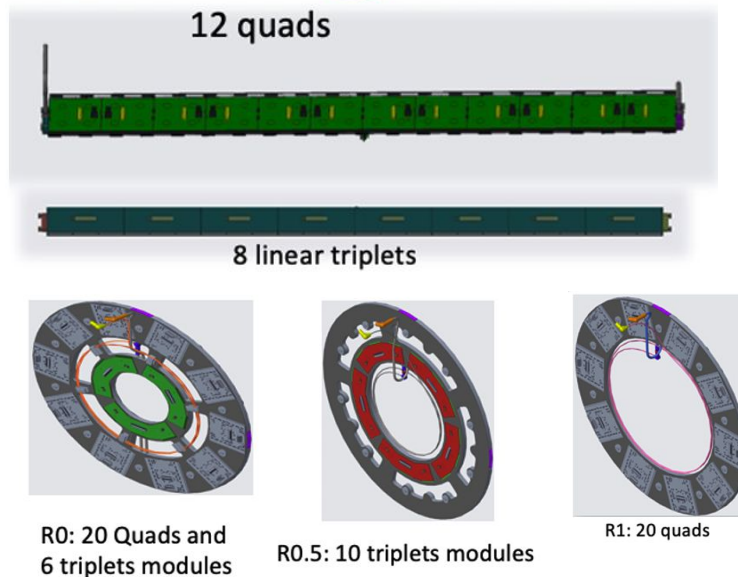
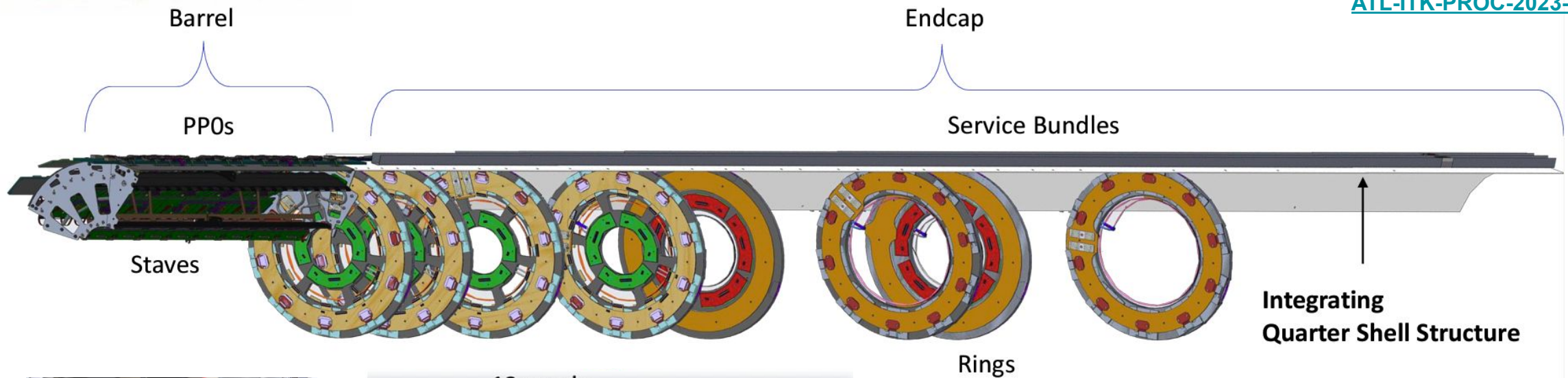


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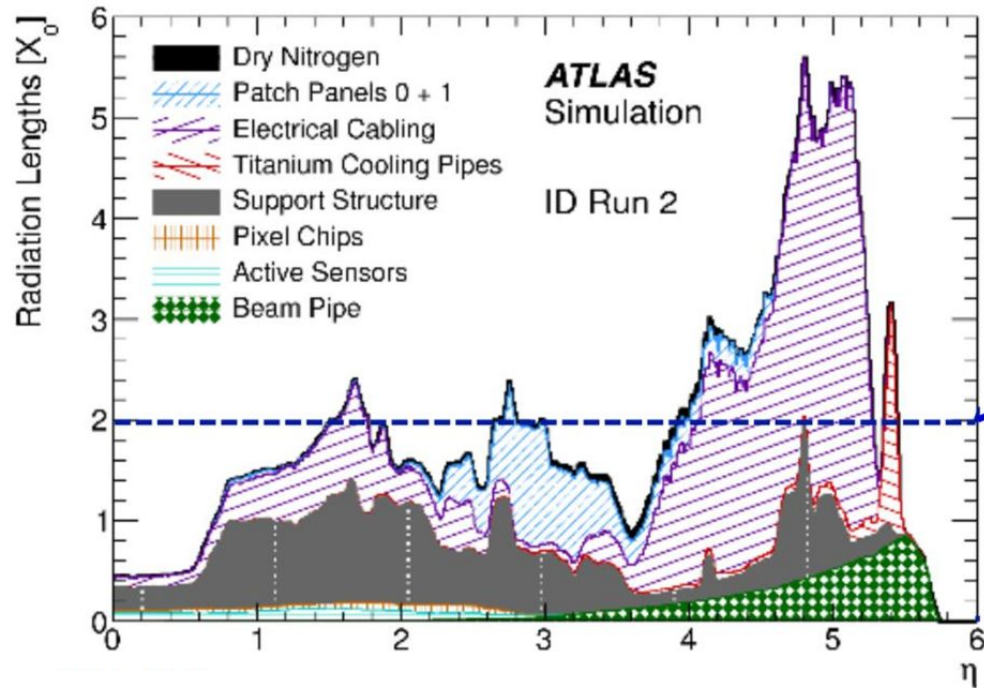
Identical structure & composition  
three different R



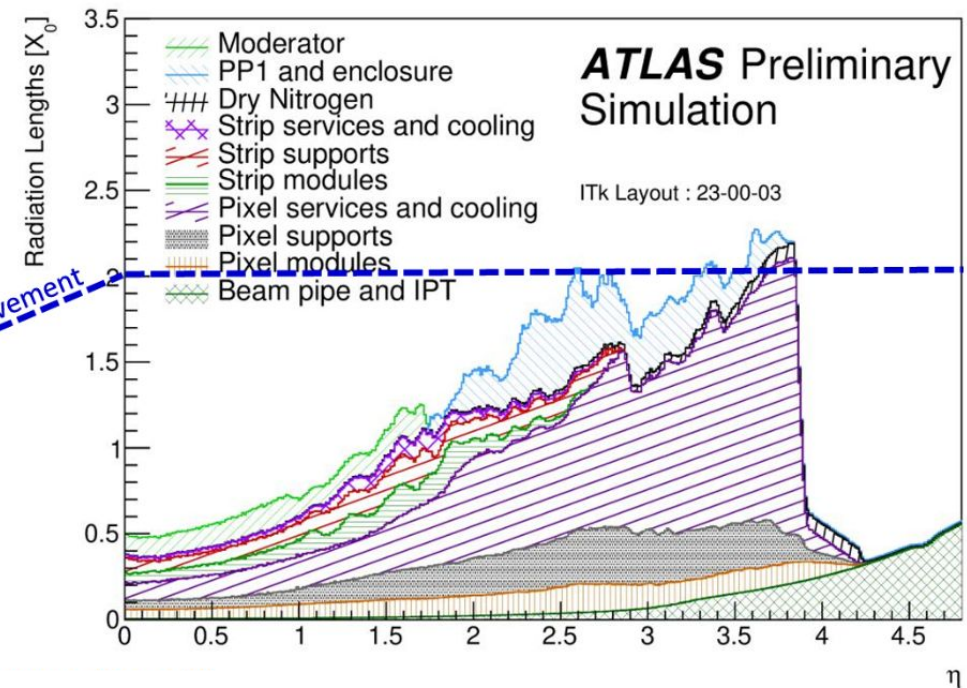




- Low-mass carbon structures for mechanical stability and mounting
- CO<sub>2</sub> cooling with thin titanium pipes (high evaporation pressure, ~50 bar)
- Serial power chain and data link sharing reduced cabling
- Thin sensor and FE chips minimise materials in module



Factor ~2 improvement





Another significant challenge is the production rate the volume required for the substantially enlarged pixel system

- Appropriate technology selections
- Well defined and documented procedures
- Centralized tools as much as possible:
  - ITk Production Database (for entire ITk)
  - Local Database
  - ITk WebApp
  - Module QC tools
  - Site qualification
  - Data quality monitoring

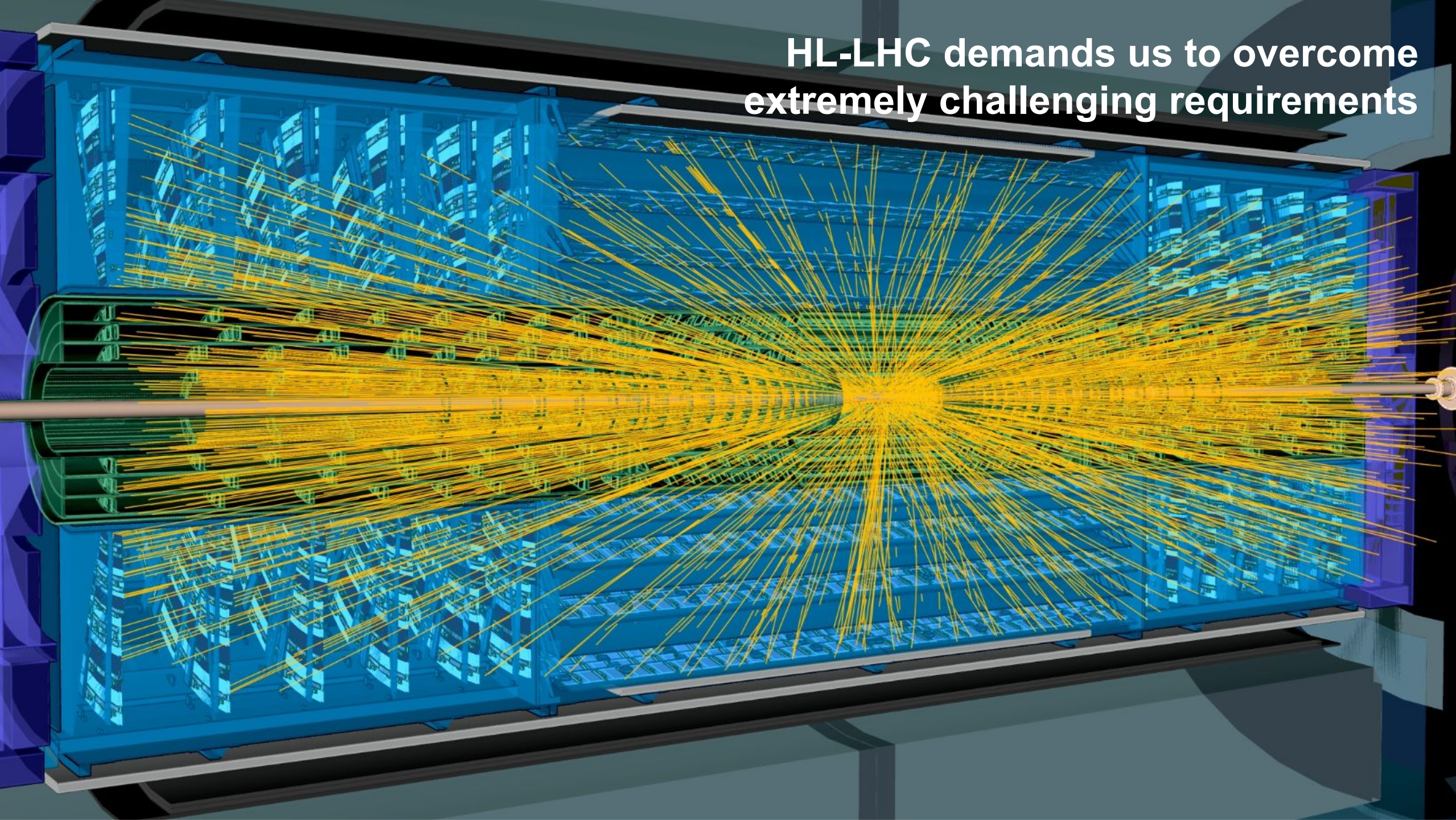
## ATLAS Collaboration Maps



**XX institutions from YY countries  
for ITk Pixel Upgrade**



**HL-LHC demands us to overcome extremely challenging requirements**





# The only way to discover the limits of the possible is to go beyond them into the impossible

- **Our recipes**

- All-silicon detector
- Extended coverage with 5 pixel layers ( $|\eta|$  up to 4)
- New sensors and FE chip advancement
- Pioneering features for optimized material budget

- **The excellent tracking performance and the forward tracking extension are essential for reaching our milestones in the HL-LHC physics program**

ex) VBF, VBS, Higgs Self-Coupling, Long-lived particle, and many more!