Advancements in the ATLAS Fast Chain for HL-LHC: Towards Efficient MC Production

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Outline

- Intro: Accelerating simulation for the HL-LHC era
 - The need for simulation speed-up
 - ATLAS detector overview
 - Fast Chain workflow for MC production
 - Strategies to speed up in ATLAS
- FATRAS: Fast ATLAS Tracking simulation
- Track Overlay
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The need for simulation speed-up

- Massive data and storage constraints.
 - simulation speed is critical for the HL-LHC, especially with increased pile-up and event rates.
- FastChain helps to meet precision demands in Physics modeling while operating within resource limitations.



ATLAS detector overview



• Inner Detector:

Tracks charged particles, measures momentum, and reconstructs vertices.

- Challenges: high particle density and complex geometry require sophisticated simulation techniques.
- Calorimeters (ECal and HCal): ECal measures energy of electrons and photons. HCal measures energy of hadrons.
- Challenges: Electromagnetic and hadronic showers produce complex (and very different) cascades of particles.
- Muon Spectrometer:

Fewer particles interact with muon spectrometer than in the ID or calorimeters, full Geant4 is often still feasible here.

Fast Chain workflow for MC production



Strategies to speed up simulation

- Wall time speed up
 - Multi-threading or Multi-processing.
 - Resource usage optimization

CPU usage	GPU usage (e.g. use Geant4 API to
* Optimized Detector	offload part of Geant4 workload to
Geometry description for	the GPUs)
FullSim.	\rightarrow e.g. <u>Celeritas</u> & <u>AdePT</u> on HPCs.
* Fast Simulation.	(Work in progress)

- <u>Calorimeter (AF3)</u>: uses parametrized models or ML GAN predictions to simulate the energy deposits in the calorimeters.
- Inner Detector: Combines parametrization and simplified geometry + Track overlay.

+ Scalable performance across diverse computing infrastructures, including traditional GRID and HPC clusters, ensuring efficient resource utilization for fast simulation.



FATRAS

- Fatras, a fast simulation tool, designed to model charged particle interactions in the ID using simplified approaches:
 - Simplified Geometry.
 - Approximation of interactions.
- Fatras currently achieves accuracy within ~10% compared to Geant4 results for EM interactions. We aim to improve the accuracy to around ~1%.

We want to further improve the photon conversion model by fine-tuning the parameters (ongoing).





Reference: ATL-COM-SOFT-2008-002

FATRAS: track reconstruction

- Due to limitations in Fatras' ability to accurately simulate hadronic interactions, we plan to use Geant4 for these processes (ongoing).
- Fatras shows about 10% better than expected resolution in transverse impact parameter, d0. Over-optimism due to mismodeling of rare hadronic interactions.
- While good agreement in track reconstruction across pseudorapidity (agrees with Geant4 within 1%.), there are notable efficiency discrepancies.



FastChain on GRID and HPCs

• The performance of Fatras on large-scale computing infrastructures. Our 5 ongoing workflows:

Scenario 1: AF3F + MC Overlay (Sim + Reco + Derivation); Scenario 2: AF3F + MC Overlay (FastChain_tf); Scenario 3: AF3F + Track Overlay (Sim + Reco + Derivation); Scenario 4: AF3F + Track Overlay (FastChain_tf); Scenario 5: AF3 + Track Overlay (FastChain_tf);

• Process scaling on HPCs: increasing the number of processes (e.g., from 8 to 64) improves the processing speed for large event counts because more events are handled in parallel. However, the writing method (I/O strategy) plays a critical role in ensuring that scaling provides real performance benefits.



AthenaMP write modes: Merge(Mrg), Shared Writer (SW), Shared Writer + parallelCompression (SW[ParCom])



Source: ATLAS PLOTS SIMU-2024-006

Fatras components and ongoing enhancements

• Expect comprehensive results on the accuracy of physics models in 2025! Stay tuned for future performance results.



Track Overlay

Overview of track overlay: simulation with pile-up

- Goal: Optimizing computational efficiency in reconstruction while maintaining the physics performance.
- Reusing reconstructed pile-up tracks

- This skips several time-consuming steps (e.g. clustering and track finding for pile-up events).



Overview of track overlay: simulation with pile-up

- The limitation: Track overlay is not suitable for all event types:
 - Dense track environment: track misinterpretation or hit sharing is common when multiple tracks overlap in high density area, complicating pattern recognition.
 - Pile-up events introduce additional noise, making it difficult to separate true hard-scatter from pile-up tracks, especially inside jets.



DNN ML model training

- We developed a NN to identify unsuitable events. The NN is used to determine, on an event-by-event basis, whether track overlay or MC overlay should be applied.
- Step 1: Network classification

label

- Features: truth information including kinematics of generator-level particles, event topology (i.e. local track density), Pile-up information.



- Step 2: Identifying "Bad" tracks
- A threshold is applied to determine which tracks are considered as "Bad" tracks. The bad tracks are associated with events that have differences between the Track overlay and MC overlay.
- If an event contains a single "bad" track, it is redirected to MC overlay.

DNN ML model training

• We integrate the trained NN model into Athena. The model predicts whether each event should be reconstructed using track overlay or redirected to MC overlay.



On the fly decision per event!

The result of this integration is our Hybrid overlay.

MC- and Track overlay work the same in the muon spectrometer and calorimeter.

Accuracy: Hybrid v.s. standard workflows

1.04

1.02

0.98

500

1000

1500

2000

Jet p_ [GeV]

2500

Track/MC

• Evaluating the accuracy of hybrid compared to MC overlay in numerous physics process: QCD multi-jet events, top quark pairs, W' \rightarrow Wh \rightarrow lvbb (for m_{w'}=3 TeV).



Source: <u>2404.06335</u> More performance plots: <u>ATLAS PLOTS SIM-2024-001</u>

Speed analysis

• Assessing the speed improvements with track overlay in terms of CPU usage with $\langle mu \rangle \sim 60$ pileup file. The evaluation has been tested on the ttbar process.

Configuration	Overlay	Reconstruction
MC overlay	2.21s (file size: 4 GB)	5.01s
Track overlay	3.00s (file size: 8.8 GB)	2.84s
Track overlay Less compression algorithm	2.18s (file size: 13 GB)	2.66s

(Plan to run multiple instances to get a reasonable average.)

- The track overlay reduces the reconstruction CPU usage by approximately 45%.
- Eased the compression setting to speed up processing.
 - Trade-off: file size increased from 8.8 GB to 13 GB.
 - This is acceptable because the files are intermediate and the performance gain outweighs the cost of larger file sizes.

Conclusions

• The Fast Chain has demonstrated a notable reduction in wall-time and CPU usage (for Run 3):

AF3: speed-ups ranging from 3 to 15 times compared to fullSim.

Fatras: a factor of ~3 compared to AF3.

Track overlay: ~45% faster in reconstruction relative to standard MC overlay approach.

- Ongoing work:
- Improving the accuracy of the Fatras model, especially for challenging physics observables like photon conversions.
- Integrating ACTS-based geometry and material maps.
- Exploring a simplified tracking geometry used in Geant4.
- Developing the Track overlay workflow for Run 4+.
- Setting up scalable workflows on both GRID and HPC clusters.
- Once the Fast Chain is optimized to be efficient and accurate enough, we aim to avoid generating and storing intermediate files. Re-simulating everything in a single streamlined process would significantly decrease storage requirements.

Backups