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RATIONAL FREQUENCY MULTIPLIER

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1. Function

This NIM-module is an RF-synthesizer for the ferrite cavities of the PS, developped for the ACOL \bar{p} production beam.

2. <u>Specifications</u>

- 4 TTL/50 Ω outputs with a frequency equal to F = f-clock $\frac{F}{2^{23}}$ out 2^{23} F = digital frequency word (TTL levels)
- Fast frequency change (
 1 µs) without phase discontinuity.
- Possibility to synchronize the output with "F-rev" (reference pulse train), after a "start synchro" pulse has been given.
- Clock frequency 2 to 30 MHz

Output frequency 0.1 to 5 MHz.

3. Description

The input word F consists of 25 bits (25-gnd, 23-N.C.), bits 1-22 form the data (TTL levels - 470 Ω pull-up resistors). Bit 24 is used to latch the data, this latching is synchronized with the internal clock system.

These TTL levels are translated in ECL. The resulting digital word is then used as the input of a digital integrator, clocked by the external "clock" signal.

For a constant input word, the result is a linearly increasing digital word at the output of the 24 bits adder. Since the adder output regularly overflows, seen in an analog form, it looks almost like a sawtooth. In order to decrease the amplitudes of the harmonics the sawtooth is then transformed into a triangle.



Fourier function sawtooth

 $f(t) = \frac{A}{2} - \frac{A}{\pi} (sinwt + \frac{1}{2} sin2wt + \frac{1}{3} sin3wt + ...)$



Fourier function triangle

$$f(t) = \frac{A}{2} - \frac{4A}{\pi^2} \left(\cos \omega t + \frac{1}{3^2} \cos 3\omega t + \frac{1}{5^2} \cos 5\omega t + \ldots \right)$$

After being latched, the digital word will be converted into an analog signal, followed by a low pass filter (5 MHz), shaping and distribution system (TTL level/ 50Ω).

There is also the possibility to synchronize the output with an external frequency (F-rev. input) after a "start synchro" pulse has been given (IC7, IC10, IC26).

That is obtained by resetting the adder output latches in synchronism with the external reference.

8 bit RFM - PS/RF-LL2012/2-2 (Reference number)

10 bit RFM - PS/RF-LL2018/2-2 (Reference number).

<u>References</u>

- 1. SPS/ARF, Note on SPS 6022 Frequency synthesizer, 1975, handwritten note.
- "Direct Digital Frequency Synthesis", Rodger H. Horking (Rockland) and H.W. Cooper, Electronic Design 15, July 19, p. 17, 1974.
- 3. "Frequency Synthesis: Techniques and Applications", Jersky, Gorski and Popiel, IEEE Press, p. 25, 1975.





