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SOME ASPECTS OF WIDE-BAND CATHODE-FOLLOWER CIRCUITS

Summary

Wide-band cathode-followers, if designed on the basis of low frequency formulas, often yield unsatisfactory results if subjected to high frequency input signals.

This report sets out to show that the maximum excitation level for distortion-free operation at high frequencies is considerably smaller than that given by he low frequency formula.

Moreover, the input conductance may assume a very high, and even negative, value which may result in unstable operation of the circuit.

Finally, it is shown that the input capacitance of the circuit increases with rising frequency which prevents one distinct advantage of a cathode-follower, i.e. a small input capacitance, from materializing.

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1. Low frequency excitation limit



Fig. 1

From fig. 1 we can write the mesh equations :

 $Va + ia \cdot Rk = Vb$ (1)

$$Vg + ia \cdot Rk = ei$$
 (2)

If we now assume the tube to be linear, we can write the following general expression for the anode current :

$$ia \cdot Ri = \mu \cdot Vg + Va$$
 (3)

Replacing Vg and Va in (3) by the values for Vg and Va as expressed in (1) and (2), we find :

$$ia \cdot Ri = \mu(ei - ia \cdot Rk) + Vb - ia \cdot Rk$$
(4)

The anode current consists of two components, one which is a D.C. current due to Vb and the other is a signal current due to ei.

The maximum current, which does not yet produce distortion occurs when

Vg = 0. This condition is met for $ei = ia \cdot Rk$. The value for the maximum current due to Vb is the D.C. current and from (4) we see that

$$ia_{max} = \frac{Vb}{Ri + Rk}$$
(5)

The current produced by the input signal ei, on the assumption that Vb is bypassed for any input signal frequency, is :

$$ia = \frac{\mu \cdot ei}{Ri + Rk(\mu+1)}$$
(6)

The limiting value of ei produces the current given in (5). Setting (5) and (6) equal, one obtains for ei_{i} :

$$e_{\mathcal{Q}}^{i} = \frac{Vb}{\mu} \left[\frac{Ri + Rk(\mu+1)}{Ri + Rk} \right]$$
(7)

This expression actually gives the whole grid voltage space. If, however, the circuit is designed so that the bias current through the tube is $\beta \cdot ia_{max}$, the expression (7) has to be multiplied by a factor $(1 - \beta)$, thus :

$$ei_{\mathcal{Q}} = \frac{\Psi b}{2\mu} \left[\frac{1 + \frac{Rk}{Ri} \cdot \mu}{1 + \frac{Rk}{Ri}} \right]$$
(9)

which is the well-known formula with which the input voltage limits for class A cathode followers are computed.

2. High frequency excitation reduction

The output voltage of the circuit of fig. 1 can be written as :

$$e_{o} = i_{R} \cdot Rk = \frac{\mu \cdot Rk \cdot ei}{Ri + Rk(\mu+1)}$$
(10)

and the gain of the circuit is

$$\frac{\overset{e}{o}}{\overset{e}{i}} = \frac{\mu \cdot Rk}{Ri + Rk(\mu+1)} = G_{o}$$
(11)

The grid-cathode voltage Vg from mesh equation 2 can with (11) be written as :

$$V_g = ei (1 - G_0)$$
(12)

It will be shown that for high frequencies the gain becomes complex and can be written as :

$$\overline{G} = G_0 \sqrt{1-\alpha_1} \cdot e^{j\Theta_1}$$
(13)

Under these conditions one has to substitute (13) for G_0 in equation (12). Under no circumstances must the grid-cathode voltage become larger than that value of Vg which exists for $ei = ei_{\chi}$. As this limiting value of Vg is the same for low and high frequencies, we can write :

$$V_{g} = e_{\ell} (1 - G_{0}) = e_{\omega \ell} (1 - G_{0} \sqrt{1 - \alpha_{1}} \cdot e^{j\theta_{1}})$$
(14)

and from (14) we derive :

$$e_{\omega} \ell = e_{i} \frac{1 - G_{o}}{1 - G_{o} \sqrt{1 - \alpha_{1}} \cdot e}$$
(15)

and the high frequency excitation reduction factor therefore is :

$$\frac{1 - G_{o}}{1 - G_{o}\sqrt{1 - \alpha_{1}} \cdot e^{j\theta_{\perp}}}$$
(16)

This reduction in excitation limit may be explained from a vector diagram. In fig. 2, no difference in phase exists between input and output signal, so fig. 2 is the vector representation of equation 12 :



Fig. 2

If now the gain becomes smaller and shows a phase difference in comparison to the input signal, the vector representation is given in fig. 3 :



and the vector Vg is now larger than the radius of the circle R_1 . In order to bring Vg_{ℓ} to its maximum value the excitation has to be reduced until e_0 lies on the circle R_1 .

3.1 Derivation of cathode-follower matrix

The general circuit of a cathode-follower circuit (when the power supply is by-passed for all frequencies, and disregarding the grid leak resistor) is given in fig. 4 :



Fig. 4

To analyse the circuit of fig. 4, the circuit will be regarded as a four-terminal network, and the matrix method for the analysis of four-terminal networks is applied (ref. 1, 2, 3).

The circuit of fig. 2 is split into a passive and an active fourterminal network, see figs. 5 and 6,



Fig. 6

From figs 3 and 4 the following relationships are derived :

$$i_{1} = i_{1}' + i_{1}''$$
 (19)

$$i_2 = i_2' + i_2''$$
 (20)

$$ei = ei' + ei''$$
(21)

$$\mathbf{e}_{\mathbf{0}} = \mathbf{e}_{\mathbf{0}}^{\dagger} \div \mathbf{e}_{\mathbf{0}}^{\dagger} \tag{22}$$

$$i_1 = 0$$
 no grid current (23)

$$i_{2} = -gm \cdot ei' + (gm + \frac{1}{Ri}) e_{0}'$$
(24)

The passive matrix from fig. 5 is :

$$\begin{vmatrix} \mathbf{i}_{1}^{"} \\ \mathbf{i}_{2}^{"} \end{vmatrix} = \begin{vmatrix} \operatorname{Yag} + \operatorname{Ygk} & -\operatorname{Ygk} \\ -\operatorname{Ygk} & + (\operatorname{Ygk} + \operatorname{Yak}) \end{vmatrix} \cdot \begin{vmatrix} \mathbf{ei}^{"} \\ \mathbf{e}_{0}^{"} \end{vmatrix}$$
(M)

The equations (23) and (24) in matrix form :

$$\begin{vmatrix} \mathbf{L}_{1} \\ \mathbf{L}_{2} \\ \mathbf{L}_{2} \\ \end{vmatrix} = \begin{vmatrix} \mathbf{0} & \mathbf{0} \\ -g\mathbf{m} & +(g\mathbf{m} + \frac{1}{R\mathbf{i}}) \end{vmatrix} \cdot \begin{vmatrix} \mathbf{e}\mathbf{i} \\ \mathbf{e}_{0} \\ \mathbf{e}_{0} \end{vmatrix}$$
(M₂)

The matrix for i₁, i₂ and ei, e₀ is the sum of the matrices $M_1 + M_2$ giving as the couplete matrix :

$$\begin{array}{c|c} L_{1} \\ = \\ L_{2} \\ L_{2} \end{array} + (gm + Ygk) + (Ygk + Yak + gm + \frac{1}{Ri}) \end{array} e_{0} \end{array}$$

We can now compare (M 3) with the admittance matrix :

$$\begin{vmatrix} \mathbf{i}_{1} \\ \mathbf{i}_{2} \end{vmatrix} = \begin{vmatrix} \mathbf{Y}_{11} & \mathbf{Y}_{12} \\ \mathbf{Y}_{21} & \mathbf{Y}_{22} \end{vmatrix} \cdot \begin{vmatrix} \mathbf{e}\mathbf{i} \\ \mathbf{e}_{0} \end{vmatrix}$$
(M 4)

From the admittance matrix we know that :

$$\frac{e_{0}}{e_{1}} = -\frac{Y_{21}}{Y_{22}}$$
(25)

$$Y_{im} = Y_{11} - \frac{Y_{12} \cdot Y_{21}}{Y_{22}}$$
 (26)

$$Y_{out} = Y_{22}$$
(27)

By comparing (M 3) with (M 4) we get :

$$Y_{11} = Yag + Vgk$$
(28)

$$Y_{12} = -Y_{gk}$$
(29)

$$Y_{21} = -(gm + Ygk)$$
 (30)

$$Y_{22} = Ygk + Yak + gm + \frac{1}{Ri}$$
(31)

where

$$Yag = j\omega Cag$$

$$Ygk = j\omega Cgk$$

$$Yak = j\omega Cf + j\omega Cak + j\omega C_{stray} + \frac{1}{Rk}$$

$$= j\omega C_{o} + \frac{1}{Rk}$$

$$(32)$$

3.2 Circuit Parameters

Inserting equations 28 - 32 into 25, 26 and 27 gives the following :

gain =
$$-\frac{Y_{21}}{Y_{22}} = \frac{j\omega Cgk + gm}{j\omega (Cgk + C_0) + gm + \frac{1}{Ri} + \frac{1}{Rk}}$$
 (33)

If in (33) we assume ω to be zero, the expression for the low frequency gain is

$$G_{0} = \frac{gm}{gm + \frac{1}{Ri} + \frac{1}{Rk}} = \frac{\mu \cdot Rk}{Ri + Rk (\mu+1)}$$
(34)

Input admittance

$$Y_{in} = Y_{11} - \frac{Y_{12} \cdot Y_{21}}{Y_{22}} = j\omega \left(Cag + Cgk\right) - \frac{j\omega Cgk \left(gm + j\omega Cgk\right)}{j\omega (Cgk + C_0) + gm + \frac{1}{Ri} + \frac{1}{Rk}}$$
(35)

If in (35) we divide the expression by jw which we assume to be near zero, we get the capacitive component of the input admittance. So

$$C_{in} = Cag + Cgk - Cgk \cdot \frac{gm}{gm + \frac{1}{Ri} + \frac{1}{Rk}}$$
(36)

Substitution of (34) in (36) gives the following for the input capacitance :

$$C_{in} = Cag + Cgk (1 - G_{o})$$
 (37)

Output admittance :

$$Y_{22} = j\omega (Cgk + C_0) + gm + \frac{1}{Ri} + \frac{1}{Rk}$$
 (38)

Again we assume ω to be zero so that the output conductance becomes :

$$Y_{22} = gm + \frac{1}{Ri} + \frac{1}{Rk}$$
 (39)

Generally one assumes $\frac{1}{Ri}$ and $\frac{1}{Rk}$ to be smaller than gm so that the output resistance of the cathode-follower is quoted to be :

$$Z_{o} = \frac{1}{Y_{22}} = \frac{1}{gm}$$
 (40)

4.1 Magnitude of H.F. excitation reduction factor

It has already been shown that this reduction factor is given by the expression :

$$\frac{1-G_{0}}{j\Theta_{1}}$$

$$1-G_{0}\sqrt{1-\alpha_{1}} \cdot e^{j\Theta_{1}}$$
(16)

From equation (33), which is the expression for the complex gain, we can write for the modulus :

$$|\bar{G}| = \left[\frac{\omega^{2}Cgk^{2} + gm^{2}}{\omega^{2}(Cgk+C_{0})^{2} + (gm+\frac{1}{Ri} + \frac{1}{Rk})^{2}}\right]^{\frac{1}{2}}$$
(35)

which can be rearranged to :

$$\left|\overline{G}\right| = C_{0} \left[1 - \frac{\omega^{2} \left(Cgk+C_{0}\right)^{2} gm^{2} - Cgk^{2} \left(gm+\frac{1}{Ri}\frac{1}{Rk}\right)^{2}\right]}{gm^{2} \left(gm+\frac{1}{Ri}\frac{1}{Rk}\right)^{2} + gm^{2} \omega^{2} \left(Cgk+C_{0}\right)^{2}}\right]^{\frac{1}{2}} = G_{0} \left(1-\alpha\right)^{\frac{1}{2}}$$
(41)

The argument of the gain is :

$$\Theta_1 = \Theta_a - \Theta_b \tag{42}$$

where, again from (33)

$$\Theta_{a} = \operatorname{arc} \operatorname{tg} \frac{\omega Cgk}{gm}$$
(43)

$$\theta_{b} = \operatorname{arc} \operatorname{tg} \frac{\omega(\operatorname{Cgk+C}_{o})}{\operatorname{gn+\frac{1}{Ri} + \frac{1}{Rk}}}$$
(44)

and, via the trigonometric identity

$$t_{g} (\theta_{a} - \theta_{b}) = \frac{t_{g}\theta_{a} - t_{g}\theta_{b}}{1 + t_{g}\theta_{a} \cdot t_{g}\theta_{b}}$$
(45)

we can write for the phase angle ϑ_{1} :

$$\Theta_{1} = \operatorname{arc} \cdot \operatorname{tg} \frac{\omega \operatorname{Cgk}(\operatorname{gm} + \frac{1}{\operatorname{Ri}} + \frac{1}{\operatorname{Rk}}) - \omega \operatorname{gm}(\operatorname{Cgk} + \operatorname{C}_{0})}{\operatorname{gm}(\operatorname{gm} + \frac{1}{\operatorname{Ri}} + \frac{1}{\operatorname{Rk}}) + \omega^{2} \operatorname{Cgk}(\operatorname{Cgk} + \operatorname{C}_{0})}$$
(46)

If in equation (41) we substitute the following expressions :

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$$\omega_{1} = \frac{gm}{C_{gk}}$$
(47)

$$\omega_2 = \frac{\operatorname{gm} + \frac{1}{\operatorname{Ri}} + \frac{1}{\operatorname{Rk}}}{\operatorname{Cgk} + \operatorname{C}_{o}}$$
(48)

the equation (41) can be written as :

$$G_{0} (1-\alpha)^{1/2} = G_{0} \left[\frac{1 + \frac{\omega^{2}}{\omega_{1}^{2}}}{1 + \frac{\omega^{2}}{\omega_{2}^{2}}} \right]^{1/2}$$
(49)

Substitution of (47) and (48) in (46) gives :

$$\Theta_{1} = \operatorname{arc} \operatorname{tg} \frac{\omega(\omega_{2} - \omega_{1})}{\omega^{2} + \omega_{1} \cdot \omega_{2}}$$
(50)

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With the expressions (49) and (50) we can now write for the complete expression for the excitation limit under class A conditions (where we write for $(1-G_0) = \frac{Ri + Rk}{Ri + Rk(\mu+1)}$):

$$e^{i}\ell_{(\omega)} = \frac{Vb}{2\mu} \cdot \frac{1}{1 - G_0 \left[\frac{1 + \frac{\omega^2}{2}}{\frac{\omega_1}{1 + \frac{\omega^2}{2}}} \right]} \cdot \cos \operatorname{arc} tg \frac{(\omega_2 - \omega_1)}{\frac{\omega^2 + \omega_1 \omega_2}{\omega^2 + \omega_1 \omega_2}}$$
(51)

4.2 Simplified expressions

If we now assume that

$$Cgk < C_{o}$$
 (52)

$$gm > \frac{1}{Ri} + \frac{1}{Rk}$$
(53)

and

it then follows that ω_2 comes much earlier than ω_1 , and if we take only ω_2 into consideration the expression (51) can be written as follows :

$$ei_{\ell(\omega)} = \frac{Vb}{2\mu} \cdot \frac{1}{1 - G_0 \left(1 + \frac{\omega}{\omega_2^2}\right)} \cos \operatorname{arc} tg \frac{\omega}{\omega_2}}$$
(54)

This expression is plotted against $\frac{\omega}{\omega_2}$ and multiplication factors of $\frac{Vb}{2\mu}$ with the low frequency gain G_o as a parameter in nomograph N I.

When the cathode-follower is used to drive a matched coaxial cable, we can assume that, if a high slope tube is used, the grid cathode capacitance and the output capacitance are roughly equal.

The expression (51) can then be written as :



This expression is plotted, in the same way as (54), in nomograph N II

5.1 Input admittance of cathode-follower

In equation 35, the input admittance has been given as :

$$Y_{in} = j\omega (Cag+Cgk) - \frac{j\omega Cgk (gm+j\omega Cgk)}{j\omega (Cgk+C_o) + gm + \frac{1}{Ri} + \frac{1}{Rk}}$$
(35)

If we split (35) into its real and imaginary component we get :

Real Part :
$$\frac{\omega^2 \operatorname{Cgk} \left[\operatorname{Cgk} \left(\operatorname{gm} + \frac{1}{R} \right) - \operatorname{gm} \left(\operatorname{Cgk} + \operatorname{C}_0 \right) \right]}{\left(\operatorname{gm} + \frac{1}{R} \right)^2 + \omega^2 (\operatorname{Cgk} + \operatorname{C}_0)^2}$$
(57)

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Imaginary part :

$$\omega(\operatorname{Cag+Cgk}) = \frac{\omega^{2}\operatorname{Cgk}\left[\operatorname{gm}\left(\operatorname{gm}+\frac{1}{R}\right) + \omega\operatorname{Cgk}\left(\operatorname{Cgk+C}_{0}\right)^{2}\right]}{\left(\operatorname{gm}+\frac{1}{R}\right)^{2} + \omega^{2}\left(\operatorname{Cgk+C}_{0}\right)^{2}}$$
(58)

where both in (57) and (58)

$$\frac{1}{R} = \frac{1}{Ri} + \frac{1}{Rk}$$
(59)

5.2 Input conductance

The input conductance of the circuit is given by the real part of the input admittance (57). Examination shows that for large values of gm, practically any value of C_0 will make this input conductance negative.

As in general the grid circuit of the cathode-follower has a grid leak resistance to earth (Rg), the input admittance of the complete circuit with grid leak resistance, on the assumption that $gm > \frac{1}{R}$ (53), is :

$$Y_{in} = \frac{1}{R_g} - \frac{\omega^2 C_{gk} \cdot gm^2 \cdot C_0}{(gm^2)^2 + \omega^2 (C_{gk} + C_0)^2}$$
(60)

and the input conductance will go through zero for the ω of :

$$\omega_{3} = \frac{1}{\sqrt{\frac{\text{Rg} \cdot \text{Cgk} \cdot \text{C}}{\text{gm}} - (\frac{\text{Cgk} + \text{C}}{\text{gm}})}}$$
(61)

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5.3 Input capacitance

The input capacitance is given by the imaginary part of the input admittance (58) divided by ω .

$$C_{in} = Cag + Cgk - \frac{\omega Cgk \left[gm^2 + \omega(Cgk + C_o) Cgk\right]}{gm^2 + \omega^2(Cgk + C_o)^2}$$
(63)

which, for those frequencies where $\omega \operatorname{Cgk}(\operatorname{Cgk+C}_{o}) > \operatorname{gm}^{2}$ and $\omega^{2}(\operatorname{Cgk+C}_{o})^{2} > \operatorname{gm}^{2}$, may be written as :

$$C_{in} = Cag + Cgk \left(1 - \frac{Cgk}{Cgk + C_{o}}\right)$$
(64)

and it becomes clear that for large values of C_0 the input capacitance of the cathode-follower assumes the value

$$C_{in} = C_{ag} + C_{gk}$$
(65)

6. <u>Special</u> conditions

On the foregoing pages it has been shown that the excitation limit is rather dependent on the value of the output capacitance.

If, however, the designer of a cathode-follower circuit adds some lumped capacitance between the grid and the cathode to wake Cgk equal to the output capacitance C_0 and if, on the other hand, he chooses the cathode-load so low that the admittance of cathode resistance and internal resistance equal the slope, a very special condition is established.

Consequently, we can write :

$$Cgk = C_{o}$$
(66)

$$gm = \frac{1}{Ri} + \frac{1}{Rk}$$
(67)

$$\omega_{l} = \frac{gm}{Cgk} \quad (47)$$

$$\omega_2 = -\frac{2 \text{ gm}}{2 \text{ Cgk}} = \omega_1 \tag{68}$$

Thus, it can easily be shown that the gain of the circuit is constant and 0,5 (up to rather high frequencies). This means that no excitation limitation factor for high frequency operation exists and that the excitation limit is given by equation (9). Under these conditions the input conductance of the tube is always zero, and so the input resistance of the circuit is only determined by the grid leak and the stopper resistance. The input capacitance of the the circuit under these conditions is :

$$C_{in} = Cag + \frac{1}{2} C_{gk} .$$
 (69)

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