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Test-beam and simulation studies for the CLICTD technology demonstrator - a monolithic CMOS pixel sensor with a small collection diode

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Abstract

The CLIC Tracker Detector (CLICTD) is a monolithic pixel sensor featuring pixels of $30 \mu m \times 37.5 \mu m$ and a small collection diode. The sensor is fabricated in a 180 nm CMOS imaging process, using two different pixel flavours: the first with a continuous n-type implant for full lateral depletion, and the second with a segmentation in the n-type implant for accelerated charge collection. Moreover, CLICTD features an innovative sub-pixel segmentation scheme that allows the digital footprint to be reduced while maintaining a small sub-pixel pitch. In this contribution, test-beam measurements for the pixel flavour with the segmented n-implant are presented. The performance is evaluated in terms of time and spatial resolution as well as efficiency. Furthermore, the test-beam data is compared to simulation studies using a combination of 3D TCAD and Monte Carlo simulation tools.

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Abstract. The CLIC Tracker Detector (CLICTD) is a monolithic pixel sensor featuring pixels of $30 \,\mu\text{m} \times 37.5 \,\mu\text{m}$ and a small collection diode. The sensor is fabricated in a 180 nm CMOS imaging process, using two different pixel flavours: the first with a continuous n-type implant for full lateral depletion, and the second with a segmentation in the n-type implant for accelerated charge collection. Moreover, CLICTD features an innovative sub-pixel segmentation scheme that allows the digital footprint to be reduced while maintaining a small sub-pixel pitch. In this contribution, test-beam measurements for the pixel flavour with the segmented n-implant are presented. The performance is evaluated in terms of time and spatial resolution as well as efficiency. Furthermore, the test-beam data is compared to simulation studies using a combination of 3D TCAD and Monte Carlo simulation tools.

1. Introduction

Tracking detectors foreseen for future High-Energy Physics experiments are required to fulfil a set of challenging requirements such as precise spatial and time resolution, full efficiency and radiation hardness. To meet these requirements, an extensive research and development programme is performed within the CLIC detector and physics (CLICdp) collaboration and the strategic R&D programme of the CERN experimental physics department (EP R&D) [1, 2] combining laboratory and test-beam measurements with detailed simulation studies. In this document, the characterisation of the monolithic CLIC Tracker Detector (CLICTD) sensor is presented. The sensor targets the requirements of the CLIC tracker, that comprise a single point resolution of 7 µm in the spatial direction perpendicular to the magnetic field, a time resolution of approximately 5 ns, a material budget below $1-2\%X_0$ and a power consumption below 150mW/cm^2 [3].

2. The CLICTD Sensor

2.1. Sensor Design

The CLICTD technology demonstrator is a monolithic sensor fabricated in a 180 nm CMOS imaging process, modified with an additional deep n-type implant to achieve full lateral depletion within the p-type epitaxial layer of the sensor [4]. The sensor features a small collection diode and consequently a small input capacitance. The epitaxial layer has a thickness of 30 µm. The



Figure 1. Schematic representation of the CLICTD sensor with segmented n-implant. The segmentation is only applied in column direction.

total thickness of the sensor is $300 \,\mu\text{m}$. Samples thinned down to $40 \,\mu\text{m}$ - $100 \,\mu\text{m}$ were produced as well by backside grinding. CLICTD is fabricated in different design variants. The first has a continuous n-type implant. In the second, the n-type implant is segmented in column direction, as shown in Fig. 1. The resulting lateral doping gradient is responsible for an increased lateral electric field that speeds up charge collection and reduces charge sharing [5].

A bias voltage is applied to the p-wells and the substrate. The bias voltage at the p-wells is limited to -6 V to avoid breakdown of the on-channel NMOS transistors [6]. The substrate voltage is set to -6 V to avoid punch-through between substrate and p-well.

2.2. Front-End Design

The CLICTD features an active matrix of 16×128 detection channels, measuring $300 \,\mu\text{m} \times 30 \,\mu\text{m}$. Each channel is segmented into eight sub-pixels with its own collection diode, analogue front-end and threshold discriminator [7]. The sub-pixel pitch is $37.5 \,\mu\text{m} \times 30.0 \,\mu\text{m}$. The discriminator outputs of the eight-sub-pixels are combined with an OR gate in a common digital front-end. This scheme allows the digital footprint to be reduced in order to maintain a small pixel pitch. The sensor provides simultaneous time and energy measurement, using an 8-bit Time-of-Arrival (ToA) (10 ns bins) and a 5-bit Time-over-Threshold (ToT) measurement.

3. Test-Beam Setup

Test-beam measurements were performed at the DESY II Test Beam Facility [8], using a EUDET-type beam telescope [9] equipped with an additional Timepix3 plane [10] for providing track time-stamps with a precision of approximately 1 ns. The track pointing resolution at the device under test is between 2.4 μ m and 2.8 μ m, depending on the plane spacing during the measurements. The Corryvreckan reconstruction framework [11] was used to reconstruct and analyse the data.

During the measurements, the detection threshold was scanned, starting from the nominal threshold of 178 e to the maximum threshold of ~ 2500 e. The noise rate at the nominal threshold is $< 1 \times 10^{-3}$ Hz/sec for the entire pixel matrix [12].



Figure 2. In-pixel efficiency for data (right-hand side) and simulation (left-hand side) at a high detection threshold of 1850 e.

4. Simulation Setup

The ever-increasing complexity of novel silicon sensor designs requires advanced simulation techniques. In this study, the generic pixel detector simulation framework Allpix² is used for a full detector simulation, starting from the initial energy deposition up to the digitisation of the signal [13, 14]. The Allpix² simulation uses field maps such as the doping profile, weighting potential and electric field imported from electrostatic 3D TCAD simulations in order to ensure a precise field modelling and to compute physics parameters such as charge carrier lifetime or mobility.

The large samples achieved with this simulation approach enable the simulation of a testbeam scenario, taking the stochastic nature of the interaction between particles and the silicon sensor into account. The simulation setup is similar to the one described in [15].

5. Results

In the following, the performance of the CLICTD sensor is evaluated for various operating conditions. The pixel flavour with the segmented n-implant was optimised for faster charge collection and reduced charge sharing, which is beneficial for the overall performance of the sensor. The focus is therefore placed on this pixel variant.

Measurement uncertainties comprise statistical uncertainties as well as systematic ones that are related to the threshold-energy calibration and the reconstruction of the telescope reference, as detailed in [12]. For the simulations, only the statistical uncertainties are given.

5.1. Efficiency

The sensor was found to be fully efficient (> 99.7%) at the nominal detection threshold of 178 e. At high thresholds of $\gtrsim 600 \text{ e}$, inefficient regions start to form at the pixel borders, as illustrated in Fig. 2 for data and simulation at a threshold of 1850 e. The inefficiencies at the edges are caused by charge sharing, which leads to a lower seed signal.

5.2. Spatial resolution

A ToT-weighted centre-of-gravity algorithm combined with an η -correction is used to reconstruct the cluster position [12]. The η -correction is crucial to take non-linear charge sharing between adjacent pixel cells into account. The resulting residual distribution at the nominal threshold is shown in Fig. 3 for data and simulation.





Figure 3. Spatial residuals between the telescope track position and CLICTD for data and simulation at the nominal detection threshold of 178 e.

Figure 4. Substrate current as a function of the substrate voltage for a CLICTD sample with epitaxial layer and a sample fabricated on high-resistivity Czochralski wafer material.

The RMS of the central 3σ of the distribution is $5.4 \pm 0.1 \,\mu\text{m}$ for data, in good agreement with the value of $5.3 \,\mu\text{m}$ obtained for simulation. The uncertainty on data is dominated by the systematic uncertainty originating from the threshold-energy calibration. The statistical uncertainty in simulations is below $0.1 \,\mu\text{m}$. Unfolding the telescope resolution of $2.8 \pm 0.1 \,\mu\text{m}$, yields a spatial resolution of $4.6 \pm 0.2 \,\mu\text{m}$ and $4.5 \pm 0.1 \,\mu\text{m}$ for data and simulation, respectively.

5.3. Time resolution

First, the ToA values provided by the CLICTD sensor are corrected for time-walk, as explained in [12]. The time resolution is determined by evaluating the RMS of the central 3σ of the time residuals between CLICTD and the Timepix3 telescope plane. At the nominal threshold, the time resolution in data is 5.8 ± 0.1 ns. The resolution is dominated by the front-end timing, as was shown in laboratory measurements using test-pulses [12].

In the simulation, the crossing time of the transient current pulse with the detection threshold is used as a measure for the sensor time resolution. This definition excludes any front-end contributions to the timing and therefore provides an estimate for the lower limit of the achievable time resolution. According to this definition, the sensor time resolution evaluates to < 1 ns.

6. High-Resistivity Czochralski material

The active volume of the CLICTD sensor is limited by the thickness of the epitaxial layer. To overcome this limitation, CLICTD sensors were fabricated on high-resistivity (> 800 Ω cm) Czochralski wafers with a total thickness of 100 µm [16, 17]. The high-resistivity Czochralski material ensures a better isolation between p-well and substrate and therefore allows for a higher substrate voltage while keeping the p-well voltage at -6 V, as shown in Fig. 4. As a consequence of the higher substrate voltage, the depleted volume increases, leading to a higher signal-to-noise ratio and larger cluster size.

7. Summary and Outlook

The monolithic CMOS pixel sensor CLICTD was characterised in test-beam and simulation studies. The simulations profit from the precise sensor modelling available in electrostatic 3D TCAD and high simulation rates provided by the Monte Carlo framework Allpix².

The optimised sensor design with the segmented n-implant was found to fulfil the main CLIC tracker requirements: The CLICTD sensor is fully efficient (>99.7%) and has a position resolution of 4.6 μ m in row direction as well as a time resolution of 5.8 ns. For the relevant performance parameters at various operation conditions, the simulations agree with data within the measurement uncertainties. Systematic uncertainties in simulations are currently under study.

CLICTD samples on high-resistivity Czochralski offer a larger depleted volume and consequently a higher signal-to-noise ratio and larger cluster size, which can potentially improve the position resolution of the sensor. The samples are currently investigated in test-beam studies.

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