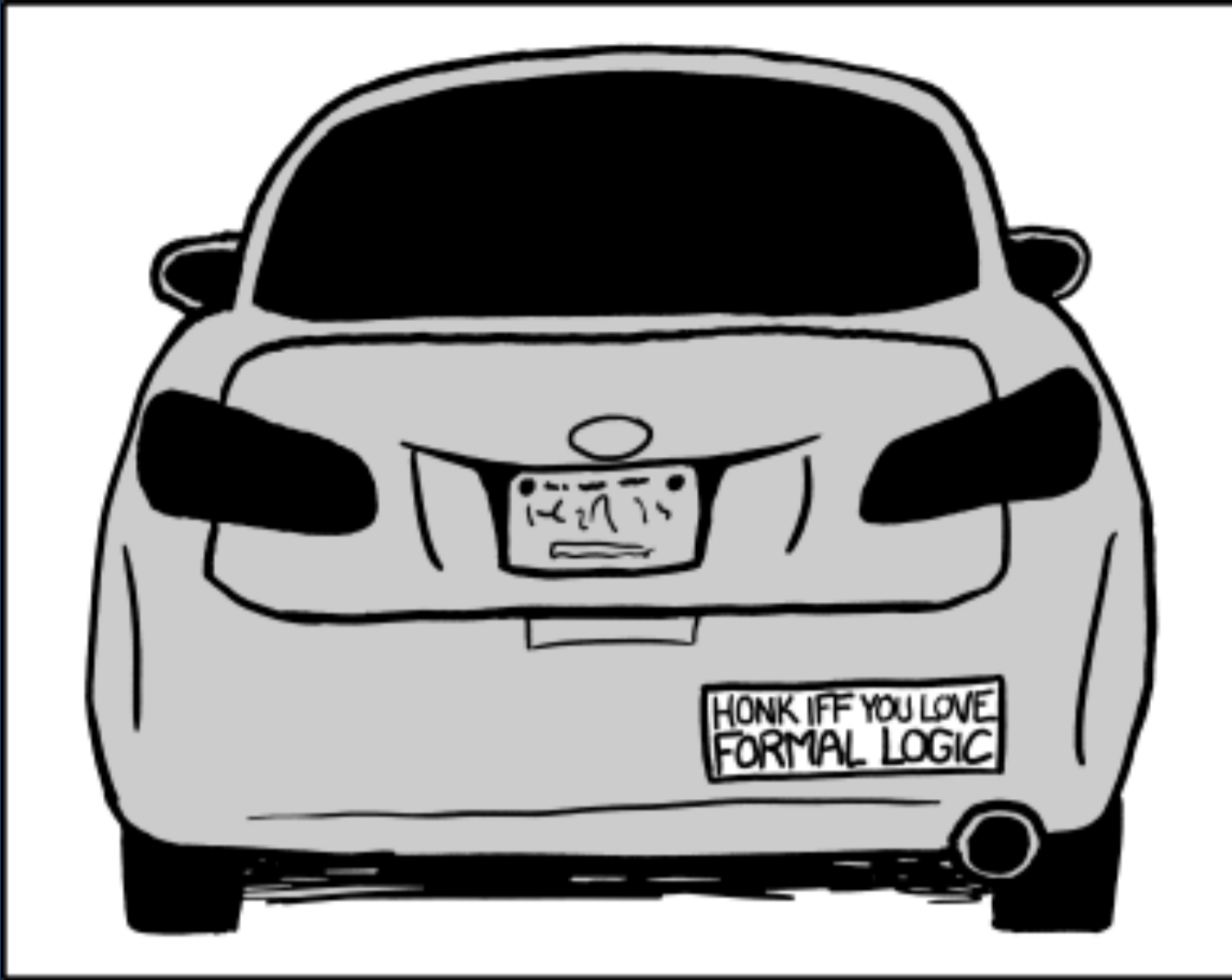


# Incremental Inprocessing in SAT Solving

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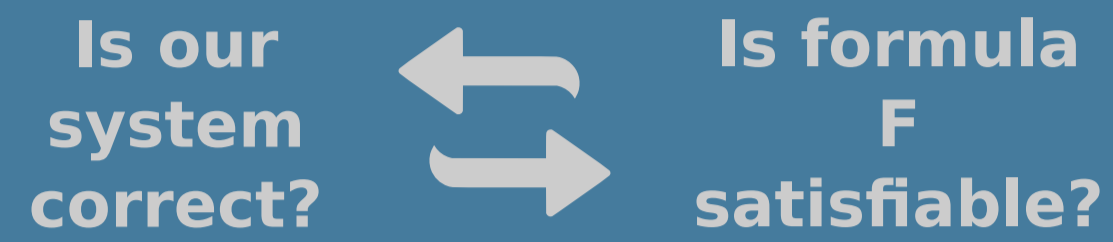


source: xkcd - 1033: Formal Logic  
 Title text: Note that this implies you should NOT honk solely because I stopped for a pedestrian and you're behind me.

## Motivation - Automated Formal Verification

Modern cars contain ~150 Electronic Control Units (ECUs) running software from different suppliers. How can we be sure that these software components are correct?

*"The only effective way to raise the confidence level of a program significantly is to give a convincing proof of its correctness."*  
 E.W. Dijkstra, 1972



We need efficient tools to answer this question!

## Background - SAT

Is this formula satisfiable?

$$(a \vee \neg b) \wedge (a \vee b) \wedge (\neg a \vee \neg b)$$

• Yes, a possible model:

$$\{a \leftarrow \text{True}, b \leftarrow \text{False}\}$$

- NP-complete decision problem
- SAT solvers find a model or prove unsatisfiability

## Shape of Verification Problems

Is formula  $\mathcal{F}_0$  satisfiable?

Is formula  $\mathcal{F}_0 \wedge \mathcal{F}_1$  satisfiable?

Is formula  $\mathcal{F}_0 \wedge \mathcal{F}_1 \wedge \mathcal{F}_2$  satisfiable?

- Sequence of decision problems
- Each problem is an extension of the previous

## Our Contributions

- Introduced a novel technique to combine incremental reasoning with inprocessing during SAT solving
- Provided a simple but efficient algorithm to implement it
- Improved performance in a hardware verification application
- Formally proved the correctness of our approach
- Simplified the use of incremental solvers, less work expected from user

## Our Method

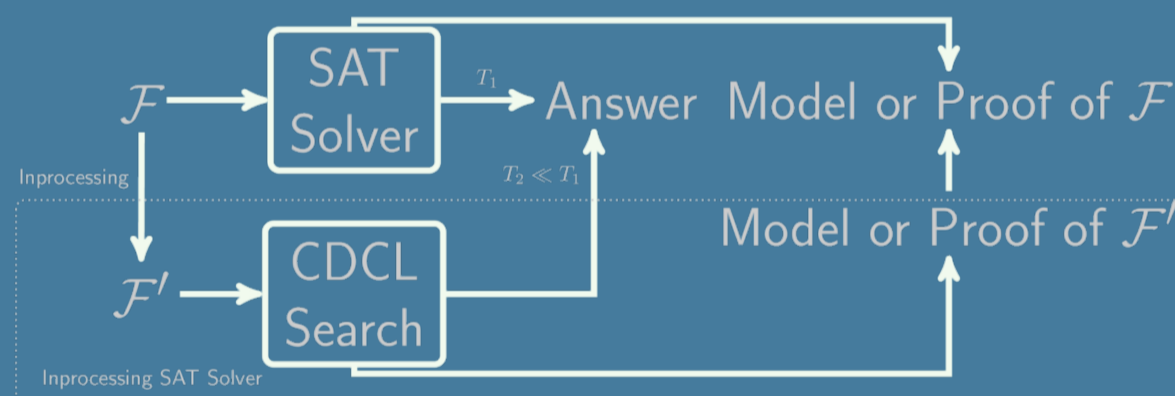
1. Allow full inprocessing as in non-incremental solvers
2. When a new formula is added, identify the potentially problematic previous simplification steps
3. Undo each problematic simplification step

## Formula Pre- and Inprocessing

• Formula simplifications before and during search

$$\mathcal{F} = C_1 \wedge C_2 \wedge C_3 \wedge C_4 \wedge C_5 \quad \mathcal{F} \equiv_{\text{sat}} \mathcal{F}'$$

$$\mathcal{F}' = C_1 \wedge C_2' \wedge C_3 \wedge C_4' \wedge C_5 \wedge C_6$$



## Non-Incremental vs. Incremental Reasoning



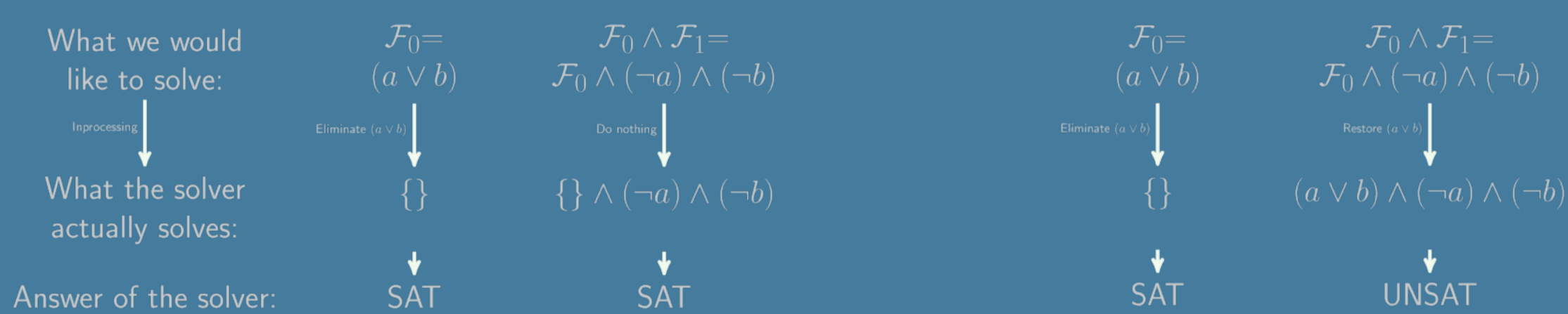
- Solve each formula with the exact same solver
- Can reuse reasoning steps instead of repeating them
- Leads to significant speed-up in practice

## Problem with Inprocessing in Incremental Reasoning - Example

$$\mathcal{F}_0 \wedge \mathcal{F}_1 \not\equiv_{\text{sat}} \mathcal{F}_0' \wedge \mathcal{F}_1$$

### Unsound solution

### Our sound solution

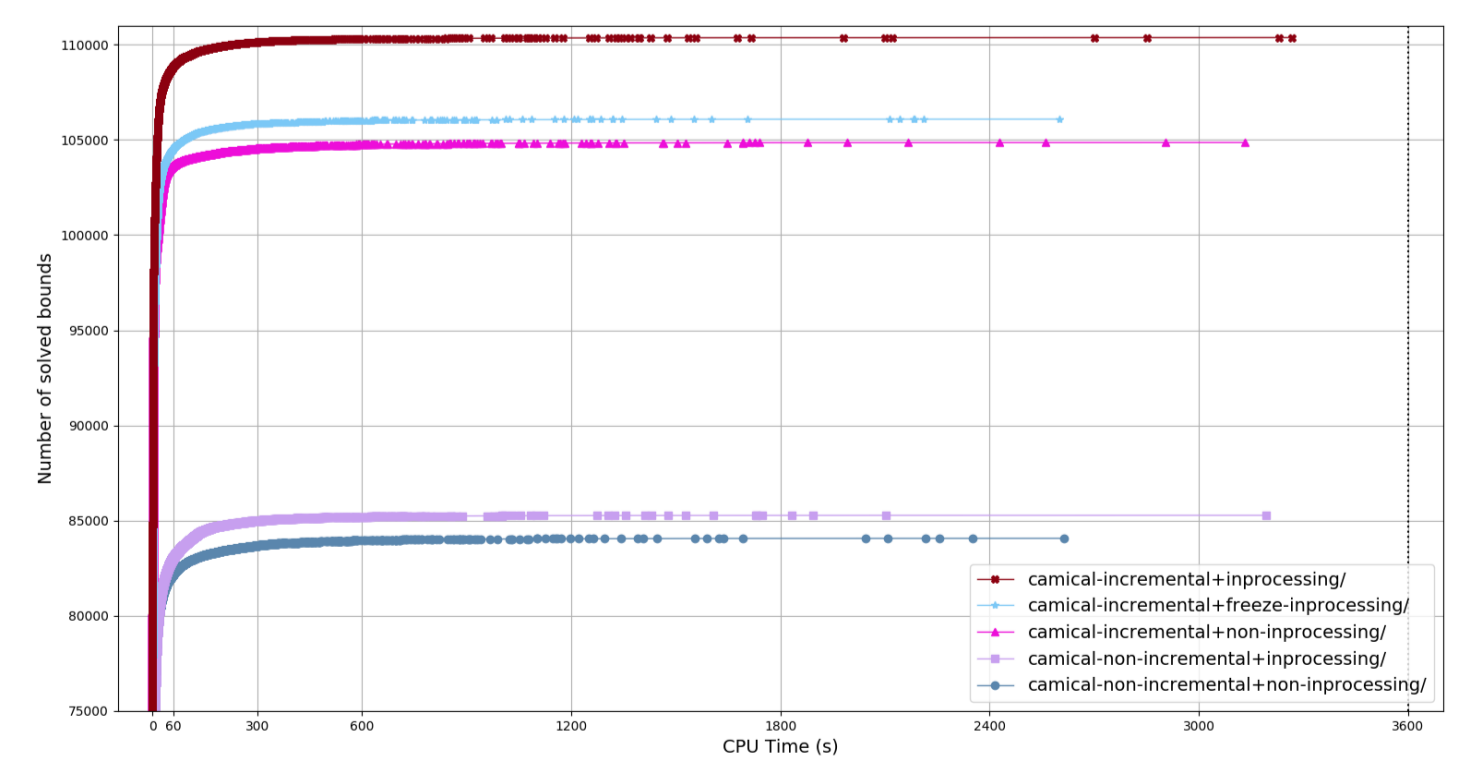


## Sound Incremental Inprocessing Rules

$\frac{\varphi[\rho]\sigma}{\varphi[\rho \wedge C]\sigma} \text{ [L]}$	$\frac{\varphi[\rho \wedge C]\sigma}{\varphi[\rho]\sigma} \text{ [F]}$	$\frac{\varphi[\rho \wedge C]\sigma}{\varphi \wedge C[\rho]\sigma} \text{ [S]}$	$\frac{\varphi[\rho]\sigma}{\varphi \wedge \Delta[\rho]\sigma} \text{ [A]}$
LEARN <sup>-</sup>	FORGET	STRENGTHEN	ADDCLAUSES
$\frac{\varphi \wedge C[\rho]\sigma}{\varphi[\rho]\sigma \cdot (\omega : C)} \text{ [W]}$	$\frac{\varphi \wedge C[\rho]\sigma}{\varphi[\rho]\sigma} \text{ [D]}$	$\frac{\varphi[\rho]\sigma \cdot (\omega : C) \cdot \sigma'}{\varphi \wedge C[\rho]\sigma \cdot \sigma'} \text{ [R]}$	
WEAKEN <sup>+</sup>	DROP	RESTORE	

where [L] is  $\varphi \wedge \rho \models C$ , [F] is  $\varphi \wedge C \equiv_{\text{sat}} \varphi$ , [S] is  $\varphi \models C$ ,  
 [W] is C is clean w.r.t.  $\sigma'$  and [R] is that each clause in  $\Delta$  is clean w.r.t.  $\sigma$

## Experimental Results



Results of CaMiCaL on the 300 instances of the single safety track of HWMCC'17, using CaDiCaL as a back-end SAT solver with different configurations. y-axis: all bounds solved over all problems sorted by the time needed for the SAT call for each bound.