Data Layout from a Type-Theoretic Perspective

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Logic and Computation

• One Important thread:

- **1** Propositions as types
- 2 Proofs as programs
- 3 Reduction as computation
- Co-design programming language and reasoning principles
- Provides some extensibility and robustness
- (1) depends on logic and its vocabulary
- (2,3) depend on details of presentation
- (2,3) yield preservation and progress
- This talk:
 - Fix the logic (= intuitionistic propositional logic)
 - Vary the judgmental principles of proof

Intuitionistic Logic	Functional Programming
Axioms + MP [Hilbert'27]	Combinators [Curry'35]
Natural Deduction [Gentzen'35]	λ -Calculus [Howard'69]
Sequents with Stoop (LJT)	Explicit Substitutions [Herbelin'94]
Semi-Axiomatic Seq Calc (SAX)	Futures [DeYoung,Pf,Pruiksma'20]
SAX with Snip (SNAX)	Data Layout [this talk]

- From Natural Deduction (ND) to Semi-Axiomatic Sequent Calculus (SAX)
- Programming in SAX
- Cut Elimination and Snips in SAX
- Data Layout and SAX with Snips (SNAX)
- Examples Revisited

Example: Disjunction in Natural Deduction

$$\frac{\Gamma \vdash A}{\Gamma \vdash A \lor B} \lor I_{1} \quad \frac{\Gamma \vdash B}{\Gamma \vdash A \lor B} \lor I_{2} \quad \frac{\Gamma \vdash A \lor B \quad \Gamma, A \vdash C \quad \Gamma, B \vdash C}{\Gamma \vdash C} \lor E$$

$$\frac{\Gamma \vdash M_{1} : A}{\Gamma \vdash 1(M_{1}) : A \lor B} \lor I_{1} \quad \frac{\Gamma \vdash M_{2} : B}{\Gamma \vdash r(M_{2}) : A \lor B} \lor I_{2}$$

$$\frac{\Gamma \vdash M : A \lor B \quad \Gamma, x : A \vdash N_{1} : C \quad \Gamma, y : B \vdash N_{2} : C}{\Gamma \vdash \text{case } M(1(x) \Rightarrow N_{1} \mid r(y) \Rightarrow N_{2}) : C} \lor E$$

$$\frac{\text{case } 1(M_{1})(1(x) \Rightarrow N_{1} \mid ...) \longrightarrow [M_{1}/x]N_{1}}{\text{case } r(M_{2})(... \mid r(y) \Rightarrow N_{2}) \longrightarrow [M_{2}/y]N_{2}}$$

Fundamental operation underlying reduction

- Substitution of proof for hypothesis (logic)
- Substitution of term for variable (computation)
- Derives from meaning of hypothetical judgments

Define futures with

$$x \leftarrow P$$
; Q

- Allocate a fresh future for x
- Compute P with destination x
- In parallel, compute Q which may read from x
- Q blocks if it tries to read from x before P has written to x

Scheduling

- Futures: compute *P* and *Q* in parallel
- Call-by-value: complete *P* before starting *Q*
- Call-by-need: postpone P until Q needs x

Logically, futures are a cut from the sequent calculus

$$\frac{P}{\Gamma \vdash A} \quad \frac{Q}{\Gamma \vdash C} \text{ cut } \frac{\Gamma \vdash P :: (x : A) \quad \Gamma, x : A \vdash Q :: (w : C)}{\Gamma \vdash x \leftarrow P ; Q :: (w : C)} \text{ cut }$$

• Interpret sequents with addresses a_i and w

$$\underbrace{a_1: A_1, \dots, a_n: A_n}_{\text{read from}} \vdash P :: (\underbrace{w: C}_{\text{write to}})$$

• Types A_i and C represent types of value at address a_i and w

Not Quite the Sequent Calculus

- Writer terminates after writing to destination
- Example: Disjunction / Sums

$$\overline{a:A\vdash \mathsf{write}\ c\ \mathtt{l}(a)::(c:A\lor B)} \lor X_{\mathtt{l}}$$

$$\overline{b: B \vdash \mathsf{write} \ c \ r(b) :: (c: A \lor B)} \lor X_2$$

a, b, and c are addresses: values are small (1(a) and r(b))
Reader continues based on value read

$$\frac{\Gamma, x : A \vdash Q :: (w : C) \quad \Gamma, y : B \vdash R :: (w : C)}{\Gamma, c : A \lor B \vdash \text{read } c \ (1(x) \Rightarrow Q \mid r(y) \Rightarrow R) :: (w : C)} \lor L$$

Specifying Dynamics as Multiset Rewriting

- State of computation represented as a multiset of objects
- Any subset can be rewritten by a transition rule
- For shared memory dynamics
 - Ephemeral objects thread P represent running threads
 - Ephemeral objects cell c □ were allocated but not yet written
 - Persistent objects !cell c V were allocated and written

Sample rules (disjunction/sums)

thread (write $c \ l(a)$), cell $c \square \longrightarrow !$ cell $c \ l(a)$ thread (write $c \ r(b)$), cell $c \square \longrightarrow !$ cell $c \ r(b)$

!cell $c \ l(a)$, thread (read $c \ (l(x) \Rightarrow Q \mid ...)) \longrightarrow$ thread [a/x]Q!cell $c \ r(b)$, thread (read $c \ (... \mid r(y) \Rightarrow R)) \longrightarrow$ thread [b/y]R

Semi-Axiomiatic Sequent Calculus (SAX)

Disjunction, purely logically

$$\frac{1}{A \vdash A \lor B} \lor X_1 \quad \frac{1}{B \vdash A \lor B} \lor X_2 \quad \frac{\Gamma, A \vdash C \quad \Gamma, B \vdash C}{\Gamma, A \lor B \vdash C} \lor L$$

General rules

$$\frac{\Gamma \vdash A \quad \Gamma, A \vdash C}{\Gamma \vdash C} \text{ cut } \qquad \frac{}{A \vdash A} \text{ id}$$

Leave weakening and contraction implicit, for conciseness

- All positive right and negative left rules become axioms
- [DeYoung, Pf, Pruiksma; FSCD 2020]

SAX, Positive Connectives

• Positive connectives (\lor , \otimes , 1)

Right rules are noninvertible*, become axioms ∨X_i, ⊗X, 1X
Left rules are invertible, remain ∨L, ⊗L, 1L

$$\frac{1}{A \vdash A \lor B} \lor X_{1} \quad \frac{1}{B \vdash A \lor B} \lor X_{2} \qquad \frac{\Gamma, A \vdash C \quad \Gamma, B \vdash C}{\Gamma, A \lor B \vdash C} \lor L$$

$$\frac{1}{A, B \vdash A \otimes B} \otimes X \qquad \frac{\Gamma, A, B \vdash C}{\Gamma, A \otimes B \vdash C} \otimes L$$

$$\frac{1}{\Box \vdash 1} 1X \qquad \frac{\Gamma \vdash C}{\Gamma, 1 \vdash C} 1L$$

- Negative connectives (\supset, \land)
- Right rules are invertible, remain $\supset R$, $\land R$
- Left rules are noninvertible, become axioms $\supset X$, $\land X_i$

$$\frac{\Gamma, A \vdash B}{\Gamma \vdash A \supset B} \supset R \qquad \frac{\overline{A, A \supset B \vdash B}}{\overline{A, A \supset B \vdash B}} \supset X$$

$$\frac{\Gamma \vdash A \quad \Gamma \vdash B}{\Gamma \vdash A \land B} \land R \qquad \frac{\overline{A \land B \vdash A}}{\overline{A \land B \vdash A}} \land X_1 \quad \frac{\overline{A \land B \vdash B}}{\overline{A \land B \vdash B}} \land X_2$$

Back to Futures

Generalize $A \lor B$ to $\sum_{\ell \in L} (\ell : A_{\ell})$, where $A \lor B = (1 : A) + (r : B)$ Generalize $A \wedge B$ to $\&_{\ell \in L}(\ell : A_{\ell})$, where $A \wedge B = (1 : A) \& (r : B)$ Labels/Tags ℓ, k Addresses a, b, c, d, w : x, y, zSmall valuesV ::= k(a) $(\sum_{\ell} (\ell : A_{\ell}))$ $(\otimes_{\ell} (\ell : A_{\ell}))$ $| \langle a, b \rangle$ $(A \otimes B)$ $(A \supset B)$ $| \langle \rangle$ (1)Continuations $K ::= (\ell(x) \Rightarrow P_{\ell})_{\ell} (\Sigma_{\ell}(\ell : A_{\ell})) (\otimes_{\ell}(\ell : A_{\ell}))$ $| \langle x, y \rangle \Rightarrow P (A \otimes B) (A \supset B)$ $| \langle \rangle \Rightarrow P (1)$ P ::= write a V $(\Sigma, \otimes, 1)$ Processes read a K $(\Sigma, \otimes, 1)$ write a K $(\&,\supset)$ $(\&,\supset)$ read a V $x \leftarrow P$; Q (allocate x) copy a b (copy to a from b)

Cut/Allocate and Identity/Copy

- Cut/Allocate: $x \leftarrow P$; Q (only form of allocation)
- P writes to x, Q may read from x

$$\frac{\Gamma \vdash P :: (x : A) \quad \Gamma, x : A \vdash Q :: (w : C)}{\Gamma \vdash (x \leftarrow P; Q) :: (w : C)} \text{ cut}$$

thread $(x \leftarrow P; Q) \longrightarrow$ thread [a/x]P, cell $a \Box$, thread [a/x]Q(a fresh)

Id/Copy: copy a b
Copy to a from b

$$\overline{\Gamma, b: A \vdash \mathbf{copy} \ a \ b :: (a:A)}$$
 Ic

!cell *b V*, thread (**copy** *a b*), cell $a \Box \longrightarrow$!cell *a V*

Generic over values and continuations

- thread (write a V), cell $a \Box \longrightarrow$!cell a Vthread (read a K), cell $a V \longrightarrow$ thread ($V \triangleright K$)
- thread (write a K), cell $a \Box \longrightarrow$!cell a Kthread (read a V), cell $a K \longrightarrow$ thread ($V \triangleright K$)

Passing values to continuations

$$k(a) \triangleright (\ell(x) \Rightarrow P_{\ell})_{\ell \in L} = [a/x]P_{k}$$

$$\langle a, b \rangle \triangleright (\langle x, y \rangle \Rightarrow P) = [a/x, b/y]P_{k}$$

$$\langle \rangle \triangleright (\langle \rangle \Rightarrow P) = P$$

• $\llbracket e \rrbracket d = P$ where P computes the value of e with destination d

[[x]] d	=	copy <i>d x</i>
$\llbracket \langle e_1, e_2 angle rbracket d$	=	$x_1 \leftarrow \llbracket e_1 rbracket x_1$;
		$x_2 \leftarrow \llbracket e_2 rbracket x_2$;
		write $d \langle x_1, x_2 \rangle$
$\llbracket case \ e \ (\langle x, y \rangle \Rightarrow e') \rrbracket d$	=	$z \leftarrow \llbracket e rbracket z$;
		read z ($\langle x, y \rangle \Rightarrow \llbracket e' \rrbracket d$)
[[<i>e</i> ₁ <i>e</i> ₂]] <i>d</i>	=	read $z (\langle x, y \rangle \Rightarrow \llbracket e' \rrbracket d)$ $x_1 \leftarrow \llbracket e_1 \rrbracket x_1;$
$\llbracket e_1 \ e_2 \rrbracket d$	=	
$\llbracket e_1 \ e_2 \rrbracket d$	=	$x_1 \leftarrow \llbracket e_1 \rrbracket x_1$;

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Let's Program: Operations on Bits

```
• Type definitions t = A
   • Cell definitions c : A = P (A positive)
   Process definitions f w x_1 \dots x_n = P
          • Requires x_1 : A_1, \ldots, x_n : A_n \vdash P :: (w : C)
          First "argument" is always the destination
Unit : 1 = write Unit \langle \rangle
bool = (false:1) + (true:1)
False : bool = write False false(Unit)
True : bool = write True true(Unit)
b : bool \vdash neg :: (c : bool)
neg c b =
  read b (false(u) \Rightarrow write c true(u)
            | \operatorname{true}(u) \Rightarrow \operatorname{write} c \operatorname{false}(u) )
b : bool, c : bool \vdash or :: (d : bool)
or d b c =
  read b (false(u) \Rightarrow copy d c
            | \operatorname{true}(u) \Rightarrow \operatorname{write} d \operatorname{true}(u) )
```

Let's Program: Binary Successor

Type and process definitions may be recursive (beyond logic) bin = (b0 : bin) + (b1 : bin) + (e : 1)Six : bin = $x_0 \leftarrow$ write $x_0 \in$ (Unit); % !cell c_0 e(Unit) $x_1 \leftarrow$ write $x_1 b1(x_0)$; % !cell $c_1 b1(c_0)$ $x_2 \leftarrow$ write x_2 b1 (x_1) ; % !cell c_2 b1 (c_1) write Six $bO(x_2)$ % !cell Six $bO(c_2)$ x : bin \vdash succ :: (y : bin) succ y x =read x ($b0(x') \Rightarrow$ write y b1(x') $|b1(x') \Rightarrow y' \leftarrow succ y' x';$ % allocate destination y' for succ write y b0(y') $| e(u) \Rightarrow$ write y e(u)) x : bin \vdash plus2 :: (z : bin) % a trivial pipeline plus2 z x = $y \leftarrow \operatorname{succ} y x$; succ z y

Example: Binary Tries



```
\mathsf{trie} = (\texttt{leaf}:1) + (\texttt{node}:\texttt{bool} \otimes (\mathsf{trie} \otimes \mathsf{trie}))
x : bin \vdash singleton :: (t : trie)
singleton t x =
    read x ( b0(x') \Rightarrow t_0 \leftarrow singleton t_0 x';
                                        p \leftarrow write p \langle t_0, \text{Leaf} \rangle;
                                        n \leftarrow write n \langle False, p \rangle ;
                                        write t node(n)
                  | b1(x') \Rightarrow t_1 \leftarrow singleton t_1 x';
                                        p \leftarrow write p \langle \text{Leaf}, t_1 \rangle;
                                        n \leftarrow \text{write } n \langle \text{False}, p \rangle;
                                       write t \operatorname{node}(n)
                  | e(u) \Rightarrow p \leftarrow write p \langle Leaf, Leaf \rangle;
                                        n \leftarrow write n \langle \text{True}, p \rangle;
                                        write t \operatorname{node}(n))
```

Let's Program: Union of Binary Tries

```
s : trie, t : trie \vdash union :: (u : trie)
union \mu s t =
    read s (leaf(_) \Rightarrow copy u t
                  node(m) \Rightarrow read m (\langle b, p \rangle \Rightarrow read p (\langle s_0, s_1 \rangle \Rightarrow
    read t (leaf(_) \Rightarrow copy u s
                | \text{node}(n) \Rightarrow \text{read } n (\langle c, q \rangle \Rightarrow \text{read } q (\langle t_0, t_1 \rangle \Rightarrow
    d \leftarrow \text{or } d b c:
    u_0 \leftarrow \text{union } u_0 s_0 t_0;
    u_1 \leftarrow \text{union } u_1 \ s_1 \ t_1;
    r \leftarrow write r \langle u_0, u_1 \rangle;
    o \leftarrow write o \langle d, r \rangle;
    write u \operatorname{node}(o)))))
x : bin, t : trie \vdash insert :: (u : trie)
insert u \times t =
    s \leftarrow \text{singleton } s \times ;
    union u s t % pipeling possible here
```

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What is the Layout of a Trie?

```
The standard "abstract" version requires many cells
   trie = (leaf : 1) + (node : bool \otimes (trie \otimes trie))
   !cell Unit ()
   !cell t leaf(Unit)
   or
   |cell t node(n)|
   !cell n \langle b, p \rangle
   !cell b false(Unit) or !cell b true(Unit)
   !cell p \langle t_0, t_1 \rangle

    Alternative "flat" layout (cell size as subscript)

         |cell_4 t | | leaf |
   or |\text{cell}_4 t | \text{node} \cdot \text{false} \cdot t_0 \cdot t_1 |
   or |\text{cell}_4 t [\text{node} \cdot \text{true} \cdot t_0 \cdot t_1]
How do we get there?
```

Return to Logic for Inspiration

In SAX, standard cut elimination fails

Example

$$\frac{\overline{B, C \vdash B \otimes C} \otimes X}{A, B, C \vdash A \otimes (B \otimes C)} \xrightarrow{\otimes X} \operatorname{cut}_{B \otimes C}$$

In code

```
\begin{array}{l} a:A,b:B,c:C \vdash f :: (d:A \otimes (B \otimes C)) \\ f \ d \ a \ b \ c = \\ b \ c \ \leftarrow \ write \ b \ c \ \langle b, \ c \rangle \\ write \ d \ \langle a, \ b \ c \rangle \end{array}
```

A New Cut-Free Form

- Notice: $B \otimes C$ is a subformula of $A \otimes (B \otimes C)$
- Therefore, $B \otimes C$ is eligible for a cut that preserves the subformula property
 - Special case of an analytic cut
 - Eligible formulas are <u>underlined</u>
 - A cut with an eligible formula is a snip
- Example revisited

$$\frac{\underline{\overline{B}, \underline{C} \vdash B \otimes C} \otimes X}{\underline{A, \underline{B} \otimes C} \vdash A \otimes (B \otimes C)} \otimes X \operatorname{snip}_{B \otimes C}}{\underline{A, \underline{B}, \underline{C} \vdash A \otimes (B \otimes C)}}$$

- Theorem [DeYoung, Pf, Pruiksma'20] Cut-free SAX proofs (possibly with snips) satisfy the subformula property
- Theorem [DeYoung, Pf, Pruiksma'20] If Γ ⊢ A in SAX, then there is a cut-free proof of Γ ⊢ A (possibly with snips).

Eligibility in SAX, Positive Connectives

- We can (implicitly) ignore that a formula is eligible
- We leave weakening and contraction implicit for conciseness
- Positive connectives: right rules become axioms

$$\frac{\overline{A}\vdash A\lor B}{\underline{A}\vdash A\lor B}\lor X_{1} \quad \frac{\overline{B}\vdash A\lor B}{\underline{B}\vdash A\lor B}\lor X_{2} \quad \frac{\overline{\Gamma, A\vdash C} \quad \overline{\Gamma, B\vdash C}}{\overline{\Gamma, A\lor B\vdash C}}\lor L$$

$$\frac{\overline{A, \underline{B}\vdash A\otimes B}}{\underline{A, \underline{B}\vdash A\otimes B}} \otimes X \quad \frac{\overline{\Gamma, A, B\vdash C}}{\overline{\Gamma, A\otimes B\vdash C}} \otimes L$$

$$\frac{\overline{\Gamma}\vdash A \quad \overline{\Gamma, A\vdash C}}{\overline{\Gamma, 1\vdash C}} 1L$$

$$\frac{\overline{\Gamma}\vdash A \quad \overline{\Gamma, A\vdash C}}{\overline{\Gamma\vdash C}} \operatorname{snip}^{+}$$

Eligibility in SAX, Negative Connectives

Negative connectives (left rules become axioms)

$$\frac{\Gamma, A \vdash B}{\Gamma \vdash A \supset B} \supset R \qquad \qquad \overline{\underline{A}, A \supset B \vdash \underline{B}} \supset X$$

$$\frac{\Gamma \vdash A \quad \Gamma \vdash B}{\Gamma \vdash A \land B} \land R \qquad \qquad \overline{\underline{A} \land B \vdash \underline{A}} \land X_1 \quad \overline{\underline{A} \land B \vdash \underline{B}} \land X_2$$

$$\frac{\Gamma \vdash \underline{A} \quad \Gamma, A \vdash C}{\Gamma \vdash C} \operatorname{snip}^-$$

General rules

$$\frac{\Gamma \vdash A \quad \Gamma, A \vdash C}{\Gamma \vdash C} \text{ cut } \qquad \frac{A \vdash A}{A \vdash A} \text{ id}$$

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SNAX and Layout

Consider

$$\frac{\Gamma \vdash A \quad \Gamma, \underline{A} \vdash C}{\Gamma \vdash C} \operatorname{snip}^+$$

- Because <u>A</u> is a subformula of C, it denotes an address at an offset from the address of C
- Snips do not allocate, but provide the location of <u>A</u>
- Example, with |A| = |B| = 1 (where |A| = size of A)

 $\frac{\overline{(\underline{\alpha+1:B}), (\underline{\alpha+2:C}) \vdash (\alpha+1:B \otimes C)} \otimes X}{\underline{(\alpha:A), (\underline{\alpha+1:B \otimes C}) \vdash (\alpha:A \otimes (B \otimes C))}} \otimes X$ snip

 $(\underline{\alpha:A}), (\underline{\alpha+1:B}), (\underline{\alpha+2:C}) \vdash (\alpha:A \otimes (B \otimes C))$

 Drop eligibility from SAX rules for negative types because we do not model layout of continuations

Pointer Values

а

Recall

 $\mathsf{trie} = (\texttt{leaf}:1) + (\texttt{node}:\mathsf{bool}\otimes(\mathsf{trie}\otimes\mathsf{trie}))$

- Both sums and pairs are positive—size would be unbounded
- Introduce positive \$\overline{A}\$ representing pointers
 - Logically, $\downarrow A \equiv A$
 - Computationally, $\downarrow a$ is small value of type $\downarrow A$
 - A is not eligible since a : A not at a fixed offset from $b : \downarrow A$.

$$\frac{1}{A \vdash \downarrow A} \downarrow X \qquad \frac{\Gamma, A \vdash C}{\Gamma, \downarrow A \vdash C} \downarrow L$$

■ We write and read small values ↓a

$$\frac{\Gamma, x : A \vdash P :: (w : C)}{\Gamma, b : \downarrow A \vdash \text{read } b (\downarrow x \Rightarrow P) :: (w : C)} \downarrow L$$

Recursion in type definitions must be guarded by a shift \downarrow .

 $\blacksquare \downarrow$ also includes negative types ($\supset, \land)$ in positive ones

Examples Revisited

- Pointer tag ↓ takes no space at runtime
- cell_n means cell of size n
- Nested pairs
 - Mapping from SAX layout ↓A ⊗ ↓(↓B ⊗ ↓C) !cell₂ c [↓c_A · ↓d] !cell₂ d [↓c_B · ↓c_C]
 Identical flat layouts in SNAX (where n = |A| + |B| + |C|) A ⊗ (B ⊗ C) !cell_n c [V_A · V_B · V_C] (A ⊗ B) ⊗ C !cell_n c [V_A · V_B · V_C]
- Unit and Booleans

```
!cell<sub>0</sub> Unit []
```

```
\begin{split} & \text{bool} = (\texttt{false}:1) + (\texttt{true}:1) \\ & \texttt{!cell_1 False} [\texttt{false}] \\ & \texttt{!cell_1 True} [\texttt{true}] \end{split}
```

■ Note |1| = 0

Examples Revisited: Tries

- Consider two different layout choices; others are possible
- Pointers to subtries

```
trie = (leaf : 1) + (node : bool \otimes (\downarrow trie \otimes \downarrow trie))
              |\text{cell}_4 t | |\text{leaf} \cdot \Box \cdot \Box \cdot \Box|
     or |\text{cell}_4 t | \text{node} \cdot \text{false} \cdot \downarrow t_0 \cdot \downarrow t_1 |
     or |\text{cell}_4 t [\text{node} \cdot \text{true} \cdot \downarrow t_0 \cdot \downarrow t_1]
Pointers to nodes
     trie = (leaf : 1) + (node : \downarrow node)
     node = bool \otimes (trie \otimes trie)
              or |\text{cell}_2 t | \text{node} \cdot \downarrow n |
              |\text{cell}_5 n | \text{false} \cdot | \text{eaf} \cdot \Box \cdot | \text{eaf} \cdot \Box |
              |\text{cell}_5 n | \text{true} \cdot \text{node} \cdot \downarrow n_0 \cdot \text{node} \cdot \downarrow n_1 |
     or
     or
              . . .
```

Some space optimizations on sums may apply

Layout Rules without Process Terms (Positive Connectives)

$$\frac{(k \in L)}{(\underline{a+1}:A_k) \vdash a: \sum_{\ell \in L} (\ell:A_\ell)} + X \quad \frac{\Gamma, (\underline{a+1}:A_\ell) \vdash w: C \quad (\text{for all } \ell \in L)}{\Gamma, (\underline{a}: \sum_{\ell \in L} (\ell:A_\ell)) \vdash w: C} + L$$

$$\frac{(\underline{a+1}:A_k) \vdash a: \sum_{\ell \in L} (\ell:A_\ell)}{(\underline{a+|A|:B|} \vdash a:A \otimes B} \otimes X \quad \frac{\Gamma, (\underline{a}:A), (\underline{a+|A|:B|} \vdash w:C}{\Gamma, (\underline{a}:A \otimes B) \vdash w:C} \otimes L$$

$$\frac{(\underline{a:A}), (\underline{a+|A|:B|} \vdash a:A \otimes B}{(\underline{-+a:1}) \vdash a:A \otimes B} \otimes X \quad \frac{\Gamma \vdash w: C}{\Gamma, \underline{a:1} \vdash w:C} \quad 1L$$

$$\frac{\Gamma \vdash a:A \quad \Gamma, (\underline{a:A}) \vdash w:C}{\Gamma \vdash w:C} \quad \text{snip} \quad \frac{\Gamma \vdash \alpha:A \quad \Gamma, \alpha:A \vdash w:C \quad \alpha \text{ fresh}}{\Gamma \vdash w:C} \quad \text{cut}$$

Cut/allocate requires size or type

$$\frac{\Gamma \vdash P :: (\alpha : A) \quad \Gamma, \alpha : A \vdash Q :: (c : C) \quad (\alpha \text{ fresh})}{\Gamma \vdash \alpha_{|A|} \leftarrow P \text{ ; } Q :: (c : C)} \text{ cut}$$

Snip no longer allocates memory

$$\frac{\Gamma \vdash P :: (a:A) \quad \Gamma, (\underline{a:A}) \vdash Q :: (c:C)}{\Gamma \vdash P; Q :: (c:C)} \text{ snip}$$

Identity/copy requires size or type

$$\overline{b: A \vdash \mathbf{copy}_{|A|} \ a \ b :: (a:A)}$$
 id

Cut and snip can still be parallel, call-by-value, or call-by-need

Eligibility is sharpened

- We can no longer silently drop or ignore it
- Instead, we use snip with identity

$$\frac{\overline{b:A \vdash \mathbf{copy}_{|A|} \ a \ b :: (a:A)} \quad \text{id} \quad \Gamma, \underline{a:A} \vdash P :: (w:C)}{\Gamma, b:A \vdash \mathbf{copy}_{|A|} \ a \ b \ ; P :: (w:C)} \quad \text{snip}$$

- ⊗X, ⊗L, 1X, and 1L just calculate addresses and have no operational significance!
- +X, +L, $\downarrow X$, $\downarrow R$ remain
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- Conclusion

```
\begin{aligned} &\text{bool} = (\text{false}:1) + (\text{true}:1) \\ &b: \text{bool} \vdash \text{neg}::(c: \text{bool}) \\ &\text{neg } c \ b = \\ & \textbf{read } b \ (\text{false} \Rightarrow \textbf{write } c \ \text{true} \\ & | \ \text{true} \Rightarrow \textbf{write } c \ \text{false} \ ) \end{aligned}
\begin{aligned} &b: \text{bool}, c: \text{bool} \vdash \text{or}:: (d: \text{bool}) \\ &\text{or } d \ c \ b = \\ & \textbf{read } b \ (\text{false} \Rightarrow \textbf{copy}_{|\text{bool}|} \ d \ c \\ & | \ \text{true} \Rightarrow \textbf{write } d \ \text{true} \ ) \end{aligned}
```

• For simplicity, tags and pointers all have size 1

$$\begin{aligned} |1| &= 0\\ |A \otimes B| &= |A| + |B|\\ |\sum_{\ell \in L} (\ell : A_{\ell})| &= 1 + \max_{\ell} (A_{\ell})\\ |\downarrow A| &= 1 \end{aligned}$$

• Type-directed definition of copying is shallow η -expansion $s: A \vdash \operatorname{copy}_A :: (d: A)$ $\operatorname{copy}_1 ds = (noop)$ $\operatorname{copy}_{A \otimes B} ds = \operatorname{copy}_A ds$; $\operatorname{copy}_B (d + |A|) (s + |A|)$ $\operatorname{copy}_{\sum_{\ell \in L} (\ell:A_\ell)} ds = \operatorname{read} s \ (\ell \Rightarrow \operatorname{write} d\ell; \operatorname{copy}_{A_\ell} (d + 1) \ (s + 1))_{\ell \in L}$ $\operatorname{copy}_{\downarrow A} ds = \operatorname{read} s \ (\downarrow x \Rightarrow \operatorname{write} d \downarrow x)$

May be implemented more efficiently

Example Revisited: Binary Numbers

```
bin = (b0 : \downarrow bin) + (b1 : \downarrow bin) + (e : 1)
Six : bin =
   x_0 \leftarrow write x_0 \in ;
   x_1 \leftarrow ( write x_1 b1; write (x_1+1) \downarrow x_0);
   x_2 \leftarrow ( write x_2 b1; write (x_2+1) \downarrow x_1);
   write Six b0; write (Six+1) \downarrow x_2
x : bin \vdash succ :: (y : bin)
succ y x =
   read x ( b0 \Rightarrow write y b1 ;
                          copy_{|\downarrow,bin|}(y+1)(x+1) % |\downarrow bin| = 1
              | b1 \Rightarrow read (x+1) (\downarrow x' \Rightarrow
                         v' \leftarrow \operatorname{succ} v' x':
                          write y b0;
                          write (y+1) \downarrow y'
               | e \Rightarrow write y e )
```

Example Revisited: Tries

```
trie = (leaf : 1) + (node : bool \otimes (\downarrow trie \otimes \downarrow trie))
x : \downarrow \text{bin} \vdash \text{singleton} :: (t : \downarrow \text{trie})
singleton t x =
   read x (\downarrow n \Rightarrow
   read n ( b0 \Rightarrow m \leftarrow ( write m node ;
                                 write (m+1) false;
                                 singleton (m+2)(n+1);
                                 write (m+3) Leaf);
                       write t \downarrow m
             | b1 \Rightarrow m \leftarrow (write m node;
                                 write (m+1) false;
                                 write (m+2) Leaf;
                                 singleton (m+3)(n+1);
                       write t \downarrow m
             | e \Rightarrow m \leftarrow ( write m node;
                                 write (m+1) true;
                                 write (m+2) Leaf
                                 write (m+3) Leaf);
                       write t \downarrow m)
```

Example Revisited: Union of Binary Tries

```
s: \downarrowtrie, t: \downarrowtrue \vdash union :: (u: \downarrowtrie)
union u \ s \ t =
   read s (\downarrow s' \Rightarrow read s' (leaf \Rightarrow copy<sub>lltriel</sub> u t
                                                                                          |\downarrow_{-}|=1
                                         node \Rightarrow
   read t (\downarrow t' \Rightarrow read t' (leaf \Rightarrow copy<sub>|.ltrie|</sub> u s
                                         node \Rightarrow
   d \leftarrow \text{or } d(s'+1)(t'+1);
   u_0 \leftarrow \text{union } u_0 (s'+2) (t'+2):
   u_1 \leftarrow \text{union } u_1 (s'+3) (t'+3);
   u' \leftarrow ( write u' node ;
               write (u'+1) d:
               write (u'+2) u_0;
               write (u'+3) u_1);
   write u \downarrow u'))))
x: \downarrow bin, t: \downarrow trie \vdash insert :: (u: \downarrow trie)
insert u \times t =
   s \leftarrow \text{singleton } s \times ;
   union u s t
                      % pipelining possible here!
```

We can still give a parallel semantics

- Synchronize only on sums and shifts (pairs and unit are silent)
- Must be able to recognize an unwritten tag or pointer
- Faithful to SAX since the straightforward translation inserts a shift before every sub-type
- Similarly for call-by-need schedule
- Call-by-value schedule guarantees a cell is written before any attempt to read from it

Layout Identities $A \equiv B$

- In a functional language: $A \leq B$ if v : A implies v : B
- Example:

```
\begin{aligned} \mathsf{nat} &= (z:1) + (s:\mathsf{nat}) \\ \mathsf{even} &= (z:1) + (s:\mathsf{odd}) \\ \mathsf{odd} &= (s:\mathsf{even}) \end{aligned}
```

```
\mathsf{even} \leq \mathsf{nat} \; \mathsf{and} \; \mathsf{odd} \leq \mathsf{nat}
```

- In SNAX, a final configuration *F* contains only !cell *a V* and !cell *b K*.
- In SNAX: $A \leq B$ if $\mathcal{F} :: (c : A)$ implies $\mathcal{F} :: (c : B)$.
- Additional examples for $A \equiv B$ (that is, $A \leq B$ and $B \leq A$)
 - $A \otimes (B \otimes C) \equiv (A \otimes B) \otimes C$
 - $A \otimes 1 \equiv A \equiv 1 \otimes A$
 - $((\texttt{false}:1) + (\texttt{true}:1)) \otimes A \equiv (\texttt{false}:A) + (\texttt{true}:A)$
 - $(A+B)\otimes C \equiv (A\otimes C) + (B\otimes C)$ (if |A| = |B|)

```
■ Internal pointers

a: A \vdash \text{self} :: (p: A \otimes \downarrow A)

self p = a = copy_{|A|} p a;

write (p + |A|) p
```

Boxed vs. unboxed representations of polymorphism

 $\begin{array}{ll} \mathsf{list}_1 \ A = (\mathsf{nil}:1) + (\mathsf{cons}: {\downarrow}A \otimes {\downarrow}\mathsf{list}_1 \ A) & \% \ |\mathit{list}_1 \ A| = 3 \\ \mathsf{list}_2 \ A = (\mathsf{nil}:1) + (\mathsf{cons}: A \otimes {\downarrow}\mathsf{list}_2 \ A) & \% \ |\mathit{list}_2 \ A| = |A| + 2 \\ \mathsf{list}_3 \ A = (\mathsf{nil}:1) + (\mathsf{cons}: {\downarrow}\mathsf{node}_3 \ A) & \% \ |\mathit{list}_3 \ A| = 2 \\ \mathsf{node}_3 \ A = {\downarrow}A \otimes \mathsf{list}_3 \ A & \% \ |\mathsf{node}_3 \ A| = 3 \end{array}$

Summary

- Fix propositional intuitionistic logic
- Natural deduction (ND)
 - Introduction and elimination rules
 - Computation by substitution
 - "Large" values v
- Semi-axiomatic sequent calculus (SAX)
 - Axioms for non-invertible rules ($\otimes R, 1R, \forall R, \supset L, \land L$)
 - Cut elimination and subformula property via snips
 - Computation via futures (write-once shared memory with concurrent threads)
 - Call-by-value and call-by-need as particular schedules
- Semi-axiomatic sequent calculus with snips (SNAX)
 - Layout for positive types ($\otimes, 1, \Sigma$) is flat
 - Type $\downarrow A$, logically equivalent to A
 - Computationally, value of type $\downarrow A$ is an address
 - \blacksquare Pairs (\otimes) and unit (1) become computationally irrelevant
 - Admits futures, call-by-value, call-by-need

Further Related Work

[Morrisett; PhD 1995] Compiling with Types

- Data layout is significant for performance
- Not explicit in the type
- [Tarditi, Morrisett, Cheng, Stone, Harper, Lee; PLDI 1996]
 TH: A Type Directed Optimizing Compiler for MI
 - TIL: A Type-Directed Optimizing Compiler for ML
 - Importance of typed intermediate languages
- [Morrisett, Walker, Crary, Glew; TOPLAS 1999]
 From System F to Typed Assembly Language
 - Richer type system (e.g., polymorphism, closures)
 - Lower-level code (RISC-like instruction set)
 - Connection to high-level proof systems only via translation
 - Continuation-passing vs. destination-passing
 - No parallelism, layout not a point of emphasis

- [Petersen, Harper, Crary, Pf; POPL 2003] A Type Theory for Memory Allocation and Data Layout
 - Approach based on ordered logic (no weakening, contraction, exchange)
 - Worked well as far as it went
 - Limitation: adjacency is not an intrinsic property of ordered logic
- Many works on data description languages

- SNAX satisfies the usual preservation and progress (in progress)
- Logical/type-theoretic foundation enable generalizations
- Indexed types to allow flat layouts ("array") seq $A \ n = (nil: (n = 0) \otimes 1) + (cons: (n > 0) \otimes A \otimes seq A (n - 1))$ sequence $A = (n: nat) \otimes seq A n$
- Polymorphism
- Concrete representation of values of negative type ("closures")

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