

AIDA-2020-MS7

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Advanced European Infrastructures for Detectors at Accelerators

Milestone Report

Simulation workshop on HV/HR-CMOS TCAD and Geant4 simulations

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26 May 2016



The AIDA-2020 Advanced European Infrastructures for Detectors at Accelerators project has received funding from the European Union's Horizon 2020 Research and Innovation programme under Grant Agreement no. 654168.

This work is part of AIDA-2020 Work Package 6: **Novel high voltage and resistive CMOS sensors.**

The electronic version of this AIDA-2020 Publication is available via the AIDA-2020 web site <http://aida2020.web.cern.ch> or on the CERN Document Server at the following URL: <http://cds.cern.ch/search?p=AIDA-2020-MS7>

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Advanced European Infrastructures for Detectors at Accelerators
Horizon 2020 Research Infrastructures project AIDA-2020

MILESTONE REPORT

SIMULATION WORKSHOP ON HV/HR-CMOS TCAD AND GEANT4 SIMULATIONS

MILESTONE: MS7

Document identifier:	AIDA-2020-MS7
Due date of milestone:	End of Month 6 (October 2015) – <i>Delayed to M13 (May 2016)</i>
Justification for delay:	The simulation workshop milestone needed to include results for the CMOS ATLAS demonstrators to validate the simulation tools. Since these results were expected in March 2016 and the workshop was organised on 12-13 May 2016, the milestone was postponed to M13.
Report release date:	25/05/2016
Work package:	WP6: Novel high voltage and resistive CMOS sensors
Lead beneficiary:	CNRS-CPPM
Document status:	Final

Abstract:

The workshop was focused on the methodology for simulating the performance of HV and HR CMOS detectors fabricated in various CMOS technology node (AMS 0.35 and 0.18 nm, LF 150 nm, etc.) using TCAD and Geant4. It was organised by WP6.

AIDA-2020 Consortium, 2016

For more information on AIDA-2020, its partners and contributors please see www.cern.ch/AIDA2020

The Advanced European Infrastructures for Detectors at Accelerators (AIDA-2020) project has received funding from the European Union's Horizon 2020 Research and Innovation programme under Grant Agreement no. 654168. AIDA-2020 began in May 2015 and will run for 4 years.

Delivery Slip

	Name	Partner	Date
Authored by	G. Casse I. Peric	UNILIV KIT	25/05/16
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Reviewed by	F. Sefkow [Scientific coordinator]	DESY	26/05/16
Approved by	F. Sefkow [Scientific coordinator]		26/05/16

1. WORKSHOP SUMMARY

1.1. OBJECTIVES

The two days' workshop took place at the Amphitheater, Centre de Physique des Particules de Marseille (CPPM), 163, avenue de Luminy, Marseille (FR) on the 12-13th of May 2016. The various aspects covered by the workshop included introductions for putting the simulation activity in the context of LHC experiments and the role of Geant-4 simulations in this context. It was followed by the introduction of device simulations using TCAD tools, comparison with measurements and hands-on sessions on TCAD simulation practice.

1.2. DETAILS ABOUT THE MEETING

Type of activity	Thematic Workshop
Title	WP6 workshop on simulations
Date	12-13 May 2016
Place	Marseille (FR)
Type of audience	Scientific community, ESRs
Size of audience	25
Scope of the workshop	International
Link	https://indico.cern.ch/event/497449/

1.3 AGENDA

Thursday, 12 May 2016

09:00 → 09:10	Welcome 10m
09:10 → 09:40	Pixels needed for LHC flavour tagging 30m <i>Speaker: Laurent Vacavant (CPPM, Aix-Marseille Université, CNRS/IN2P3 (FR))</i>
09:40 → 10:10	ATLAS CMOS pixel program 30m <i>Speaker: Marlon B. Barbero (CPPM - CNRS/IN2P3 / Aix-Marseille Université (FR))</i>
10:10 → 10:40	Simulation of silicon detector with GEANT-4 (part1) 30m <i>Speaker: Fares Djama (Universite d'Aix - Marseille II (FR))</i>
10:40 → 11:00	Coffee break
11:00 → 12:30	Simulation of particle detector with GEANT-4 (part2) 1h 30m <i>Speakers: Fares Djama (Universite d'Aix - Marseille II (FR))</i>

12:30 → 14:00	Lunch
14:00 → 14:30	Pixel simulation studies and performance with HV-CMOS 30m <i>Speaker: Alessandro Calandri (CPPM, Aix-Marseille Université, CNRS/IN2P3 (FR))</i>
14:30 → 15:15	CMOS perspectives and what do we need to simulate 45m <i>Speaker: Walter Snoeys (CERN)</i>
15:15 → 15:45	TCAD infrastructure, tips and tricks for successful simulation 30m <i>Speaker: Mathieu Benoit (Universite de Geneve (CH))</i>
15:45 → 16:15	TCAD simulation of LF CCPD 30m <i>Speaker: Jian Liu (CNRS (FR))</i>
16:15 → 16:45	Coffee break
16:45 → 17:05	TCAD Simulation of CCPDv3 and C3PD for CLIC 20m <i>Speaker: Matthew Daniel Buckland (University of Liverpool (GB))</i>
17:05 → 17:25	TCAD Simulation of SiGe CMOS Monolithic integration and AMS HV-CMOS Sensors 20m <i>Speaker: Francesco Armando Di Bello (Universite de Geneve (CH))</i>
17:25 → 17:45	Experience on the process and electrical simulation of Foundry process 20m <i>Speaker: Lingxin Meng (University of Liverpool/Universite de Geneve)</i>
17:45 → 18:30	Discussions

Friday, 13 May 2016

09:00 → 11:00	Hands on TCAD simulation (part1) 2h <i>Speakers: Jian Liu (CNRS (FR)), Mathieu Benoit (UNIGE), Patrick Pangaud (CNRS (FR))</i>
11:00 → 11:20	Coffee break
11:20 → 12:30	Hands on TCAD simulation (part2) 1h 10m <i>Speakers: Patrick Pangaud (CNRS (FR)), Jian Liu (CNRS (FR)), Mathieu Benoit (UNIGE)</i>
12:30 → 14:00	Lunch
14:00 → 14:45	HV CMOS results and simulation program 45m <i>Speaker: Ivan Peric (KIT - Karlsruhe Institute of Technology (DE))</i>
14:45 → 15:15	Recent CMOS results and simulations 30m <i>Speaker: Tomasz Hemperek (Universitaet Bonn (DE))</i>
15:15 → 15:35	Coffee break
15:35 → 15:55	KDetSim - a ROOT based detector simulation package 20m <i>Speaker: Gregor Kramberger (Jozef Stefan Institute (SI))</i>
15:55 → 16:15	Edge TCT measurements and simulation of charge collection on HVCMOS detector prototype 20m

	<i>Speakers: Igor Mandic (Jozef Stefan Institute (SI)) , Bojan Hiti (Jozef Stefan Institute (SI))</i>
16:15 → 16:35	Simulation of High Resistivity Pixels 20m <i>Speaker: Stefan Lauxtermann (Sensorscreation)</i>
16:35 → 17:15	Discussions 40m
17:15 → 17:25	Closure

1.4. LIST OF PARTICIPANTS

Name		Institute
Fabrice	Balli	CEA/IRFU,Centre d'etude de Saclay Gif-sur-Yvette (FR)
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