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INTRODUCTION

Many accelerator projects as well as other technologies and research areas involving large electromagnetic fields are beginning to run up against limits in performance imposed by breakdown. In CLIC the rate of breakdown is a critical issue as it results in a loss of acceleration and a transverse kick to the beam which can cause a loss in luminosity.

Results of the RF tests indicate that low group velocity, and consequently narrow bandwidth structures are able to sustain much higher surface fields than high group velocity, large bandwidth, structures [1][2]. This dependency is captured by the high power limits $P/\lambda C$ and S_c presented in [2]. Reference [2] also suggests a physical model to explain the origin of these limits and further study has led to the idea that the process which governs the turn on time is the instantaneous power flow available to feed the breakdown during its onset. In other words a high group velocity structure could more quickly replenish local energy density absorbed by a growing breakdown leading to faster turn on times.

Field emission currents at single emission sites are of the order of pA while the current during breakdown can be hundreds and even thousands of amps and as shown the transition between these two regimes, the turn on time, can be extremely fast. An accurate measure of the rise time of breakdowns under various conditions is an essential step in understanding whether the transient response of RF systems to the breakdown currents determine breakdown limits.

THE CERN DC SPARK SYSTEM

The first CERN DC Spark Systems have been used for conducting quick and cost effective breakdown tests on different materials and to develop a better understanding of breakdown in general. The systems consist of an anode and cathode - the sample under test (copper in this paper) - in a point-plane geometry in ultra high vacuum.[3]

The previous DC spark system at CERN stored the energy needed for a breakdown on a capacitor and used a mechanical relay as the switching mechanism with which to apply the high voltage to the sample under test. This method was limited in two respects: firstly the maximum repetition rate achievable was only around 0.5Hz, thus it would require several weeks of testing to reach a breakdown rate of 10^{-7} per pulse, the region of interest for CLIC; and secondly the frequency response of the circuit was complicated at high frequencies.

Recently a new High Repetition Rate (HRR) system has been designed in which coax and matched impedances are used throughout. The energy for the breakdown is stored on a 200m long coaxial cable known as a Pulse Forming Line (PFL). The mechanical relay has also been replaced by a solid state MOSFET switch which allows the system to operate at up to 1kHz and with a much faster switching time. A detailed description of the HRR system is given in [4].

As indicated in Fig.1 the circuit consists of three parts: the charging section; the energy storage section; and the power delivery section. The charging section contains a power supply whose voltage can be set between zero and 12kV and which can deliver a maximum current of 10mA. This section charges the energy storage section to the selected voltage. The energy storage section contains the PFL, this is where the energy which will be dissipated during a breakdown is stored. The total capacitance of the PFL is 2nF, thus it can store up to ~ 1.4 J at 12kV, the PFL can be totally discharged in 2μ s corresponding to a power of 0.7MW. The power delivery section consists of the MOSFET switch a 50Ω matching resistor and the spark gap.

Whilst the switch is open the PFL charges up to the selected voltage, always 4kV for measurements in this paper. When the switch closes this voltage is all dropped across the gap which normally has a resistance far greater than the 50Ω resistor. If the sample then breaks down the resistance of the gap becomes very low and the PFL discharges through the 50Ω resistor and the spark gap to ground. Once the PFL has fully discharged and the breakdown has extinguished the switch opens and the PFL is charged up again ready for the next pulse.

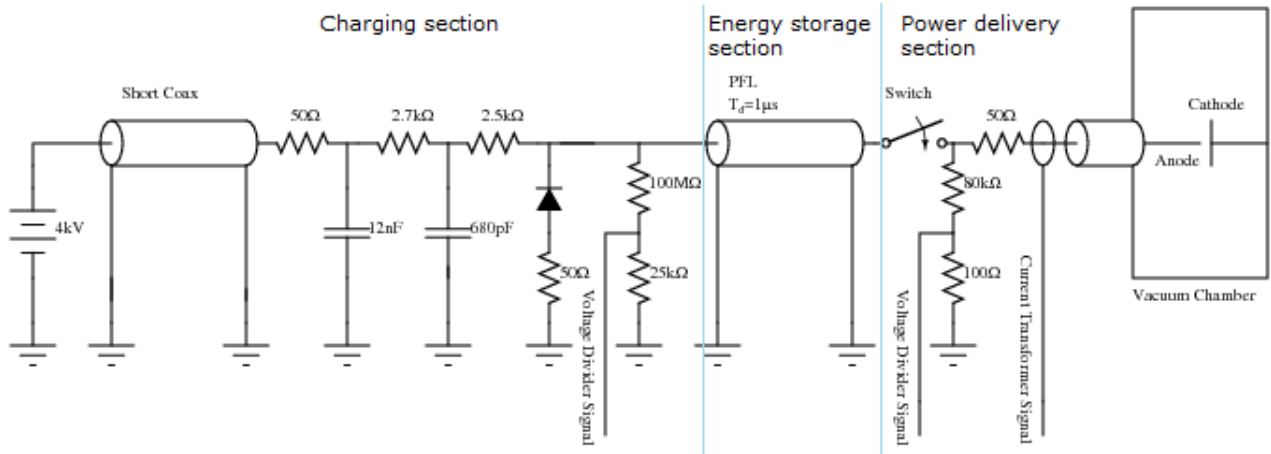


Figure 1: Circuit diagram of the new high-rep-rate CERN DC spark system. Use of coax, matched impedances and the fast switching time of the MOSFET switch allow much better insight into the turn on time of the breakdown.

SIMULATIONS

In order to ensure that the HRR circuit was well understood we modelled a simplified version in PSpice. The results of this simulation are shown in Fig.2. Simplifying assumptions include the characteristics of the MOSFET switch the lack of various stray reactances and the resistance of the PFL which was set to the measured DC value. The spark gap was also modelled as a switch which was closed for breakdown and open otherwise.

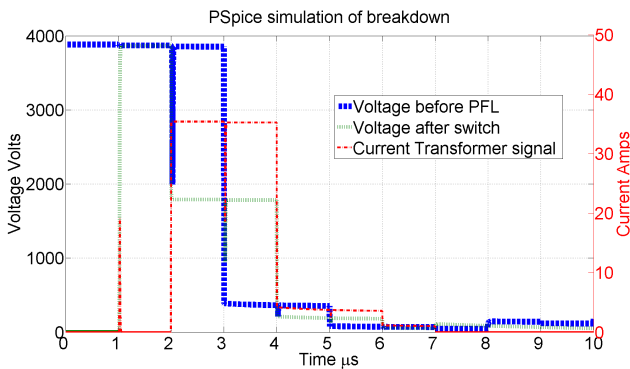


Figure 2: Breakdown simulation

Three signals with which to compare simulation with measurement were chosen, these were the “voltage before the PFL”, the “voltage after the switch” and the “current transformer signal”. The current measured by the Current Transformer (CT) is that which flows through the spark gap (and that required to charge the stray capacitance). The voltage after the switch is a sum of the voltage drop across the spark gap and the 50Ω resistor.

Fig.2 shows the simulated values of these signals during a breakdown. The first current spike is that required to charge stray capacitances between the switch and spark gap. Signals due to the reflection of this spike from the ends of the PFL can be seen on all the measured signals at

multiples of 1μs. The current quickly drops to zero again until the sample breaks down and a larger current flows for 2μs, the time it takes to discharge the PFL.

The voltage after the switch rises to 4kV when the switch is closed before dropping to approximately half that value during the breakdown as the resistance of the gap becomes very small and some of the voltage is dropped across the PFL and most of the rest across the 50Ω resistor.

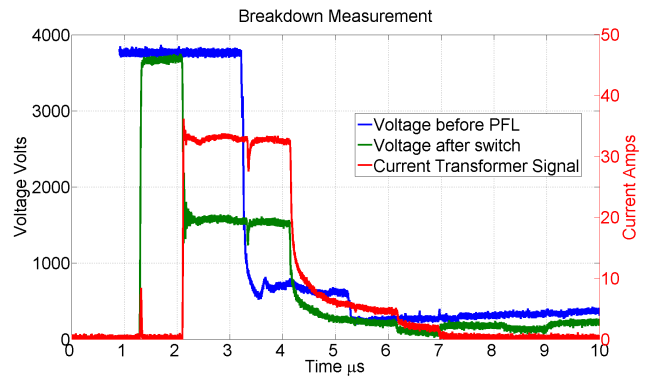


Figure 3: Breakdown measurement.

The voltage before the PFL stays at 4kV for ~ 1μs after the breakdown starts, the time it takes for the signal to propagate down the line before dropping to nearly zero.

CIRCUIT BEHAVIOUR

Fig.3 shows measurements of the circuit behaviour made for comparison to the simulation. Despite the simple model used the agreement between the simulations and measurements was generally good with the same basic features. All the transitions in the real system were generally less sharp than in the simulation and the amplitudes of the reflections were larger and of shorter duration, this is likely due to stray impedances and other bandwidth limitations. We would also expect the measured voltage after the switch to

drop by half following the breakdown but it actually drops to less than this. This is likely due to a slight miss match in impedances somewhere and will be investigated further.

BREAKDOWN TURN ON TIME AND BURNING VOLTAGE MEASUREMENTS

Fig.4 shows the measured voltage over the gap (the other side of the 50Ω resistor than was measured previously) and the current measured with the CT during the initiation of a breakdown. The voltage measurement was made with a Tektronix P6015A voltage probe with a 75MHz bandwidth and the current measurements with a Bergoz CT-B0.5 current transformer with a bandwidth of 200MHz. Both signals were read into a LeCroy wavepro 7100 digital oscilloscope with a bandwidth of 1GHz. Using these bandwidths and the well known formula $BW * T_r = 0.35$ [9] the smallest voltage and current rise times measurable were estimated to be 5ns and 2ns respectively.

To facilitate clear comparisons between this measurement and others, an error function style fit (eqn.1) was performed on both signals and we calculate the 10% to 90% rise (or fall times)[8].

$$V \propto (1 - erf(\alpha\tau)) \quad (1)$$

$$I \propto (1 + erf(\alpha\tau)) \quad (2)$$

We measure 13.5ns and 12ns for the rise/fall times of the voltage and current respectively. Whilst this measured rise times do not appear to be limited by the bandwidth of the measurement techniques, it is not clear if they are measures of the intrinsic turn on time of the breakdown or, as hypothesised in the introduction, are governed by the bandwidth of the system; which Fourier transforms of the oscillations after the transient suggest is ~ 0.1 GHz.

The CLIC accelerating structures have a similar bandwidth and the falling edges of transmitted RF signals are also similar typically a few 10s of ns. The previous CERN DC Spark system also had a much lower bandwidth and the measured current rise time there was larger still at 150ns[6]. Whilst a confident quantitative prediction of the rise time is still just beyond the scope of current particle in cell breakdown simulation efforts, rise times in the order of a few ns often appear, even shorter than this latest measurement[7].

With this measurement we have clearly shown that the intrinsic turn on time of a breakdown, if there is such a thing, is less than 15ns, we would have been easily able to measure it with our system if it had been longer. In the future we will reduce stray impedances in order to increase the bandwidth and see if the turn on time is further reduced. Dedicated measurements and analysis of the falling edge of transmitted power due to breakdown in RF structures is keenly awaited so the results may be compared. Comparison of the results together with the bandwidth of the systems will be able to show whether the turn on time in RF structures is limited by the bandwidth of the structures,

which could help explain how such low breakdown rates have been achieved in low group velocity structures.

After the breakdown transit has occurred the gap will develop a constant voltage, known as the burning voltage, which is normally around 20V for clean copper[5]. By averaging the measured voltage from 250ns to 400ns we obtain a burning voltage of 11V, quite consistent to the value above. In the future efforts will be made to reduce the ringing as well as improve the dynamic range so more accurate measurements can be made.

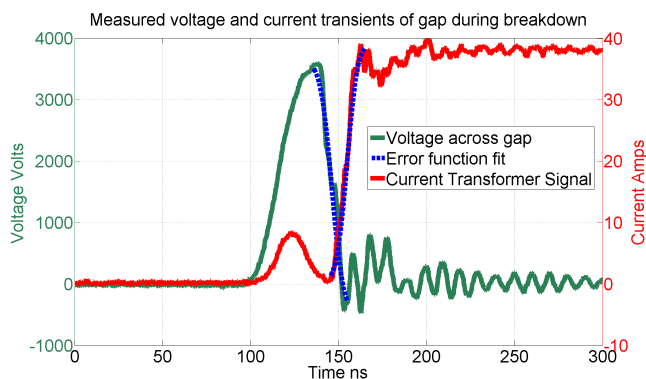


Figure 4: Rise time and current voltage measurements across gap.

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